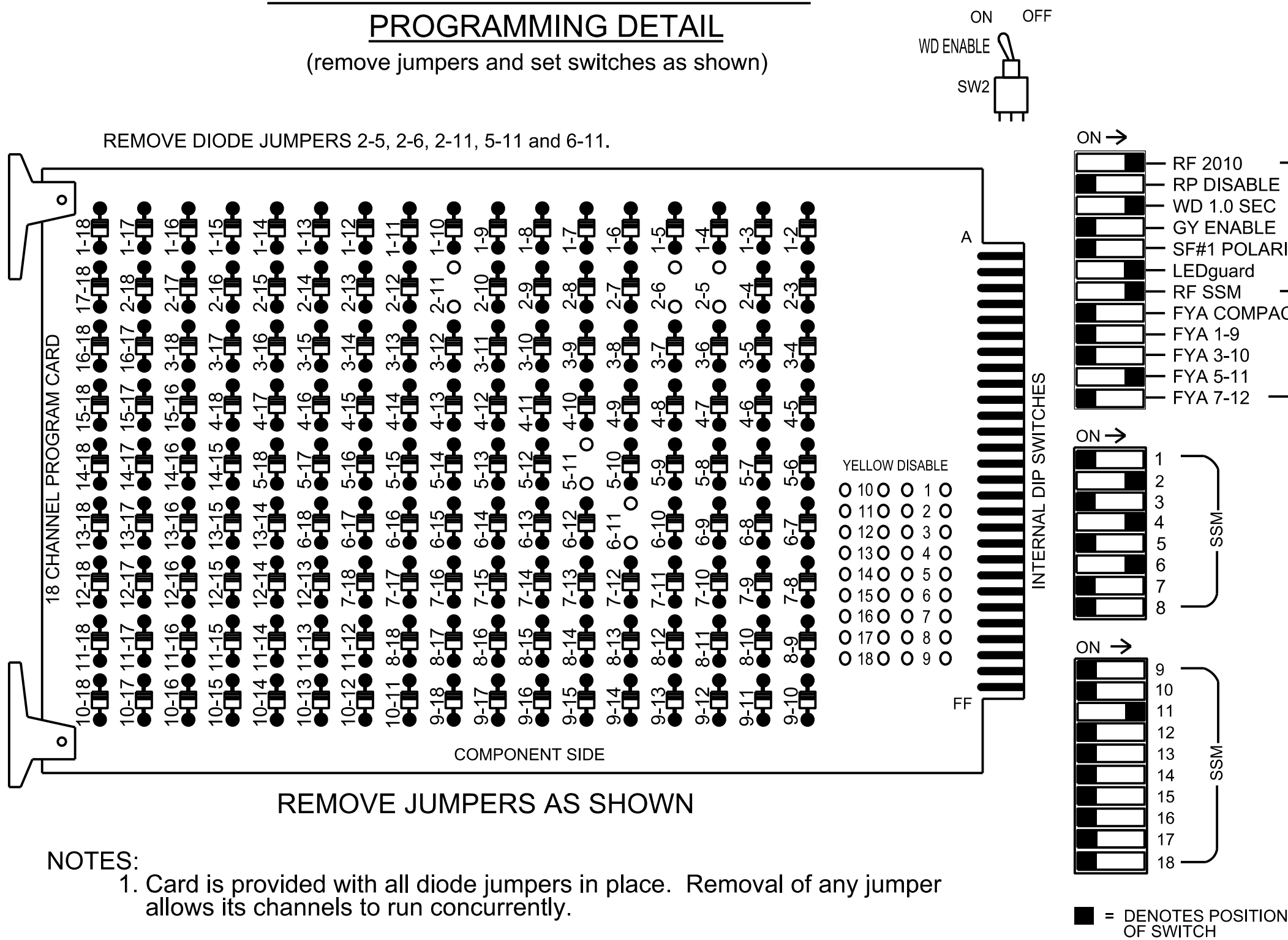


18 CHANNEL IP CONFLICT MONITOR
PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

Controller.....2070LX
Cabinet.....332 w/ Aux
Software.....Q-Free MAXTIME
Cabinet Mount.....Base
Output File Positions.....18 With Aux. Output File
Load Switches Used.....S2, S5, S7, S8, AUX S4
Phases Used.....2, 4, 5, 6
Overlap "1".....NOT USED
Overlap "2".....NOT USED
Overlap "3".....*
Overlap "4".....NOT USED

*See overlap programming detail on this sheet

PROJECT REFERENCE NO.	SHEET NO.
U-5711	Sig.2.1

SIGNAL HEAD HOOK-UP CHART

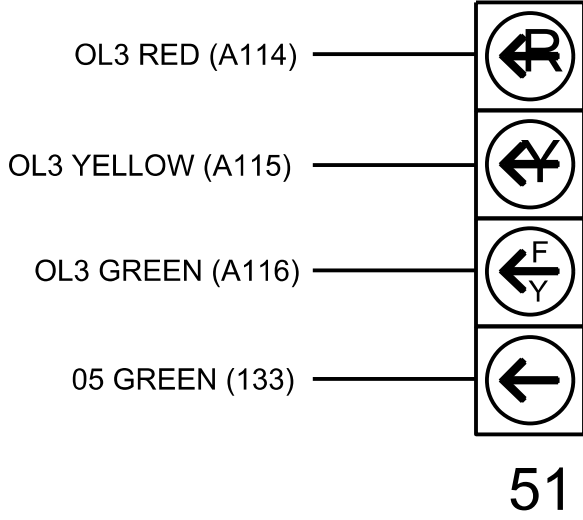
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	NU	21,22	NU	NU	41	42	NU	51★	61,62	NU	NU	NU	NU	NU	NU	51★	NU	NU
RED		128			101	101			134									
YELLOW		129					*		135									
GREEN		130							136									
RED ARROW																	A114	
YELLOW ARROW					102	102											A115	
FLASHING YELLOW ARROW																	A116	
GREEN ARROW					103	103		133										

NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.
*See pictorial of head wiring in detail this sheet.

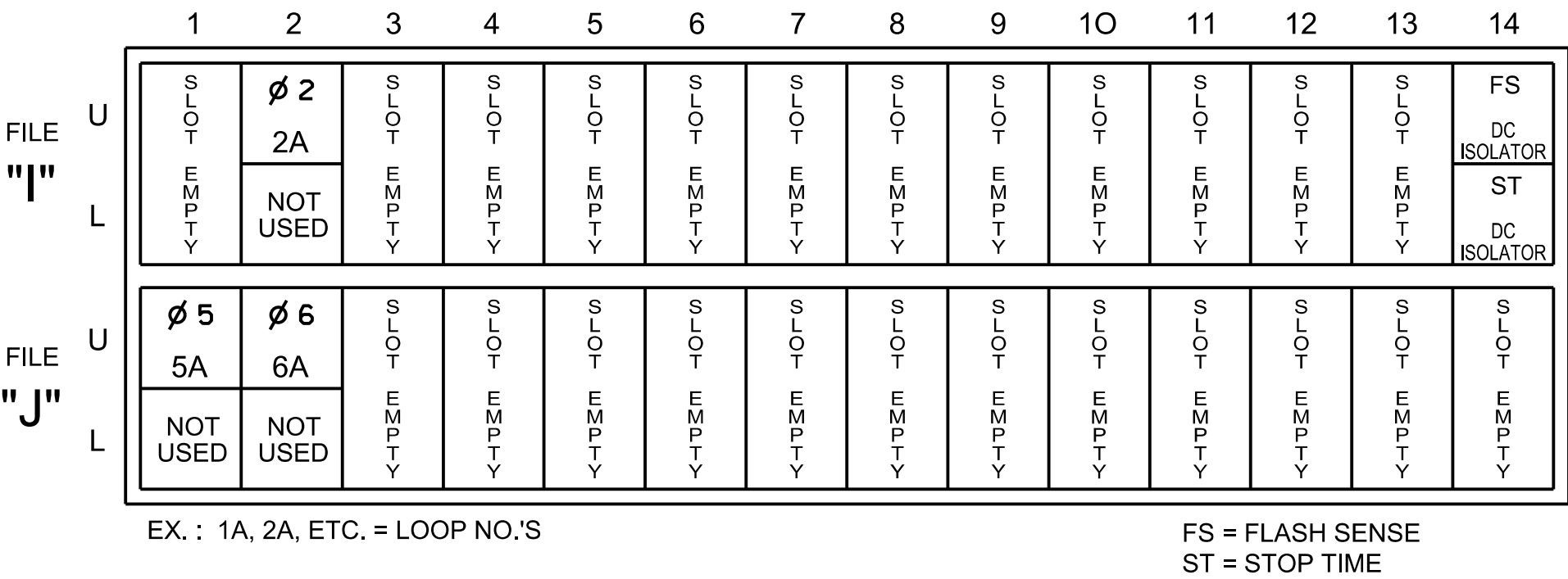
FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



INPUT FILE POSITION LAYOUT

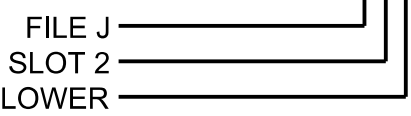
(front view)



INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
2A	TB2-5,6	I2U	39	1	2	2				X	X	
5A	TB3-1,2	J1U	55	17	15	5	15.0			X	X	
				.	31	2	3.0			X	X	X
6A	TB3-5,6	J2U	40	2	16	6				X	X	

INPUT FILE POSITION LEGEND: J2L



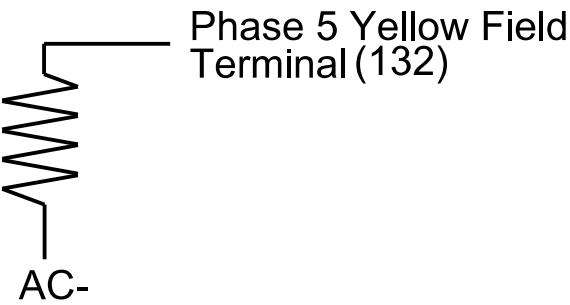
SPECIAL DETECTOR NOTE

Install a video detection system for zone 4A
Perform installation according to manufacturer's recommendations and NCDOT engineer-approved mounting location(s) to accomplish the detection schemes shown on the Signal Design Plans.

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

ACCEPTABLE VALUES	
Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



OVERLAP PROGRAMMING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	4
Type	Off	Off	FYA 4 - Section	Off
Included Phases	-	-	6	-
Modifier Phases	-	-	5	-
Modifier Overlaps	-	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0:0	0:0	0:0	0:0
Trail Red	0:0	0:0	0:0	0:0

M M

MOTT MACDONALD

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Electrical Detail - Sheet 1 of 2 - Temporary Design

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared for the Offices of:		US 220 Bus (N Fayetteville St) at SR 1712 (Pineview Street)	
Division 8 Randolph County Asheboro		PLAN DATE: November 2025 REVIEWED BY: R Mullinax	
PREPARED BY: LD Stouchko		REVIEWED BY:	
REVISIONS		INIT.	DATE
24-Nov-2025		24-Nov-2025	
SIG. INVENTORY NO.		08-0580T	