

### MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel  
Main Menu >Controller >Unit

Web Interface  
Home >Controller >Unit

Modify parameters as shown below and save changes.

Start Up Parameters	Unit Flash Parameters
StartUp Clearance Hold 6	All Red Flash Exit Time 6

### FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

### OUTPUT CHANNEL CONFIGURATION

Front Panel  
Main Menu >Controller >More>Channels>Channels Config

Web Interface  
Home >Controller >Advanced IO>Channels>Channels Configuration

#### Channel Configuration

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Phase Vehicle	1		X	X	1
2	Phase Vehicle	2		X		2
3	Phase Vehicle	3		X	X	3
4	Phase Vehicle	4		X		4
5	Phase Vehicle	5		X		5
6	Phase Vehicle	6		X	X	6
7	Phase Vehicle	7		X		7
8	Phase Vehicle	8		X	X	8
9	Overlap	1		X	X	9
10	Overlap	2		X	X	10
11	Overlap	3		X		11
12	Overlap	4		X		12
13	Phase Ped	2				13
14	Phase Ped	4				14
15	Phase Ped	6				15
16	Phase Ped	8				16
17	Overlap	5		X	X	17
18	Overlap	6		X		18

### MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

#### ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phase for head 11 to run protected turns only.

VEH DET PLAN 2: Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 0 seconds.

THIS ELECTRICAL DETAIL IS FOR  
THE SIGNAL DESIGN: 04-1470  
DESIGNED: April 2025  
SEALED: 04/24/2025  
REVISED: N/A



Electrical Detail - Sheet 3 of 3 - Final Design

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared in the Offices of:

750 N. Greenfield Pkwy, Garner, NC 27529

SR 1700 (Covered Bridge Road)  
at  
SR 1705 (Castleberry Road)

Divison 4	Johnston County	Archer Lodge
PLAN DATE: April 2025	REVIEWED BY: M.L. Stygles	
PREPARED BY: L. Gottlieb	REVIEWED BY: J. Ma	
REVISIONS	INIT.	DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

Signed by: *Matthew L. Stygles* 4/24/2025  
DATE  
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SIG. INVENTORY NO. 04-1470