MAXTIME OVERLAP PROGRAMMING DETAIL

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	3
Type	FYA 4 - Section
Included Phases	2,8
Modifier Phases	-
Modifier Overlaps	-
Trail Green	0
Trail Yellow	0.0
Trail Red	0.0

MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel
Main Menu >Controller >Unit

Web Interface
Home >Controller >Unit

Modify parameters as shown below and save changes.

Start Up Parameters

StartUp Clearance Hold
6

Unit Flash Parameters

All Red Flash Exit Time
6

OUTPUT CHANNEL CONFIGURATION

Front Panel
Main Menu >Controller >More>Channels>Channels Config

Web Interface
Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Phase Vehicle	1		X	X	1
2	Phase Vehicle	2		X		2
3	Phase Vehicle	3		X	X	3
4	Phase Vehicle	4		X		4
5	Phase Vehicle	5		X		5
6	Phase Vehicle	6		X	X	6
7	Phase Vehicle	7		X		7
8	Phase Vehicle	8		X	X	8
9	Overlap	1		X	X	9
10	Overlap	2		X	X	10
11	Overlap	3		X		11
12	Overlap	4		X		12
13	Phase Ped	2				13
14	Phase Ped	4				14
15	Phase Ped	6				15
16	Phase Ped	8				16
17	Overlap	5		X	X	17
18	Overlap	6		X		18

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 04-1470T2
DESIGNED: February 2025
SEALED: 02/04/25
REVISED: N/A

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DOCUMENT NOT CONSIDERED
FINAL UNLESS ALL
SIGNATURES COMPLETED

Electrical Detail - Sheet 2 of 2 - Temporary Design 2

ELECTRICAL AND PROGRAMMING
DETAILS FOR:

Prepared In the Offices of:

SR 1700 (Covered Bridge Road)
at
SR 1705 (Castleberry Road)

Divison 4	Johnston County	Archer Lodge
PLAN DATE: February 2025	REVIEWED BY: M.L. Stygles	
PREPARED BY: L. Gottlieb	REVIEWED BY: J. Ma/J.L. Lewis	

REVISIONS | INIT. | DATE

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PROFESSIONAL
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046057
ENGINEER
MATTHEW L. STYGLES

DocuSigned by:
2/4/2025
DATE

SIG. INVENTORY NO. 04-1470T2