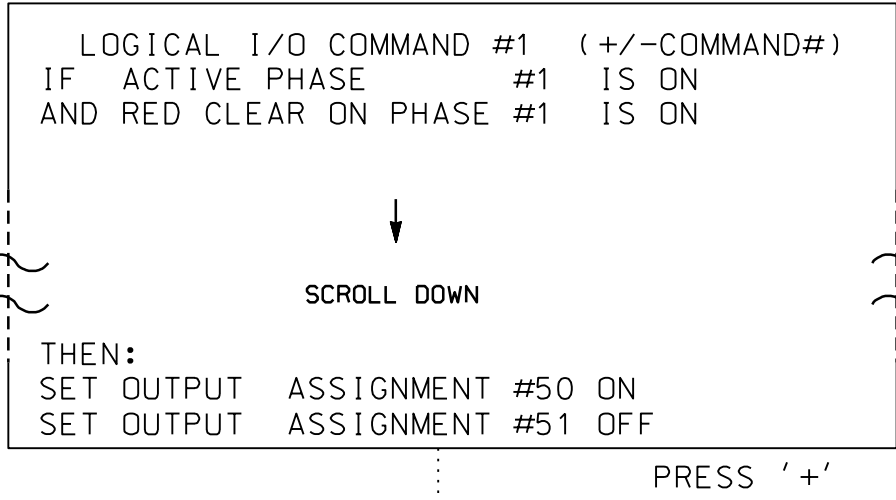


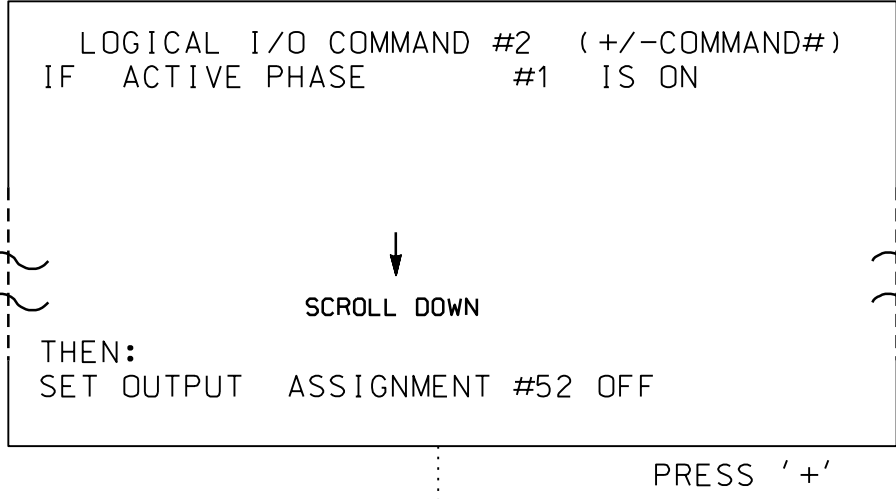
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL FOR FYA-PPLT SEQUENCE AND LEADING PEDESTRIAN INTERVAL EXCEPTION

(program controller as shown below)

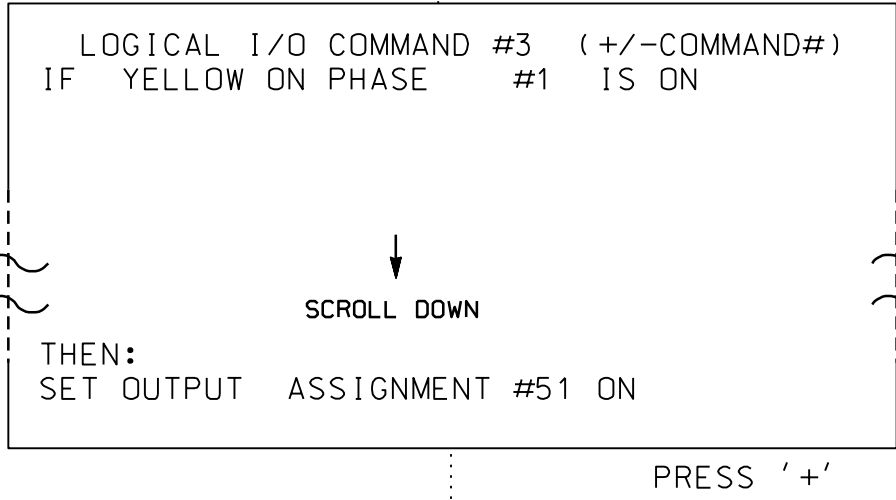
- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12 AND 13.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



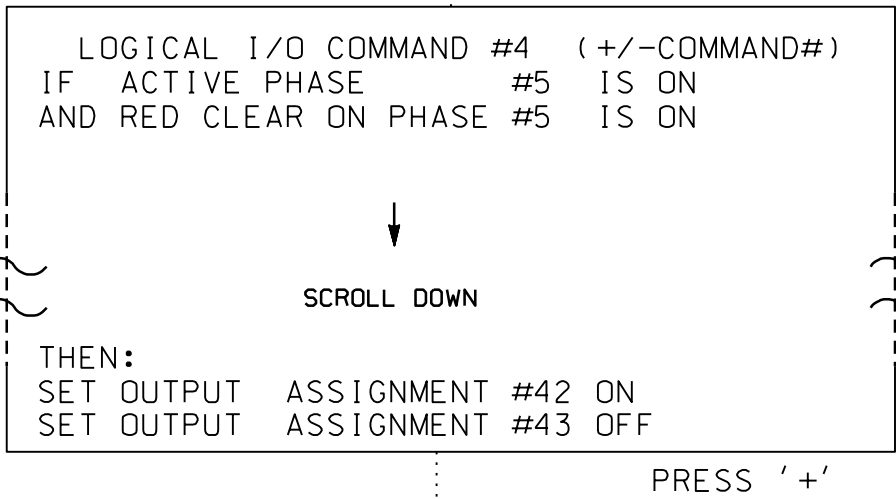
NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).



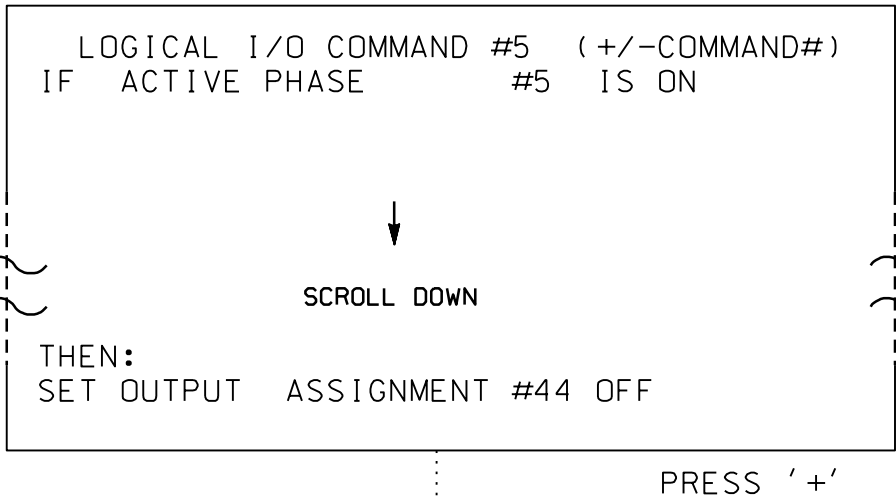
NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW OFF DURING PHASE 1 (HEAD 11).



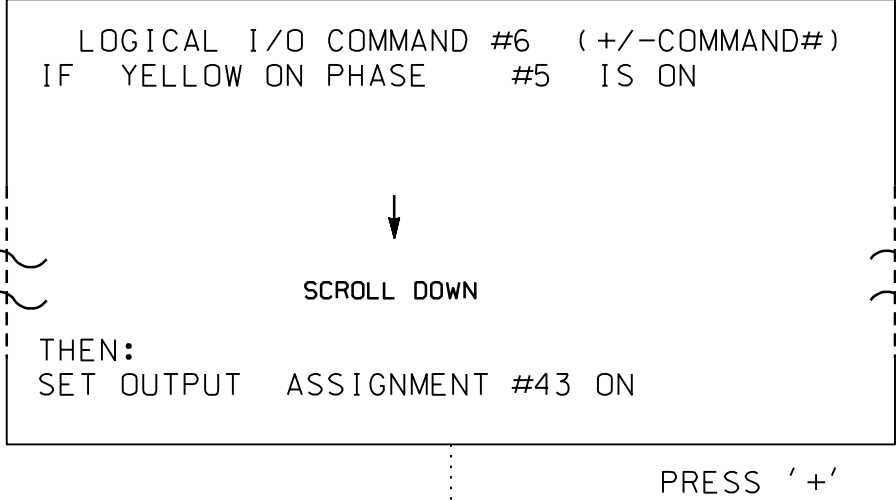
NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).



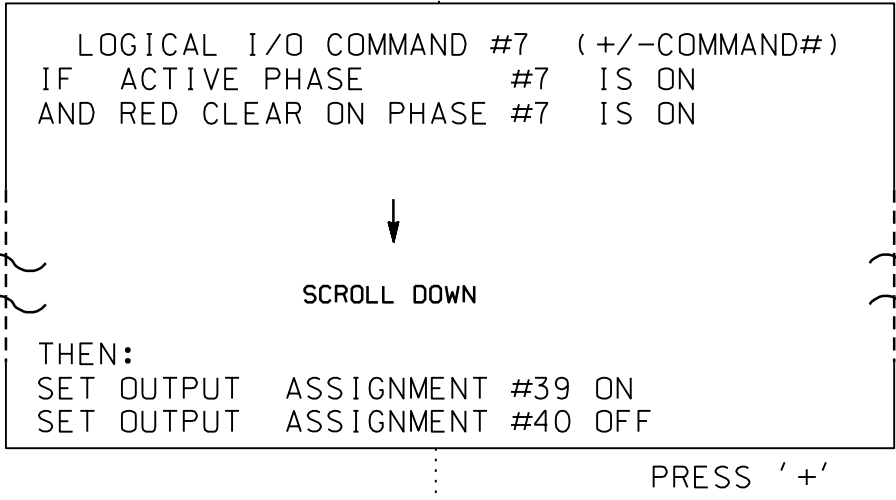
NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).



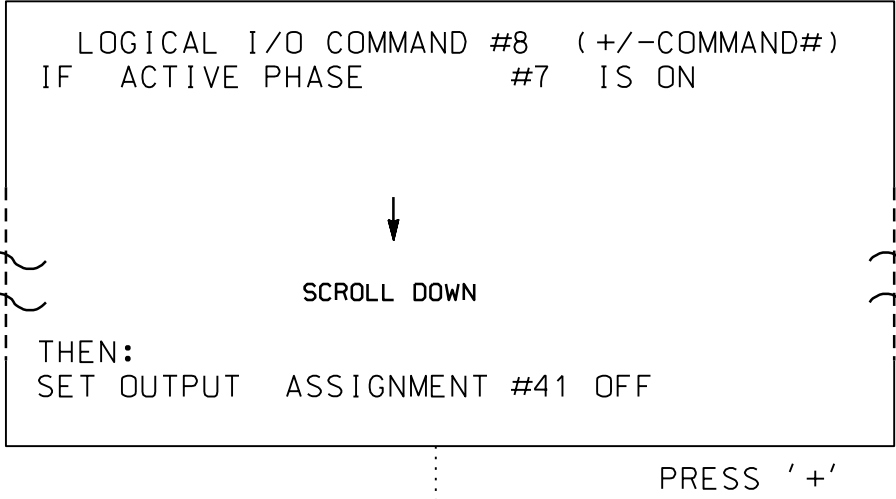
NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW OFF DURING PHASE 5 (HEAD 51).



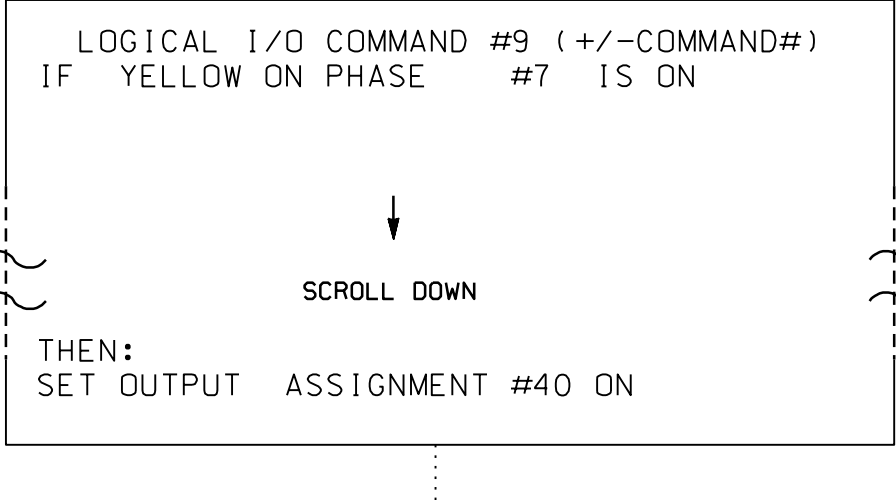
NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).



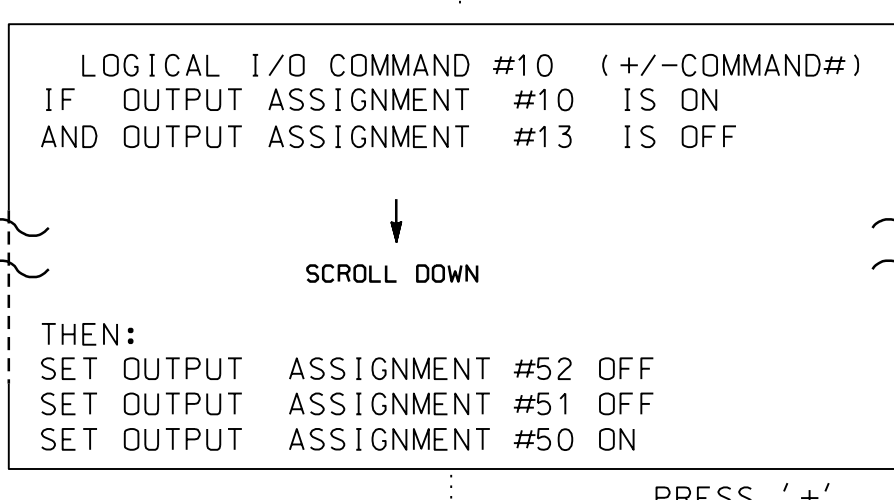
NOTE: LOGIC FOR PHASE 7 RED CLEAR WHEN TRANSITIONING FROM PHASE 7 TO PHASE 8 (HEAD 71).



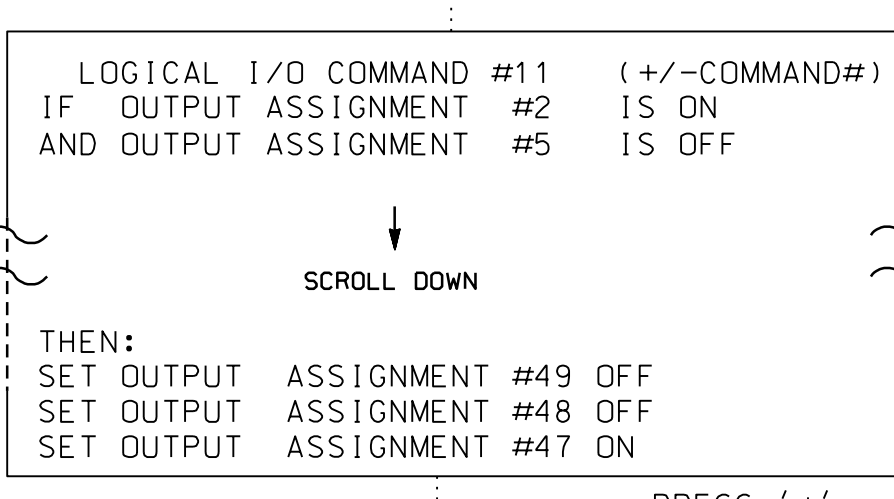
NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW OFF DURING PHASE 7 (HEAD 71).



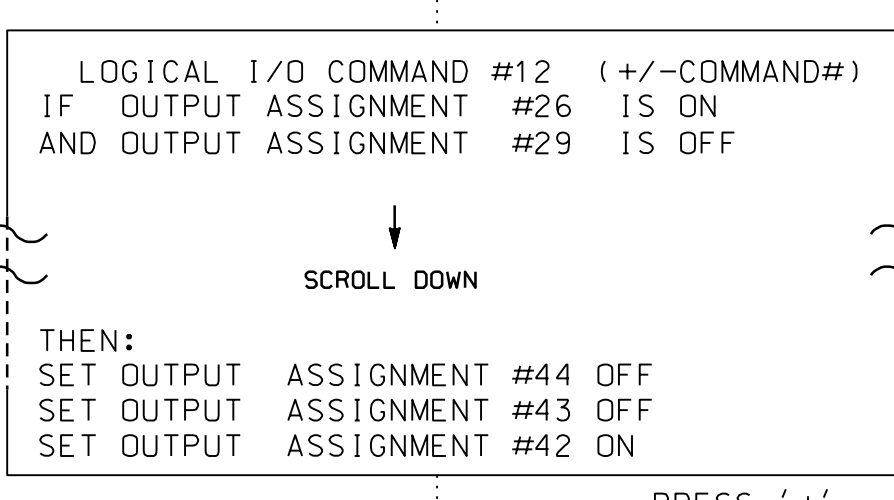
NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 7 (HEAD 71).



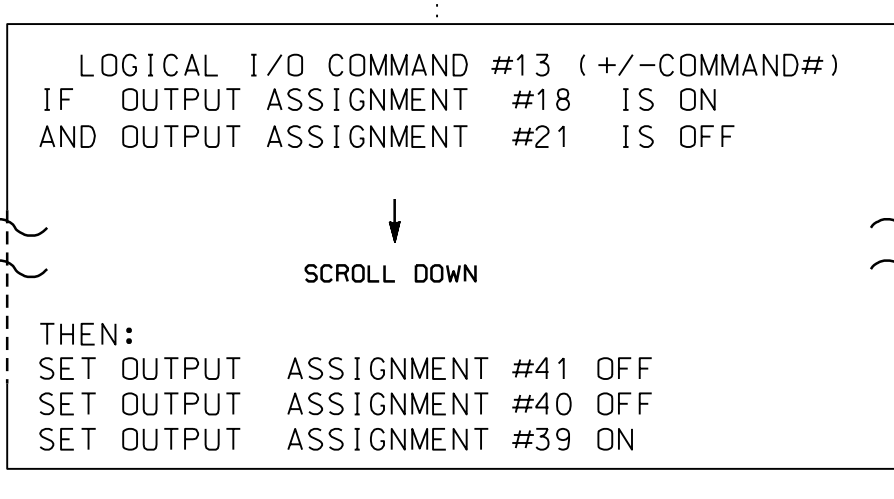
NOTE: LOGIC TO HOLD HEAD 11 FYA "OFF" DURING PED 2 LPI.



NOTE: LOGIC TO HOLD HEAD 81 FYA "OFF" DURING PED 4 LPI.



NOTE: LOGIC TO HOLD HEAD 51 FYA "OFF" DURING PED 6 LPI.



NOTE: LOGIC TO HOLD HEAD 71 FYA "OFF" DURING PED 8 LPI.

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

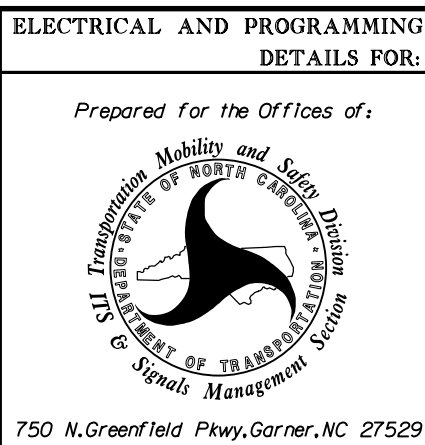
USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 2 = 4 PED Walk
OUTPUT 5 = Vehicle 4 Green
OUTPUT 10 = 2 PED Walk
OUTPUT 13 = Vehicle 2 Green
OUTPUT 18 = 8 PED Walk
OUTPUT 21 = Vehicle 8 Green
OUTPUT 26 = 6 PED Walk
OUTPUT 29 = Vehicle 6 Green
OUTPUT 39 = Overlap D Red
OUTPUT 40 = Overlap D Yellow
OUTPUT 41 = Overlap D Green
OUTPUT 42 = Overlap C Red
OUTPUT 43 = Overlap C Yellow
OUTPUT 44 = Overlap C Green
OUTPUT 47 = Overlap B Red
OUTPUT 48 = Overlap B Yellow
OUTPUT 49 = Overlap B Green
OUTPUT 50 = Overlap A Red
OUTPUT 51 = Overlap A Yellow
OUTPUT 52 = Overlap A Green

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 04-0234
DESIGNED: February 2025
SEALED: 3/27/2025
REVISED:

Electrical Detail - Sheet 3 of 3

DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED



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at
US 70/ US 117 Business (George St)

Division 4 Wayne County Goldsboro

PLAN DATE: February 2025 REVIEWED BY: J O Deaton

PREPARED BY: M W Yalch REVIEWED BY:

REVISIONS INIT. DATE

SIGNATURE DATE



DocuSigned by:
James O. Deaton 3/27/2025

SIGNATURE DATE

SIG. INVENTORY NO. 04-0234