

(program controller as shown below)

- LOGIC FOR PHASE 7 RED CLEAR WHEN TRANSITIONING FROM PHASE 7 TO PHASE 8 (HEAD 71).**

LOGICAL I/O COMMAND #1 (+/-COMMAND#)
 IF ACTIVE PHASE #7 IS ON
 AND RED CLEAR ON PHASE #7 IS ON

↓

SCROLL DOWN

THEN:
 SET OUTPUT ASSIGNMENT #39 ON
 SET OUTPUT ASSIGNMENT #40 OFF

PRESS '+'

LOGIC FOR SWITCHING FLASHING YELLOW ARROW OFF DURING PHASE 7 (HEAD 71).

LOGICAL I/O COMMAND #2 (+/-COMMAND#)
 IF ACTIVE PHASE #7 IS ON

↓

SCROLL DOWN

THEN:
 SET OUTPUT ASSIGNMENT #41 OFF

PRESS '+'

LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 7 (HEAD 71).

LOGICAL I/O COMMAND #3 (+/-COMMAND#)
 IF YELLOW ON PHASE #7 IS ON

↓

SCROLL DOWN

THEN:
 SET OUTPUT ASSIGNMENT #40 ON

LOGIC FOR ADVANCE WALK WITH FYA's. TURN FYA HEAD 81 OFF DURING PED 4 ADVANCE WALK.

LOGICAL I/O COMMAND #4 (+/-COMMAND#)
 IF OUTPUT ASSIGNMENT #2 IS ON
 AND OUTPUT ASSIGNMENT #5 IS OFF

↓

SCROLL DOWN

THEN:
 SET OUTPUT ASSIGNMENT #49 OFF
 SET OUTPUT ASSIGNMENT #48 OFF
 SET OUTPUT ASSIGNMENT #47 ON

PRESS '+'

LOGIC FOR ADVANCE WALK WITH FYA's. TURN FYA HEAD 71 OFF DURING PED 8 ADVANCE WALK.

LOGICAL I/O COMMAND #5 (+/-COMMAND#)
 IF OUTPUT ASSIGNMENT #18 IS ON
 AND OUTPUT ASSIGNMENT #21 IS OFF

↓

SCROLL DOWN

THEN:
 SET OUTPUT ASSIGNMENT #41 OFF
 SET OUTPUT ASSIGNMENT #40 OFF
 SET OUTPUT ASSIGNMENT #39 ON

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE
 USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 2 = 4 PED Walk
 OUTPUT 5 = Vehicle 4 Green

From Main Menu press '2' (Phase Control), then '1' (Phase Control Functions). Program phases 2, 4, 6 and 8 for 'Advanced Walk'. Make sure the Advance Walk times shown on the Signal Design plan are programmed in the 'Phase Timing' menu.

From MAIN MENU press '8' (OVERLAPS), then '1' (VEHICLE OVERLAP SETTINGS). Press '+' once.

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PAGE 1: VEHICLE OVERLAP 'B' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: X
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
FLASH COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-255 SEC).....0
YELLOW CLEAR (0=PARENT, 3=25.5 SEC)...0.0
RED CLEAR (0=PARENT, 1=25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0

```

PRESS '+' TWICE

NOTICE
GREEN
FLASH

```

PAGE 1: VEHICLE OVERLAP 'D' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: _ RED _ YELLOW _ GREEN
FLASH COLORS: _ RED _ YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?..N
GREEN EXTENSION (0-255 SEC).....0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)....0
RED CLEAR (0=PARENT,0.1-25.5 SEC)....0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0

```

NOTICE
GREEN
FLASH

OVERLAP PROGRAMMING COMPLETE

1. From Main Menu press '2' (Phase Control), then '1' (Phase Control Functions). Scroll to the bottom of the menu and enable Dynamic/Backup Control Function 1.
2. From Phase Control Functions Menu press '2' (Dynamic/Backup Control Functions).

```

DYNAMIC/BACKUP CONTROL FUNCTION #01
                                OVERLAPS: ABCDEFGHIJKLMNOP
IF OVERLAPS ARE ACTIVE :
OR PHASES: 12345678910111213141516
IF PHASES ARE ON : X
OMIT PHASES : X
CALL PHASES :

```

BACKUP PROTECTION PROGRAMMING COMPLETE

NOTE: Remove the Dynamic Omit Control Programming above.

In order to ensure that signals flash concurrently on the same approach, make the following flasher circuit changes:

1. On rear of PDA - Remove wire from Term. T2-4 and terminate on T2-2.
2. On rear of PDA - Remove wire from Term. T2-5 and terminate on T2-3.
3. Remove Flasher Unit 2.

The changes listed above tie all phases and overlaps to Flasher Unit 1.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 04-0233
DESIGNED: February 2025
SEALED: 3/27/2025
REVISED:

Final Design
Electrical Detail - Sheet 2 of 2

**DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETE**

**ELECTRICAL AND PROGRAMMING
DETAILS FOR:**

750 N.Greenfield Pkwy, Garner, NC 2752

US 70 Business (George Street)
at
US 70 Business (Grantham Street)

Division 4 Wayne County Goldsboro

PLAN DATE: February 2025	REVIEWED BY: J O Deaton
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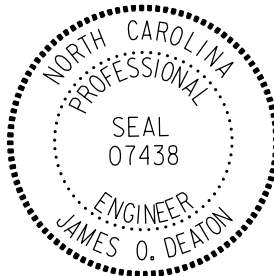
PREPARED BY: M W Yalch	REVIEWED BY:
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REVISIONS	INIT.	
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SEAL



DocuSigned by:

James R.

SIGNA
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3/27/202

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