

OUTPUT CHANNEL CONFIGURATION

Front Panel
Main Menu >Controller >More >Channels >Channel Options

Web Interface
Home >Controller >Advanced IO >Channels >Channel Configuration

Channel Configuration

NOTICE OL7 ASSIGNED TO CHANNEL 1 ➡
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NOTE: ALL RED FLASH

MAXTIME OVERLAP PROGRAMMING DETAIL

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	7	8
Type	FYA 4 - Section	FYA 4 - Section	Normal	Normal
Included Phases	3	6	5	3
Modifier Phases	5	3	-	-
Modifier Overlaps	-	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel
Main Menu >Controller >Unit

Web Interface
Home > Controller > Unit

Modify parameters as shown below and save changes.

Start Up Parameters	Unit Flash Parameters
Startup Clearance Hold	All Red Flash Exit Time
6	6

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 06-0537T2
DESIGNED: Feb 2025
SEALED: 3/19/2025
REVISED:

Temporary Design 2 - TMP Phase II, Step 3
Electrical Detail - Sheet 2 of 2

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