Project No. Sheet No. HL-0008ISig. 1.0

STATE OF NORTH CAROLINA DIVISION OF HIGHWAYS

WAKE COUNTY

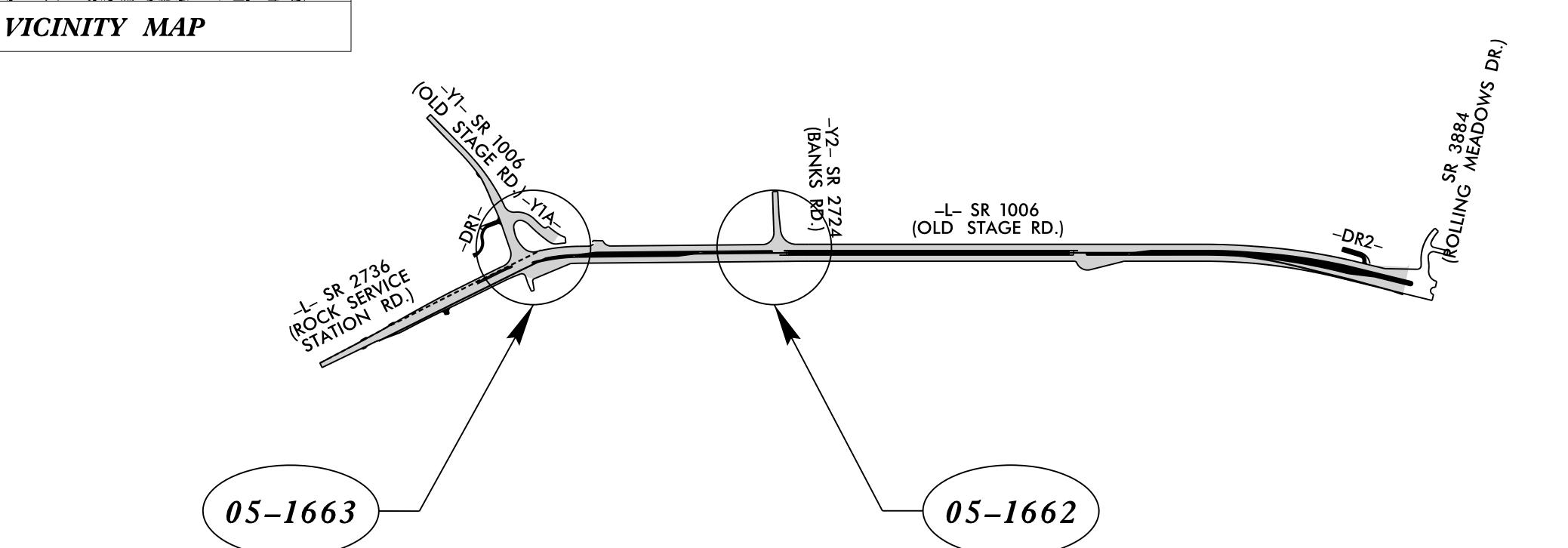
LOCATION: SR 1006 (OLD STAGE ROAD) FROM

SR 2736 (ROCK SERVICE STATION ROAD) TO

SR 3884 (ROLLING MEADOWS DRIVE)

TYPE OF WORK: SIGNALS AND SIGNAL COMMUNICATIONS





Sheet #

MIA–M9

SCP 1 - 8

 ∞

18000

Index of Plans

Location/Description

Sig. 1.0 Sig. 2.0 - 4.3 Sig. 5.0 - 5.4 N/A 05-1663 05-1662

Reference #

Title Sheet SR 1006 (Old-Stage Road) at SR 2736 (Rock Service Station Road) And Back Field Road SR 1006 (Old-Stage Road) at SR 2724 (Banks Road) Standard Metal Pole Sheets

Signal Communication Plans

TRANSPORTATION SYSTEMS MANAGEMENT & OPERATIONS UNIT

Contacts:

Robert J. Ziemba, PE, CPM - Central Region Signals Engineer Ryan W. Hough, PE - Signal Equipment Design Engineer Gregory A. Green - Signal Communication Project Engineer

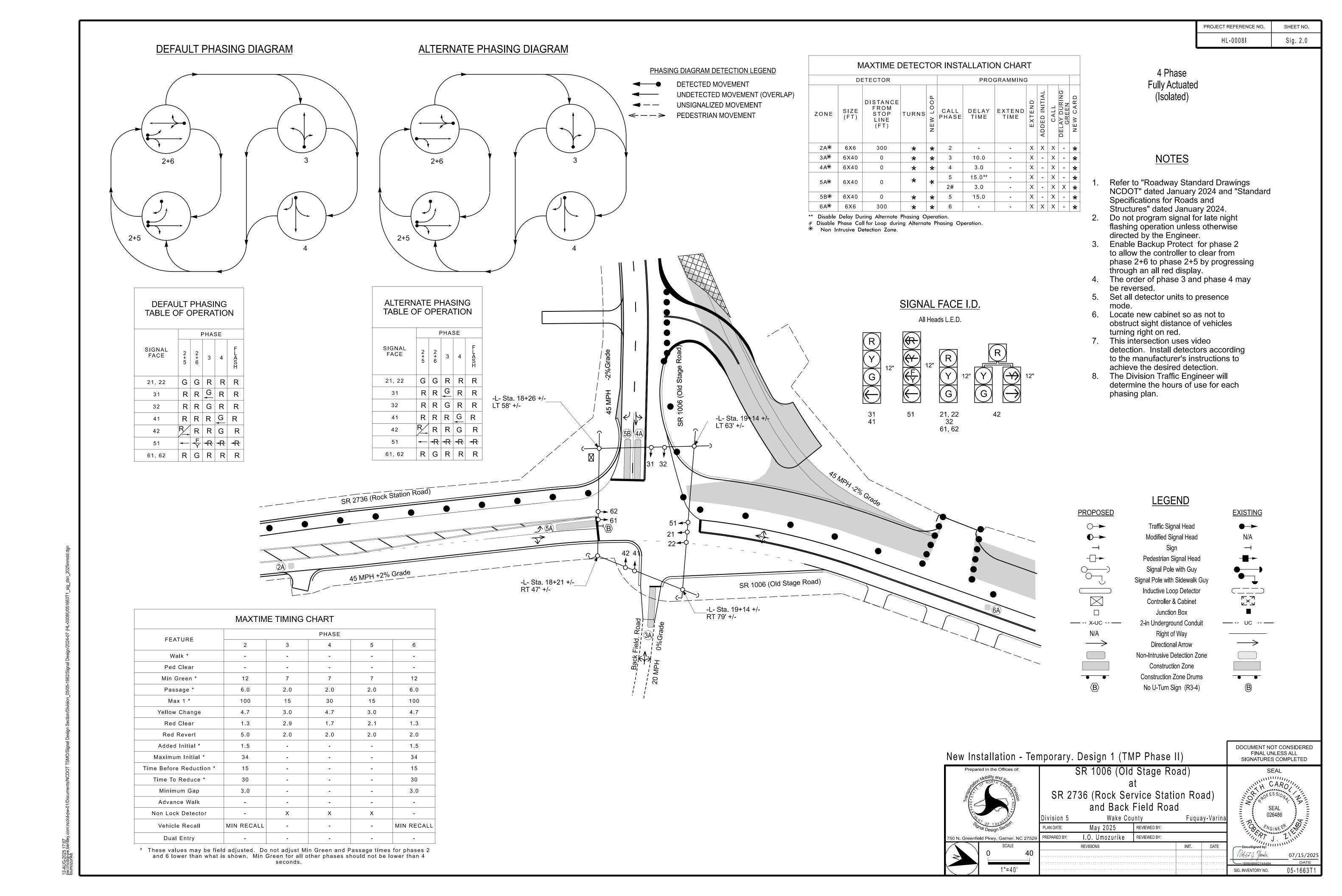
Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January 2024.

DIVISION OF HIGHWAYS TRANSPORTATION MOBILITY & SAFETY DIVISION

Prepared in the Office of:



750 N. Greenfield Parkway, Garner, NC 27529



NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- 2. Program controller to start up in phase 2 Green No Walk and 6 Green
- 3. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

Phases Used Overlap "1" Overlap "2" Overlap "3"	332 w/ AuxQ-Free MAXTIMEBase18 With Aux. Output FileS2, S5, S7, S8, S11, AUX S42, **3, 4, 5, 6Not UsedNot Used*
Overlap "4" Overlap "8"	Not Used

*See overlap programming detail on sheet 2

** Used for timing purposes only

= DENOTES POSITION OF SWITCH

ST = STOP TIME

ROJECT REFERENCE NO. HL-00081 Sig. 2.1

				SIC	SNA	AL H	ΙEΑ	DΗ	00	K-U	PC	HA	RT								
LOAD SWITCH NO.	S1	S2	S3	S4	S	5	S6	S	57	S8	S9	S10	S	11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	۷	1	14	į	5	6	15	7	8	3	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	1	4 PED	į	5	6	6 PED	7	Ol	_8	8 PED	OL1	OL2	SPARE			SPARE
SIGNAL HEAD NO.	NU	21.22	UИ	NC	41	42	NU	42	★ 51	61,62	NU	NU	31	32	NU	NU	NU	NU	★ 51	NU	NU
RED		128			101	101			*	134			107	107							
YELLOW		129			102	102				135			108	108							
GREEN		130			103	103				136			109	109							
RED ARROW																			A114		
YELLOW ARROW								132											A115		
FLASHING YELLOW ARROW																			A116		
GREEN ARROW					103			133	133				109								
\\																					
Ķ																					

NU = Not Used NC = No Connection

*Denotes install load resistor. See load resistor installation detail this sheet.

★See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.

2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.

3. Ensure that the Red Enable is active at all times during normal operation.

Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

(front view)

	1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE U	SLOT ESP	0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	SLOT EX	WLOH HYP	0.LOH	SLOT EM	⊘_ОТ ш ∑ Ф	010H HZD	SLOT EX	WLOH HZP	SLOT EXP	SLOT EX	SLOT EXP	FS DC ISOLATOR
· L	P T Y	P T Y	P T Y	P T Y	P T Y	P T Y	P T Y	P T Y	P T Y	P T Y	P T Y	P T Y	P T Y	DC ISOLATOR
FILE U	ø 5 5A	SLOT	SLOT	SLOT	SLOT	SLOT	SLOT	SLOT	SLOT	SLOT	SLOT	SLOT	SLOT	S LO T
"J" L	NOT USED	EMPTY	EMPTY	EMPTY	ШΣΡΤΥ	EMPTY	EMPTY	EMPTY	ΕΔΡΤΥ	EMPTY	EMPTY	EMPTY	EMPTY	E M P T Y
	EX.: 1A, 2A, ETC. = LOOP NO.'S FS = FLASH SENSE									 E				

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
5A	TB3-1,2	J1U	55	17	15 ★	5	15		Х		Х	
) JA	103-1,2	J 10	55	-	31★	2	3		Х		Х	Х

★ For the detectors to work as shown on the signal plan see the Detector Programming Detail for Alternate Phasing on Sheet 2 of this plan.

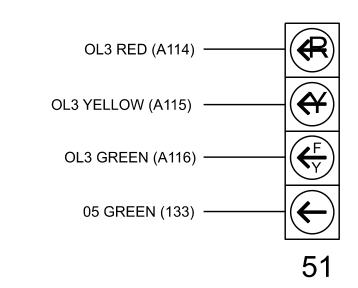
INPUT FILE POSITION LEGEND: J2L LOWER -

DETECTOR NOTES

- I. For all loops install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.
- 2. For loop 5A, detector card placement is typical for a NCDOT installation. Detectors associated with these slots are compatible with the Vehicle Detector Programming Detail located on sheet 2 of this electrical detail.

FYA SIGNAL WIRING DETAIL

(wire signal head as shown)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1663T1 DESIGNED: May 2025 SEALED: 7/15/2025 REVISED: N/A

| Electrical Detail - Sheet 1 of 3

Prepared in the Offices of:

Electrical and Programming Details For: SR 1006 (Old Stage Road) SR 2736 (Rock Service Station Road) and Back Field Road Wake County

Fuguay-Varina PLAN DATE: June 2025 REVIEWED BY:

PREPARED BY: Zarrar Zafar REVIEWED BY: REVISIONS

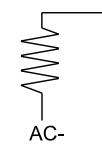
DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED 031001 D. told Joya 07/18/2025

SIG. INVENTORY NO. 05-1663T1

LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown)

ACCEPTABLE VALUES Value (ohms) Wattage 1.5K - 1.9K 25W (min) 2.0K - 3.0K | 10W (min)



Phase 5 Red Field Terminal (131)

MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

•			
Overlap	3	8	
Type	FYA 4 - Section	Normal	
Included Phases	-	3	NOTICE INCLUDED PHASE
Modifier Phases	5		
Modifier Overlaps	-	-	
Trail Green	0	0	
Trail Yellow	0.0	0.0	
Trail Red	0.0	0.0	
	_	•	

MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOP 5A

Front Panel

Main Menu >Controller >Detector >Veh Det Plans

Web Interface

Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

5A

Detector	Call Phase	Delay
15	5	0
31	0	_

MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

MAXTIME OVERLAP PROGRAMMING DETAIL

FOR DEFAULT PHASING

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Normal

0.0

0.0

Home >Controller >Overlap Configuration >Overlaps

FYA 4 - Section

0.0

Front Panel

Web Interface

Overlap Plan 1

Overlap

Type
Included Phases
Modifier Phases
Modifier Overlaps
Trail Green

Trail Yellow

Trail Red

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases

for head 51 to run protected turns

only.

VEH DET PLAN 2: Disables phase 2 call on loop 5A

and reduces delay time for phase 5 call on loop 5A to 0 seconds.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 05-1663T1
DESIGNED: May 2025
SEALED: 7/15/2025
REVISED: N/A

MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Coordination >Patterns

Web Interface

Home >Controller >Coordination >Patterns

Pattern Parameters

Pattern	Veh Det Plan	Overlap Plai
*	2	2

*The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

Electrical Detail - Sheet 2 of 3

Prepared in the Offices of:

NORTH CARREST OF TRANSPORTED TO MANAGEMENT OF

SR 1006 (Old Stage Road) at SR 2736 (Rock Service Station Road)

and Back Field Road

was a station Road

and Back Field Road

was County Fuguay-Varina

PLAN DATE: June 2025 REVIEWED BY:

PREPARED BY: Zarrar Zafar REVIEWED BY:

REVISIONS INIT. DA

Docusigned by:

07/18/2025

A90CADEDBD4241D

DATE

SIG. INVENTORY NO. 05-1663T1

031001

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zzafar

ALL RED BACKUP **PROGRAMMING**

Front Panel

Main Menu >Controller >Sequence & Phs Config>Backup Prevention > Backup Through Red

Web Interface

Home >Controller >Backup Prevention >Backup Calls Phase Plans > (scroll down) to Backup Through Red

Backup Through All Red

Sequence	Backup Through All Red
1	YES

BACKUP PREVENTION **PROGRAMMING**

Front Panel

Main Menu > Controller > Sequence & Phs Config > Backup Prevention > Backup Protection Plan

Web Interface

Home >Controller> Backup Prevention >Backup Protection Plan

Sequence 1

No Backup Phase	1	2	3	4	5	6	7	8
Serve Phase 1	ı	ı	ı	ı	ı	ı	ı	-
Serve Phase 2	ı	ı	ı	ı	ı	ı	ı	-
Serve Phase 3	ı	ı	ı	ı	ı	ı	ı	-
Serve Phase 4	ı	ı	ı	ı	ı	ı	ı	-
Serve Phase 5	ı	ı	ı	ı	ı	ı	ı	-
Serve Phase 6	-	1	1	1	X	1	1	-
Serve Phase 7	-			ı				-
Serve Phase 8	-	-	-	-	-	-	-	-

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1663T1

DESIGNED: May 2025 SEALED: 7/15/2025 REVISED: N/A

OUTPUT CHANNEL CONFIGURATION

Front Panel

Main Menu >Controller >More>Channels>Channels Config

Web Interface

NOTE CONTROL TYPE

AND CONTROL SOURCE

Home >Controller >Advanced IO>Channels>Channel Configuration

Channel Configuration

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Phase Vehicle	1		Х	Х	1
2	Phase Vehicle	2		Х		2
3	Phase Vehicle	3		Х	Х	3
4	Phase Vehicle	4		Х		4
5	Phase Vehicle	5		Х		5
6	Phase Vehicle	6		Х	Х	6
7	Phase Vehicle	7		Х		7
8	Overlap	8		Х	Х	8
9	Overlap	1		Х	Х	9
10	Overlap	2		Х	Х	10
11	Overlap	3		Х		11
12	Overlap	4		Х		12
13	Phase Ped	2				13
14	Phase Ped	4				14
15	Phase Ped	6				15
16	Phase Ped	8				16
17	Overlap	5		Х	Х	17
18	Overlap	6		Х		18

MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Unit

Web Interface

Home >Controller >Unit

Modify parameters as shown below and save changes.

Start Up Parameters

StartUp Clearance Hold

Unit Flash Parameters All Red Flash Exit Time

Electrical Detail - Sheet 3 of 3

Prepared in the Offices of:

SR 1006 (Old Stage Road) SR 2736 (Rock Service Station Road) and Back Field Road

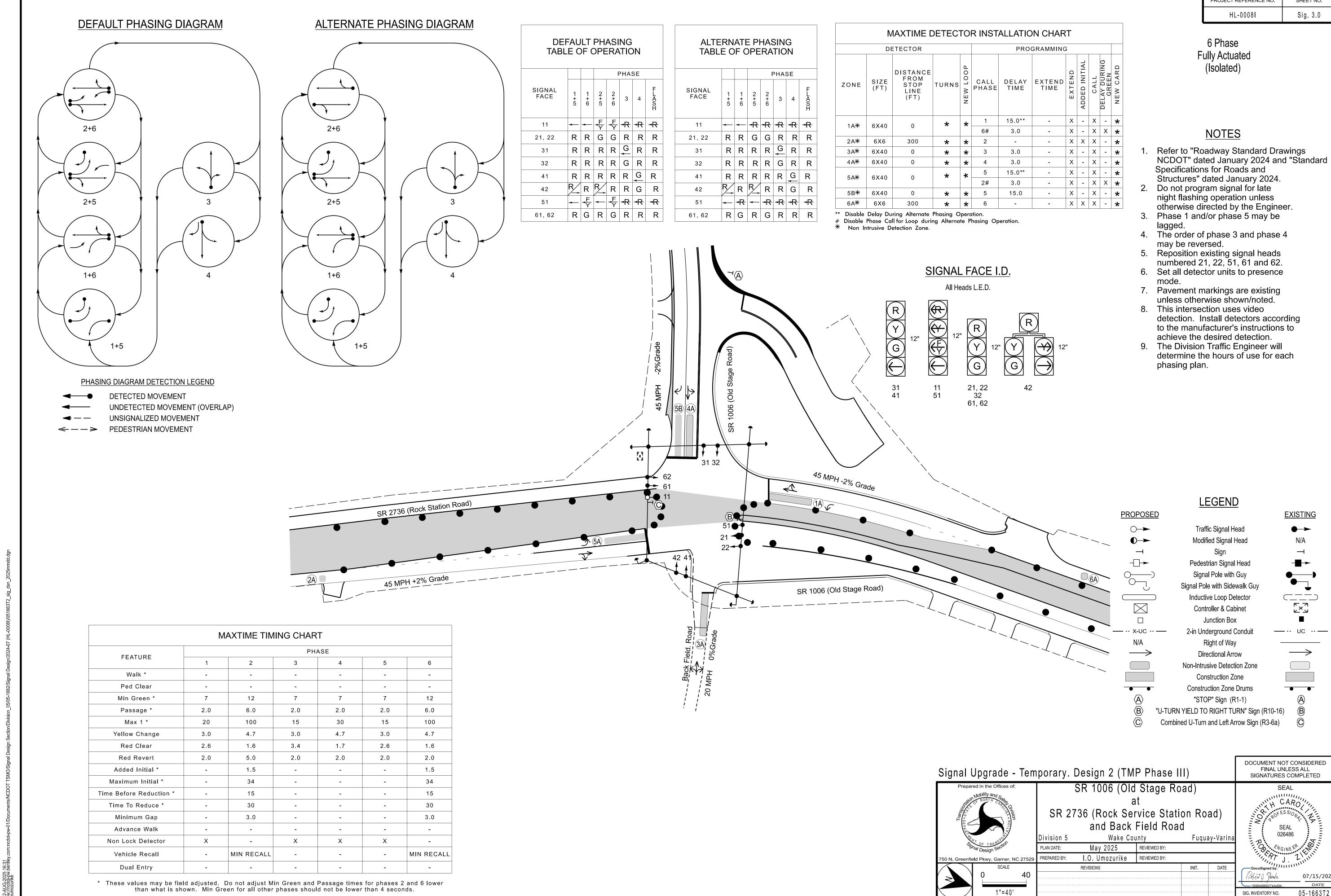
Fuquay-Varina June 2025 PLAN DATE: REVIEWED BY:

PREPARED BY: Zarrar Zafar REVIEWED BY: REVISIONS

D. Told Joya 07/18/2025 SIG. INVENTORY NO. 05-1663T1

031001

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED



PROJECT REFERENCE NO. SHEET NO.

NOTES

- heads flash in accordance with the signal plan.
- 2. Program controller to start up in phase 2 Green No Walk and 6 Green
- 3. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

Controller	2070LX
Cabinet	332 w/ Aux
Software	Q-Free MAXTIME
Cabinet Mount	Base
Output File Positions	18 With Aux. Output File
Load Switches Used	
	AUXS1, AUX S4
Phases Used	1, 2, **3, 4, 5, 6
Overlap "1"	*
Overlap "2"	
Overlap "3"	
Overlap "4"	
Overlap "8"	*

*See overlap programming detail on sheet 2 ** Used for timing purposes only

= DENOTES POSITION OF SWITCH

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all vehicle load switches in the output file. The installer shall verify that signal

S12 AUX AUX AUX AUX AUX AUX S5 S6 LOAD SWITCH NO S8 | S9 | S10 | S1 | S2 | S3 | S4 CMU CHANNEL NO. 10 17 12 8 OL1 OL2 SPARE OL3 OL4 SPARE PHASE 11 NU NU 51 NU NU 1 21.22 NU NC 41 42 NU 42 51 61,62 NU NU 31 32 NU HEAD NO. 101 101 ***** 134 107 | 107 RED 102 | 102 108 | 108 ***** | 129 | YELLOW 103 | 103 | 136 109 | 109 130 GREEN RED ARROW YELLOW 132 ARROW FLASHING A116 YELLOW ARROW GREEN 103 133 | 133 109 ARROW

SIGNAL HEAD HOOK-UP CHART

NU = Not Used NC = No Connection

*Denotes install load resistor. See load resistor installation detail this sheet. ★See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.

2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.

3. Ensure that the Red Enable is active at all times during normal operation.

4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

(front view)

	1	2	3	4	5	6	7	8	9	10	11	12	13	14
file U "I" L	Ø 1 1A NOT USED	SLOT EMPTY	010F EXP+>	010F m2r+>	010F EXP+>	010F EXP+>	010F EXPLY	010H HZ4+>	SLOT EMPTY	SLOT EMPTY	010H HZ4+>	SLOT EMPTY	SLOT EMPTY	FS DC ISOLATOR ST DC ISOLATOR
FILE U "J" L	Ø 5 5A NOT USED	SLOT EMPTY	SLOT EXPTY	SLOT EXPTY	SLOT EXPTY	SLOF EXPTY	SLOT EXPTY	SLOF EXPFY	SLOT EMPTY	SLOT EMPTY	SLOF EXPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY
	EX.: 14	A, 2A, E	TC. = L	00P NO) . ′S						FS =	FLASH	SENS	E

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
1A	TB2-1,2	I1U	56	18	1*	1	15		Х		Х	
L IA	162-1,2	110	36	•	29★	6	3		Х		Х	Χ
5A	TB3-1,2	J1U	55	17	15 ★	5	15		Χ		Χ	
5A	100-1,2	310	33	-	31★	2	3		Х		Х	Х

★ For the detectors to work as shown on the signal plan see the Detector Programming Detail for Alternate Phasing on Sheet 2 of this plan.

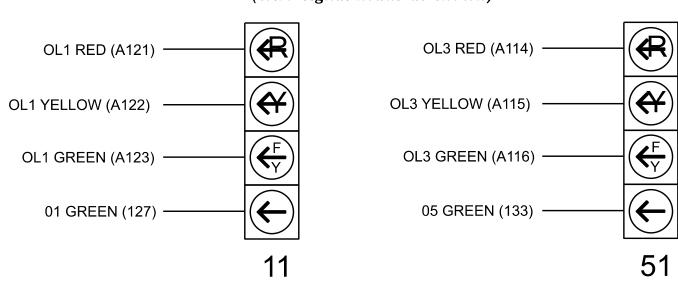
INPUT FILE POSITION LEGEND: J2L FILE J —— LOWER -

DETECTOR NOTES

- 1. For all loops install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.
- . For loops 1A and 5A, detector card placement is typical for a NCDOT installation. Detectors associated with these slots are compatible with the Vehicle Detector Programming Detail located on sheet 2 of this electrical detail.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1663T2 DESIGNED: May 2025 SEALED: 7/15/2025 REVISED: N/A

| Electrical Detail - Sheet 1 of 3

Prepared in the Offices of:

SR 1006 (Old Stage Road) SR 2736 (Rock Service Station Road) and Back Field Road Wake County

Fuguay-Varina PLAN DATE: June 2025 REVIEWED BY: PREPARED BY: Zarrar Zafar REVIEWED BY: REVISIONS

D. told Joya 07/24/2025 SIG. INVENTORY NO. 05-1663T2

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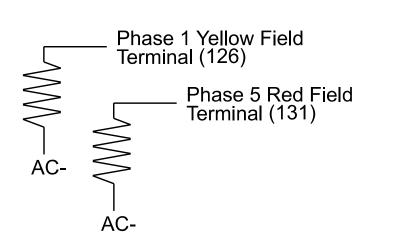
Sig. 3.1

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

ACCEPTABLE VALUES Value (ohms) Wattage 1.5K - 1.9K 25W (min) 2.0K - 3.0K | 10W (min)

NOTES:



ST = STOP TIME

MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	3	8
Type	FYA 4 - Section	FYA 4 - Section	Normal
Included Phases	2	6	3
Modifier Phases	1	5	-
Modifier Overlaps	ı	-	•
Trail Green	0	0	0
Trail Yellow	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0

MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	1	3	8	
Type	FYA 4 - Section	FYA 4 - Section	Normal	
Included Phases	-	-	3	NOTICE INCLUDED PHASE
Modifier Phases	1	5	-	
Modifier Overlaps	-	-	1	
Trail Green	0	0	0	
Trail Yellow	0.0	0.0	0.0	
Trail Red	0.0	0.0	0.0	

MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 1A & 5A

Front Panel

Main Menu >Controller >Detector >Veh Det Plans

Web Interface

Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

1A

Detector	Call Phase	Delay
1	1	0
29	0	-

	Detector	Call Phase	Delay
١	15	5	0
	31	0	_

MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases for heads 11 and 51 to run

protected turns only.

VEH DET PLAN 2: Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 0 seconds.

> Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Coordination >Patterns

Web Interface

Home >Controller >Coordination >Patterns

Pattern Parameters

Pattern	Veh Det Plan	Overlap Plan
*	2	2

*The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

| Electrical Detail - Sheet 2 of 3

Electrical and Programming Details For: Prepared in the Offices of:

SR 1006 (Old Stage Road) SR 2736 (Rock Service Station Road)

and Back Field Road Fuguay-Varina June 2025 PLAN DATE: REVIEWED BY:

PREPARED BY: Zarrar Zafar REVIEWED BY: REVISIONS

031001 D. Told Joya 07/24/2025 SIG. INVENTORY NO. 05-1663T2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1663T2 DESIGNED: May 2025 SEALED: 7/15/2025 REVISED: N/A

MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel Main Menu >Controller >Unit

Web Interface

Home >Controller >Unit

Modify parameters as shown below and save changes.

Start Up Parameters

StartUp Clearance Hold

All Red Flash Exit Time

Unit Flash Parameters

NOTE CONTROL TYPE AND CONTROL SOURCE

	Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channe
	1	Phase Vehicle	1		Х	Х	1
	2	Phase Vehicle	2		Х		2
	3	Phase Vehicle	3		Х	Х	3
	4	Phase Vehicle	4		Х		4
	5	Phase Vehicle	5		Х		5
	6	Phase Vehicle	6		Х	Х	6
	7	Phase Vehicle	7		Х		7
→	8	Overlap	8		Х	Х	8
	9	Overlap	1		Х	Х	9
	10	Overlap	2		Х	Х	10
	11	Overlap	3		Х		11
	12	Overlap	4		Х		12
	13	Phase Ped	2				13
	14	Phase Ped	4				14
	15	Phase Ped	6				15
	16	Phase Ped	8				16
	17	Overlap	5		Х	Х	17
	18	Overlap	6		Х		18
			-				•

OUTPUT CHANNEL CONFIGURATION

Main Menu >Controller >More>Channels>Channels Config

Home >Controller >Advanced IO>Channels>Channel Configuration

Front Panel

Web Interface

Channel Configuration

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1663T2 DESIGNED: May 2025 SEALED: 7/15/2025 REVISED: N/A

Electrical Detail - Sheet 3 of 3

Prepared in the Offices of:

SR 1006 (Old Stage Road) SR 2736 (Rock Service Station Road)

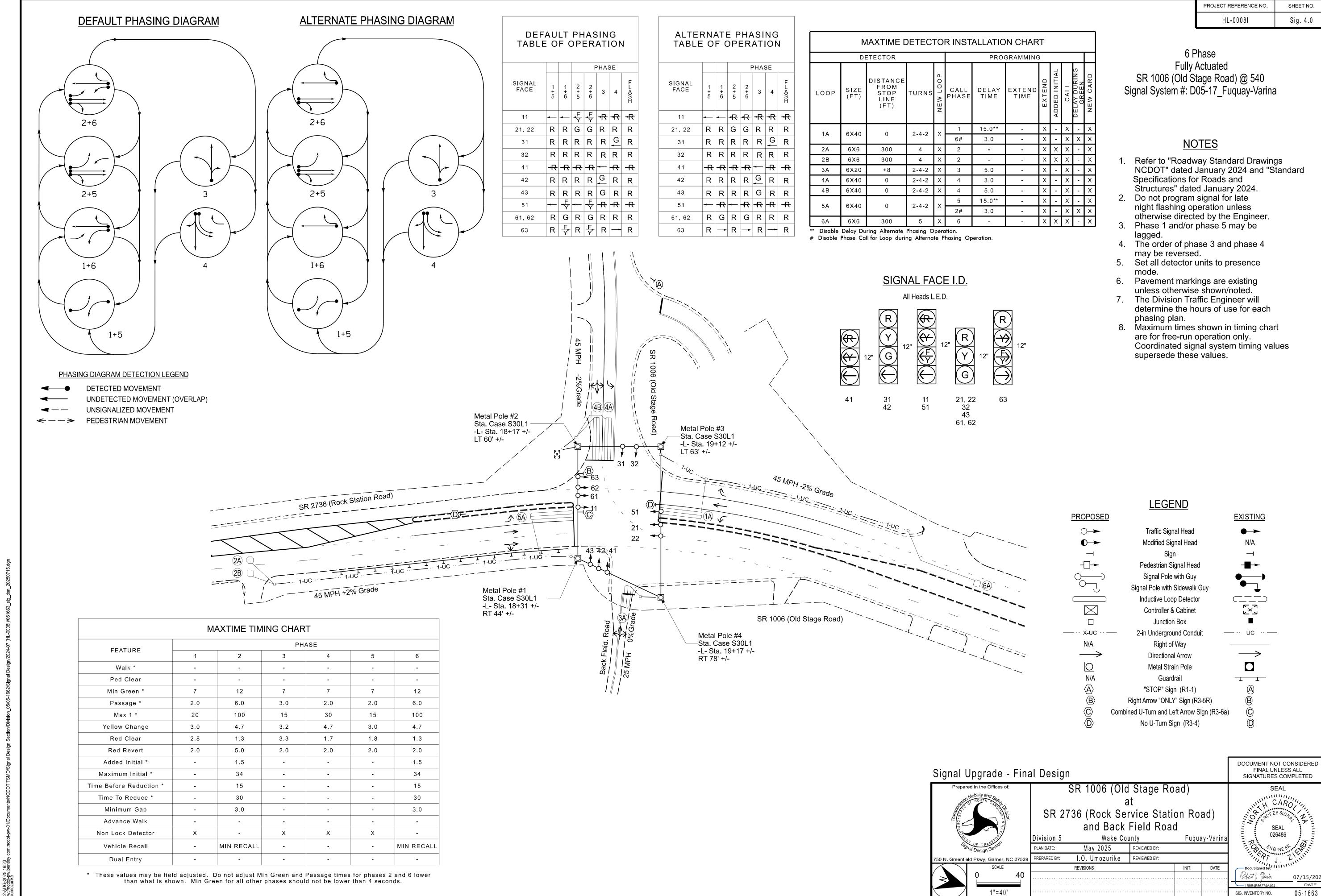
and Back Field Road Wake County

June 2025 REVIEWED BY: PREPARED BY: Zarrar Zafar REVIEWED BY:

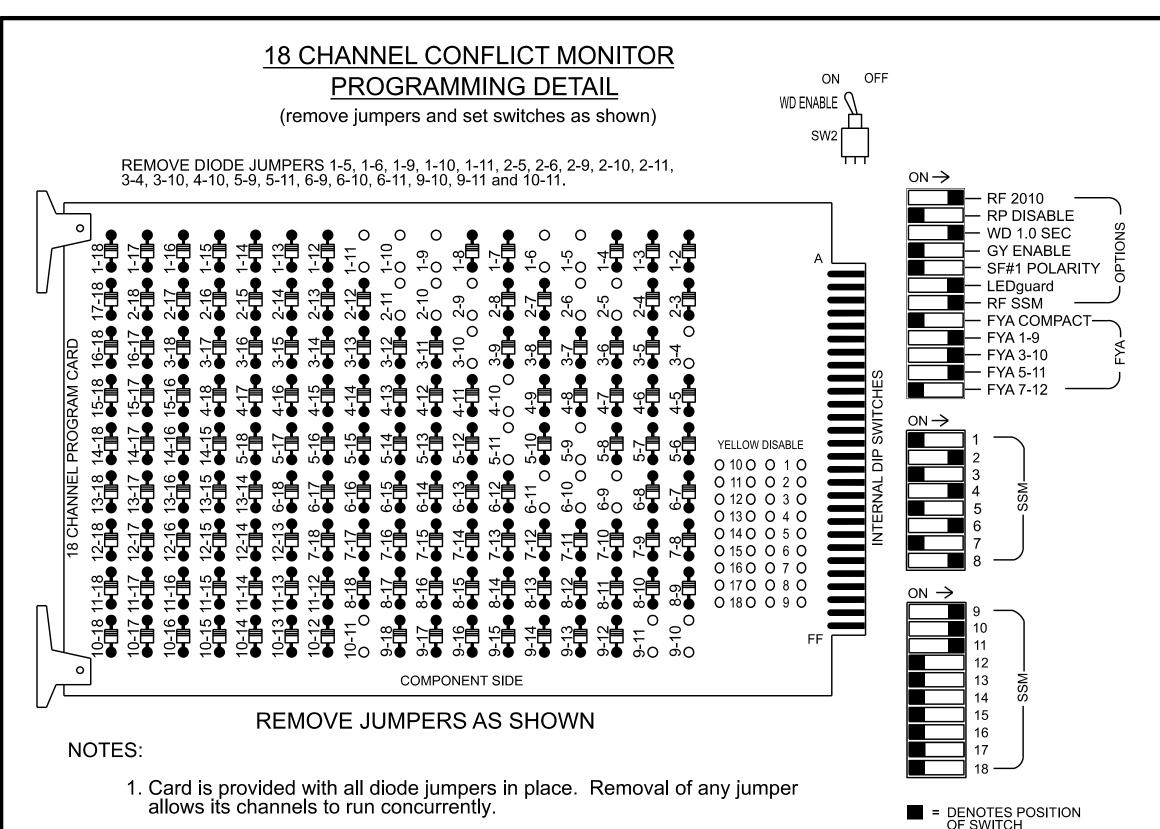
DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

D. told Joya 07/24/2025

SIG. INVENTORY NO. 05-1663T2



07/15/202



2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.

3. Ensure that the Red Enable is active at all times during normal operation.

INPUT FILE POSITION LAYOUT

(front view)

Phase 1 Yellow Field Terminal (126)

Phase 5 Yellow Field Terminal (132)

Overlap 7 Yellow Field

Terminal (117)

DC ISOLATOR

ST

FS = FLASH SENSE ST = STOP TIME

4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

S | Ø3 | Ø4 | S |

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

AC-

NOT USED

| ø 1 | ø 2 | s |

2A

2B

6A

EX.: 1A, 2A, ETC. = LOOP NO.'S

NOT USED

5A

USED | USED

ACCEPTABLE VALUES Value (ohms) Wattage

1.5K - 1.9K 25W (min)

2.0K - 3.0K | 10W (min)

NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- 2. Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- 3. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
- 4. The cabinet and controller are part of the SR 1006 (Old Stage Road) @ 540 CLS Signal System #: D05-17_Fuquay-Varina.

EQUIPMENT INFORMATION

Controller	2070LX
Cabinet	332 w/ Aux
Software	Q-Free MAXTIME
Cabinet Mount	Base
Output File Positions	18 With Aux. Output File
Load Switches Used	S1, S2, S4, S5, S7, S8, S11,
	AUX S1, AUX S2, AUX S4
Phases Used	1, 2, 3, 4, 5, 6
Overlap "1"	*
Overlap "2"	
Overlap "3"	
Overlap "4"	
Overlap "7"	*
Overlap "8"	

*See overlap programming detail on sheet 2

HL-00081 Sig. 4.1

(P)

(4)

⟨F Y

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					SIC	3NA	AL H	IEA	DΗ	00	K-U	PC	HA	RT							
LOAD SWITCH NO.	S1	S2	S3	S4		S5		S6	S7	S8	S9	S10	S	11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3		4		14	5	6	15	7	8	3	16	9	10	17	11	12	18
PHASE	1	2	2 PED	OL7		4		4 PED	5	6	6 PED	7	0	L8	8 PED	OL1		SPARE	OL3	OL4	SPAR
SIGNAL HEAD NO.	11	21,22	NU	★ 63	41	42	43	NU	★ 51	61,62	NU	NU	31	32	NU	11	63 [*]	NU	★ 51	NU	NU
RED		128				101	101			134			107	107			A124				
YELLOW	*	129		*		102	102		*	135			108	108							
GREEN		130				103	103			136			109	109							
RED ARROW					101											A121			A114		
YELLOW ARROW					102											A122	A125		A115		
FLASHING YELLOW ARROW																A123	A126		A116		
GREEN ARROW	127			118	103	103			133				109								
<u></u>																					
Not U	Jsed																				

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)

OL3 RED (A114) -

OL3 YELLOW (A115) -

OL3 GREEN (A116) -

05 GREEN (133) -

OL2 RED (A124)

OL2 YELLOW (A125) —

OL2 GREEN (A126) -

OL7 GREEN (118) -

Denotes install load resistor. See load resistor installation detail this sheet.

OL1 RED (A121) -

OL1 YELLOW (A122)

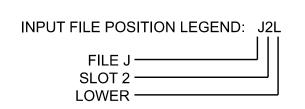
OL1 GREEN (A123) -

01 GREEN (127) -

★See pictorial of head wiring in detail this sheet.

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
1 /	TD0 4.0	1411	56	18	1★	1	15		Х		Х	
1A	TB2-1,2	I1U	56	-	29★	6	3		Х		Х	Х
2A	TB2-5,6	I2U	39	1	2	2			Х	Χ	Χ	
2B	TB2-7,8	I2L	43	5	3	2			Х	Χ	Х	
3A	TB4-5,6	I5U	58	20	7	3	5		Х		Х	
4A	TB4-9,10	I6U	41	3	8	4	3		Х		Х	
4B	TB4-11,12	I6L	45	7	9	4	5		Х		Χ	
5A	TB3-1,2	J1U	55	17	15★	5	15		Х		Χ	
JA	100-1,2	310	J5	•	31★	2	3		Х		Χ	Х
6A	TB3-5,6	J2U	40	2	16	6			Х	Χ	Χ	

[★] For the detectors to work as shown on the signal plan see the Detector Programming Detail for Alternate Phasing on Sheet 2 of this plan.



Prepared in the Offices of:

Electrical Detail - Sheet 1 of 3 SR 1006 (Old Stage Road)

Electrical and Programming Details For: SR 2736 (Rock Service Station Road) and Back Field Road

Fuguay-Varina PLAN DATE: June 2025 REVIEWED BY:

PREPARED BY: Zarrar Zafar REVIEWED BY: REVISIONS D. told Joya 07/23/2025 SIG. INVENTORY NO. 05-1663

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
1A	TB2-1,2	I1U	56	18	1*	1	15		Х		Χ	
IA	102-1,2	110	30	-	29★	6	3		Х		Χ	Х
2A	TB2-5,6	I2U	39	1	2	2			Х	Χ	Χ	
2B	TB2-7,8	I2L	43	5	3	2			Х	Χ	Χ	
3A	TB4-5,6	I5U	58	20	7	3	5		Х		Χ	
4A	TB4-9,10	I6U	41	3	8	4	3		Х		Χ	
4B	TB4-11,12	I6L	45	7	9	4	5		Х		Χ	
5 A	TD2 1 2	1411	55	17	15 *	5	15		Х		Χ	
5A	TB3-1,2	33-1,2 J1U	10 55	-	31*	2	3		Х		Χ	Х
6A	TB3-5,6	J2U	40	2	16	6			Х	Χ	Χ	
6A	TB3-5,6	J2U	40	2	16	6			Х	Χ	Χ	

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1663 DESIGNED: May 2025 SEALED: 7/15/2025 REVISED: N/A

FILE

MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	7	8	
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal	Normal	
Included Phases	2	6	6	4	3	
Modifier Phases	1	4	5	-	-	
Modifier Overlaps	-	1	-	-	ı	
Trail Green	0	0	0	0	0	
Trail Yellow	0.0	0.0	0.0	0.0	0.0	
Trail Red	0.0	0.0	0.0	0.0	0.0	

MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 1A & 5A

Front Panel

Main Menu >Controller >Detector >Veh Det Plans

Web Interface

Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

1A

Detector	Call Phase	Delay
1	1	0
29	0	0

5A

Detector	Call Phase	Delay
15	5	0
31	0	0

MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel

Main Menu > Controller > Coordination > Patterns

Web Interface

Home >Controller >Coordination >Patterns

Pattern Parameters

Pattern	Veh Det Plan	Overlap Plan		
*	2	2		

*The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	1	2	3	7	8
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal	Normal
Included Phases	1	6	-	4	3
Modifier Phases	1	4	5	•	-
Modifier Overlaps	1	1	-	ı	-
Trail Green	0	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0	0.0

NOTICE INCLUDED PHASE

THIS ELECTRICAL DETAIL IS FOR

THE SIGNAL DESIGN: 05-1663 DESIGNED: May 2025 SEALED: 7/15/2025 REVISED: N/A

Electrical Detail - Sheet 2 of 3

Prepared in the Offices of:

SR 1006 (Old Stage Road) SR 2736 (Rock Service Station Road)

and Back Field Road Fuquay-Varina June 2025 PLAN DATE: REVIEWED BY:

PREPARED BY: Zarrar Zafar REVIEWED BY: REVISIONS

D. told Joya 07/23/2025 SIG. INVENTORY NO. 05-1663

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To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases

for heads 11 and 51 to run protected turns only.

VEH DET PLAN 2: Disables phase 6 call on loop 1A

and reduces delay time for phase 1 call on loop 1A to 0 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Unit

Web Interface

Home >Controller >Unit

Modify parameters as shown below and save changes.

Start Up Parameters

StartUp Clearance Hold

Unit Flash Parameters
All Red Flash Exit Time

All Red Flash Exit Tim
6

OUTPUT CHANNEL CONFIGURATION

Front Panel

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channel Configuration

Channel Configuration

	Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
	1	Phase Vehicle	1		Х	Χ	1
NOTE: Control Type	2	Phase Vehicle	2		Χ		2
and Control Source	3	Overlap	7		X	Х	3
	4	Phase Vehicle	4		X		4
	5	Phase Vehicle	5		X		5
	6	Phase Vehicle	6		X	Х	6
NOTE: Control Type	7	Phase Vehicle	7		Х		7
and Control Source	8	Overlap	8		X	X	8
	9	Overlap	1		X	Х	9
	10	Overlap	2		X	Χ	10
	11	Overlap	3		X		11
	12	Overlap	4		X		12
	13	Phase Ped	2				13
	14	Phase Ped	4				14
	15	Phase Ped	6				15
	16	Phase Ped	8				16
	17	Overlap	5		X	Χ	17
	18	Overlap	6		X		18

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1663 DESIGNED: May 2025 SEALED: 7/15/2025 REVISED: N/A

Electrical Detail - Sheet 3 of 3

Prepared in the Offices of:

SR 1006 (Old Stage Road)

SR 2736 (Rock Service Station Road) and Back Field Road

Division 5 Wake County Fuquay-Varina
PLAN DATE: June 2025 REVIEWED BY:

PREPARED BY: Zarrar Zafar REVIEWED BY:

REVISIONS INIT. DATE

Docusigned by:

07/23/2025

07/23/2025

DATE

SIG. INVENTORY NO. 05-1663

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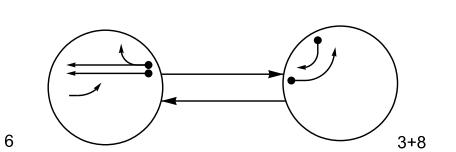
DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

PROJECT REFERENCE NO.

HL-0008I

pw://ncdot-pw.benfley.com:ncdot-pw-01/Documents/NCD0T TSM0/Signal
zzafar

DEFAULT PHASING DIAGRAM



PHASING DIAGRAM DETECTION LEGEND

UNSIGNALIZED MOVEMENT

UNDETECTED MOVEMENT (OVERLAP)

→ DETECTED MOVEMENT

← − → PEDESTRIAN MOVEMENT

ALTERNATE PHASING DIAGRAM

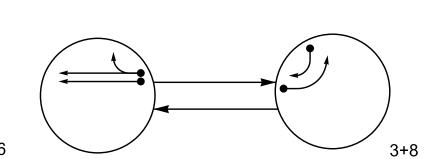


	TABLE OF	OPE	RAT	ION	
		F	PHASE	Ξ	
	SIGNAL FACE	6	3 + 8	FLASH	
	31, 32	- F Y	-	⊀R	
	61	†	R	R	
	62	G	R	R	
	81, 82	R	F	R	

DEFUALT PHASING

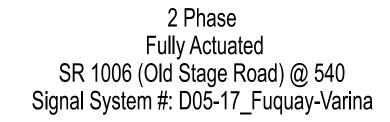
ALTERNATE PHASING TABLE OF OPERATION								
	F	PHASE						
SIGNAL FACE	6	3 + 8	FLASH					
31, 32	⊀R	-	⊀R					
61	1	R	R					
0.0		_						

81, 82 R F R

-	OPE	RAT	ION		
	PI	HASE	<u> </u>	MAXTIME DETECTO	R INSTALLATION CHAR
			DETECTOR	PROGRAMMING	

	DE	ETECTOR			PROGRAMMING							
LOOP	SIZE (FT)	DISTANCE FROM STOP LINE (FT)	TURNS	NEW LOOP	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	ALL	DELAY DURING GREEN	NEW CARD
3A	6X40	0	2-4-2	Х	3	15.0*	-	Х	-	Х	-	Χ
6A	6X6	300	5	Х	6	-	-	Х	Х	Х	-	Χ
6B	6X6	300	5	Х	6	-	-	Х	Х	Х	-	Χ
8A	6X40	0	2-4-2	Х	8	15.0	-	Х	-	Х	-	Χ

* Disable Delay During Alternate Phasing Operation.



NOTES

- Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and
- Structures" dated January 2024.

 2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.

 3. Set all detector units to presence
 - mode.
- 4. Locate new cabinet so as not to
- obstruct sight distance of vehicles turning right on red.

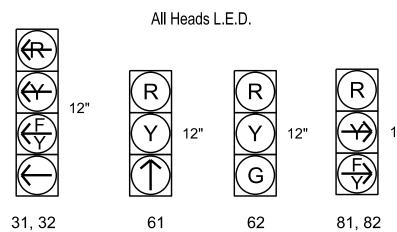
 The Division Traffic Engineer will determine the hours of use for each
- phasing plan.
 6. Maximum times shown in timing chart are for free-run operation only.
 Coordinated signal system timing values supersede these values.

	Metal Pole -I Sta. 28+19 +/- LT 52; +/- Wetal Pole -I. 522 +/-
SR 1006 (Old Stage Road)	45 MPH -3% Grade 32
	31

MAXTIM	IE TIMINO	G CHART	
FEATURE	PHASE		
FEATURE	3	6	8
Walk *	-	-	-
Ped Clear	-	-	-
Min Green *	7	12	7
Passage *	2.0	6.0	2.0
Max 1 *	15	100	15
Yellow Change	3.0	4.8	3.0
Red Clear	1.8	1.2	1.8
Added Initial *	-	1.5	-
Maximum Initial *	-	34	-
Time Before Reduction *	-	15	-
Time To Reduce *	-	30	-
Minimum Gap	-	3.0	-
Advance Walk	-	7	-
Non Lock Detector	-	-	-
Vehicle Recall	-	MIN RECALL	-
Dual Entry	Х	-	Х

* These values may be field adjusted. Do not adjust Min Green and Passage times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

SIGNAL FACE I.D.



\bigcirc	Traffic Signal Head	
0	Modified Signal Head	N/A
\dashv	Sign	\dashv
-□->	Pedestrian Signal Head	
<u> </u>	Signal Pole with Guy	•
	Signal Pole with Sidewalk Guy	
	Inductive Loop Detector	
	Controller & Cabinet	K×3
	Junction Box	
	- 2-in Underground Conduit	— ·· UC ·· —
N/A	Right of Way	
\longrightarrow	Directional Arrow	\longrightarrow
0	Metal Pole with Mastarm	
2-DD	 Directional Drill 	N/A
\bigcirc	Type II Signal Pedestal	
A	"RIGHT TURN YIELD TO U-TURN" Sign	A
$\langle \overline{B} \rangle$	No U-Turn/ No Left Turn Sign (R3-18)	(B)

Right Arrow "ONLY" Sign (R3-5R)

<u>LEGEND</u>



SR 1006 (Old Stage Road) SR 2724 (Banks Road)

		ON ZIZT (D		<i>,</i>		
	Division 5	Wake Co	unty	Fuqua	ay-Varina	
	PLAN DATE:	May 2025	REVIEWED BY:			
29	PREPARED BY:	I.O. Umozurike	REVIEWED BY:			
		REVISIONS		INIT.	DATE	
)						1
				[

	SIGNATURES COMPLETED
а	SEAL CARO SEAL 026486 DocuSigned by:
	DocuSigned by: 7711111111111111111111111111111111111
-	
-	1B9B4B66274A494 DATE

SIG. INVENTORY NO.

05-1662

DOCUMENT NOT CONSIDERED

FINAL UNLESS ALL

 \bigcirc

REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.

COMPONENT SIDE

- 2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- 3. Ensure that the Red Enable is active at all times during normal operation.

INPUT FILE POSITION LAYOUT

(front view)

4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

S | Ø3 |

S

EX.: 1A, 2A, ETC. = LOOP NO.'S

NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- 2. Program phases 3 and 8 for Dual Entry.
- 3. Program controller to start up in phase 2 Phase Not On and phase 6 Green No Walk.
- 4. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
- 5. The cabinet and controller are part of the SR 1006 (Old Stage Road) @ 540 CLS Signal System #: D05-17_Fuquay-Varina.

EQUIPMENT INFORMATION

Controller	2070LX
Cabinet	.332 w/ Aux
Software	.Q-Free MAXTIME
Cabinet Mount	.Base
Output File Positions	.18 With Aux. Output Fi
Load Switches Used	S1, S4, S8, AUX S1,
	AUX S2, AUX S4
Phases Used	3, 6, 8**
Overlap "1"	*
Overlap "2"	
Overlap "3"	
Overlap "4"	
Overlap "7"	
•	

*See overlap programming detail on sheet 2

**Phase used for timing purposes

= DENOTES POSITION OF SWITCH

DC ISOLATOR

ST

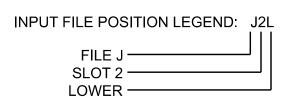
FS = FLASH SENSE

ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
3A	TB4-5,6	I5U	58	20	7 ★	3	15		Х		Х	
6A	TB3-5,6	J2U	40	2	16	6			Х	Χ	Х	
6B	TB3-7,8	J2L	44	6	17	6			Х	Х	Х	
8A	TB5-9,10	J6U	42	4	22	8	15		Х		Х	

★ For the detectors to work as shown on the signal design plan, see the Detector Programming Detail for Alternate Phasing on sheet 2.



HL-00081 Sig. 5.1

				SIC	3NA	AL H	IEA	DΗ	00	K-U	РC	HA	RT						
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S	8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	(6	15	7	8	16	9	10	17	11	12	18
PHASE	OL7	2	2 PED	3	4	4 PED	5	(6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	31 [★]	NU	NU	32 ★	NU	NU	NU	61	62	NU	NU	NC	NU	★ 31	★ 32	NU	★ 81,82	NU	NU
RED								134	134								A114		
YELLOW	*			*				135	135										
GREEN									136										
RED ARROW														A121	A124				
YELLOW ARROW														A122	A125		A115		
FLASHING YELLOW ARROW														A123	A126		A116		
GREEN ARROW	127			118				136											
₩																			
*																			
NU = Not	Used												1			1	<u> </u>	1	

NU = Not Used

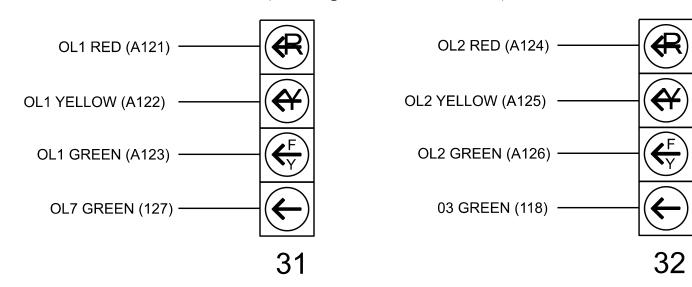
NC = No Connection

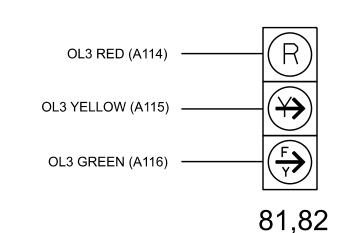
 $^{m{\star}}$ Denotes install load resistor. See load resistor installation detail this sheet.

★See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)





THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1662 DESIGNED: May 2025 SEALED: 8/28/2025

| Electrical Detail - Sheet 1 of 3

Prepared in the Offices of:

REVISED: N/A

SR 1006 (Old Stage Road) SR 2724 (Banks Road)

Fuquay-Varina PLAN DATE: August 2025 REVIEWED BY: PREPARED BY: Zarrar Zafar REVIEWED BY:

D. told Joya 08/29/2025 SIG. INVENTORY NO. 05-1662

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

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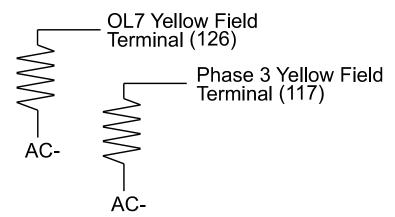
REVISIONS

LOAD RESISTOR INSTALLATION DETAIL

NOT USED

(install resistors as shown)

ACCEPTABLE VALUES Value (ohms) Wattage 1.5K - 1.9K 25W (min) 2.0K - 3.0K | 10W (min)



MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	7
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal
Included Phases	6	6	8	3
Modifier Phases	3	3	1	ı
Modifier Overlaps	1	-	•	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	1	2	3	7	
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal	
Included Phases	1	-	8	3	NOTICE INCLUDED PHASE
Modifier Phases	3	3	-	-	
Modifier Overlaps	1	-	-	1	
Trail Green	0	0	0	0	
Trail Yellow	0.0	0.0	0.0	0.0	
Trail Red	0.0	0.0	0.0	0.0	

MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOP 3A

Front Panel

Main Menu >Controller >Detector >Veh Det Plans

Web Interface

Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Detector	Call Phase	Delay
7	3	0

MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

OVERLAP PLAN	VEH DET PLAN
1	1
2	2
	OVERLAP PLAN 1 2

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases for heads 31 and 32 to run

VEH DET PLAN 2: Reduces delay time for phase 3

protected turns only.

all on loop 3A to 0 seconds.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1662 DESIGNED: May 2025 SEALED: 8/28/2025 REVISED: N/A

MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Coordination >Patterns

Web Interface

Home >Controller >Coordination >Patterns

Pattern Parameters

Pattern	Veh Det Plan	Overlap Pla		
*	2	2		

*The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

| Electrical Detail - Sheet 2 of 3

Prepared in the Offices of:

SR 1006 (Old Stage Road) SR 2724 (Banks Road)

Fuguay-Varina

August 2025 PLAN DATE: REVIEWED BY: PREPARED BY: Zarrar Zafar REVIEWED BY: REVISIONS

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED 031001

D. told Joya 08/29/2025 SIG. INVENTORY NO. 05-1662

MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel Main Menu >Controller >Unit

Web Interface

Home >Controller >Unit

Modify parameters as shown below and save changes.

Start Up Parameters

StartUp Clearance Hold

Unit Flash Parameters All Red Flash Exit Time

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1662 DESIGNED: May 2025 SEALED: 8/28/2025 REVISED: N/A

Electrical Detail - Sheet 3 of 3

Prepared in the Offices of:

SR 1006 (Old Stage Road) SR 2724 (Banks Road)

Wake County Fuquay-Varina August 2025 REVIEWED BY: PLAN DATE:

PREPARED BY: Zarrar Zafar REVIEWED BY: DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED Docusigned by:

08/29/2025

08/29/2025

DATE

OUTPUT CHANNEL CONFIGURATION

Front Panel

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channel Configuration

Channel Configuration

NOTE: Control Type	Channel	Control Type	Control Source F	lash Yellow	Flash Red	Flash Alt	MMU Channel
and Control Source	1	Overlap	7		Χ	X	1
	2	Phase Vehicle	2		Χ		2
	3	Phase Vehicle	3		Χ	Х	3
	4	Phase Vehicle	4		Х		4
	5	Phase Vehicle	5		Х		5
	6	Phase Vehicle	6		Х	Х	6
	7	Phase Vehicle	7		Х		7
	8	Phase Vehicle	8		Х	Х	8
	9	Overlap	1		Х	Х	9
	10	Overlap	2		Х	Х	10
	11	Overlap	3		Х		11
	12	Overlap	4		Х		12
	13	Phase Ped	2				13
	14	Phase Ped	4				14
	15	Phase Ped	6				15
	16	Phase Ped	8				16
	17	Overlap	5		Х	Х	17
	18	Overlap	6		Х		18

SIG. INVENTORY NO. 05-1662

Elevation View

Base line reference elev. = 0.0'

High Point of Roadway Surface

SPECIAL NOTE

The contractor is responsible for verifying that the mast arm attachment height (H1) will provide the "Design Height" clearance from the roadway before submitting final shop drawings for approval. Verify elevation data below which was obtained by field measurement or from available project survey

Elevation Data for Mast Arm Attachment (H1)

Elevation Differences for:	Pole
Baseline reference point at © Foundation @ ground level	0.0 ft.
Elevation difference at High point of roadway surface	+1.5 ft.
Elevation difference at Edge of travelway or face of curb	N/A

POLE RADIAL ORIENTATION

Direction

B.C

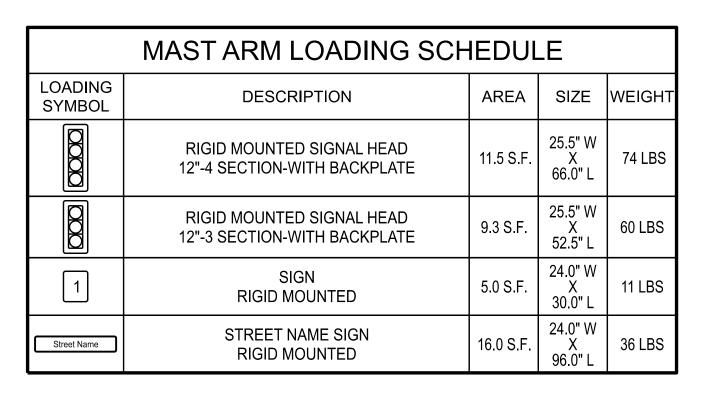
H2

See Note 8

Note 7

See Note 7d

Ç Foundation



PROJECT REFERENCE NO.

HL-00081

Sig. 5.4

NOTES

METAL POLE

DESIGN REFERENCE MATERIAL

Terminal

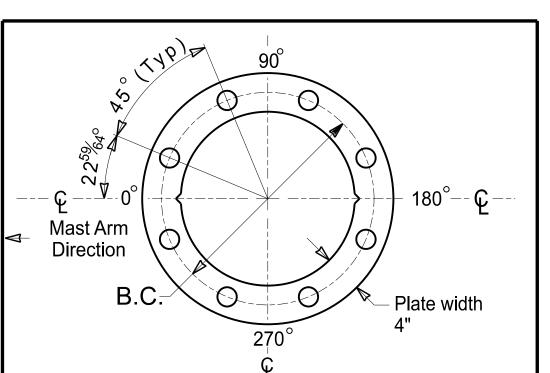
Compartment

· 180°—-

- 1. Design the traffic signal structure and foundation in accordance with:
- The 6th Edition 2013 AASHTO "Standard Specifications for Structural Supports for Highway Signs, Luminaires, and Traffic Signals, including all of
- The 2018 NCDOT "Standard Specifications for Roads and Structures." The latest addenda to the specifications can be found in the traffic signal project special provisions.
- The 2018 NCDOT Roadway Standard Drawings.
- The traffic signal project plans and special provisions.
- The NCDOT "Metal Pole Standards" located at the following NCDOT website: https://connect.ncdot.gov/resources/safety/Pages/ITS-Design-Resources.aspx

DESIGN REQUIREMENTS

- 2. Design the traffic signal structure using the loading conditions shown in the elevation views. These are anticipated worst case "design loads" and may not represent the actual loads that will be applied at the time of the installation. The contractor should refer to the traffic signal plans for the actual loads that will be applied at the time of the installation.
- 3. Design all signal supports using stress ratios that do not exceed 0.9.
- 4. A clamp-type bolted mast arm-to-pole connection may be used instead of the welded ring stiffened box connection shown as long as the connection meets all of the design requirements. This requires staggering the connections. Use elevation data for each arm to determine appropriate connection
- 5. Design base plate with 8 anchor bolt holes. Provide 2 inch x 60 inch anchor bolts.
- 6. The mast arm attachment height (H1) shown is based on the following design assumptions:
- a. Nominal vertical rise in mast arm is 5 feet as measured from the centerline of the arm base to the centerline of the free end of the arm.
- b. Signal heads are rigidly mounted and vertically centered on the mast arm.
- c. The roadway clearance height for design is as shown in the elevation views.
- d. The top of the pole base plate is 0.75 feet above the ground elevation.
- e. Refer to the Elevation Data Chart for the elevation differences between the proposed foundation ground level and the high point of the roadway.
- f. Provide horizontal distance from the proposed centerline of the foundation to the edge of travelway. Refer to the Elevation Data Chart for elevation difference between the proposed foundation ground level and the edge of travelway. This information is necessary to ensure that the roadway clearance is maintained at the edge of the travelway and to aid in the camber design of the arm.
- 7. The pole manufacturer will determine the total height (H2) of each pole using the greater of the following:
- Mast arm attachment height (H1) plus 2 feet, or
- H1 plus 1/2 of the total height of the mast arm attachment assembly plus 1 foot.
- 8. If pole location adjustments are required, the contractor must gain approval from the Engineer as this may affect the mast arm lengths and arm attachment heights. The contractor may contact the Signal Design Section Senior Structural Engineer for assistance at (919) 814-5000.
- 9. The contractor is responsible for verifying that the mast arm length shown will allow proper positioning of the signal heads over the roadway.
- 10. The contractor is responsible for providing soil penetration testing data (SPT) to the pole manufacturer so site specific foundations can be designed.



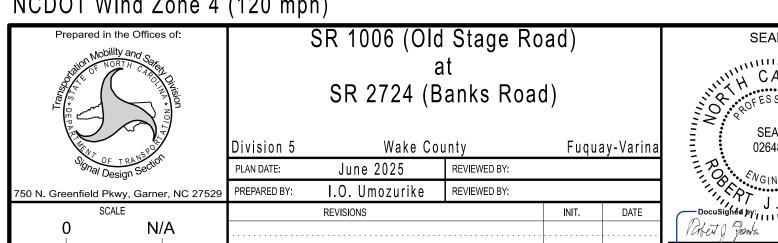
8 BOLT BASE PLATE DETAIL

See Note 6

BASE PLATE TEMPLATE & ANCHOR BOLT LOCK PLATE DETAIL For 8 Bolt Base Plate

NCDOT Wind Zone 4 (120 mph)

N/A

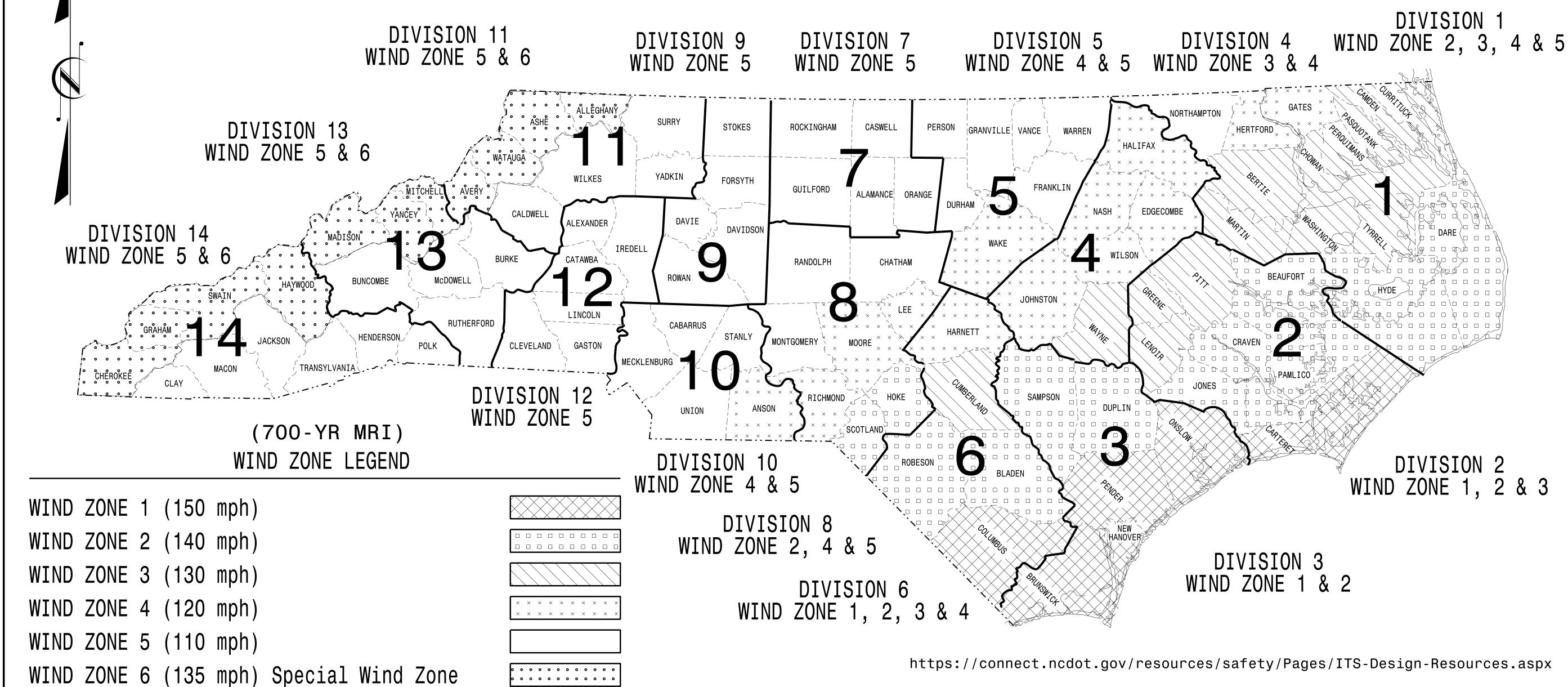


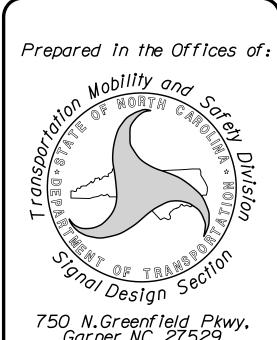
SEAL SIG. INVENTORY NO. 05-1662

STATE OF NORTH CAROLINA DIVISION OF HIGHWAYS

PROJECT I.D. NO. SHEET NO Sig.M1A

STANDARD DRAWINGS FOR ALL METAL POLES (LRFD)





Designed in conformance with the latest 2020 Interim to the 1st Edition 2015

AASHTO LRFD

Standard Specifications for Highway Signs, Luminaires, and Traffic Signals

DRAWING

Sig. M 9

INDEX OF PLANS **NUMBER DESCRIPTION**

Statewide Wind Zone Map (700-yr MRI) Sig. M 1A Statewide Wind Zone Map (10-yr MRI) Sig. M 1B Typical Fabrication Details-All Metal Poles **Sig.** M 2 **Sig.** *M* 3 Typical Fabrication Details-Strain Poles Typical Fabrication Details-Mast Arm Poles Sig. M 4 Typical Fabrication Details-Mast Arm Connection **Sig.** *M* 5 Typical Fabrication Details-Strain Pole Attachments Sig. M 6 Construction Details-Foundations Sig. M Standard Strain Pole Foundation-All Soil Conditions Sig. M 8

Typical Fabrication Details-CCTV Camera Poles

MOBILITY AND SAFETY DIVISION -TRANSPORTATION SYSTEMS MANAGEMENT AND OPERATIONS UNIT

D.Y. ISHAK – STATE SIGNALS ENGINEER

K. DURIGON, P.E. – ITS AND SIGNALS STRUCTURAL ENGINEER

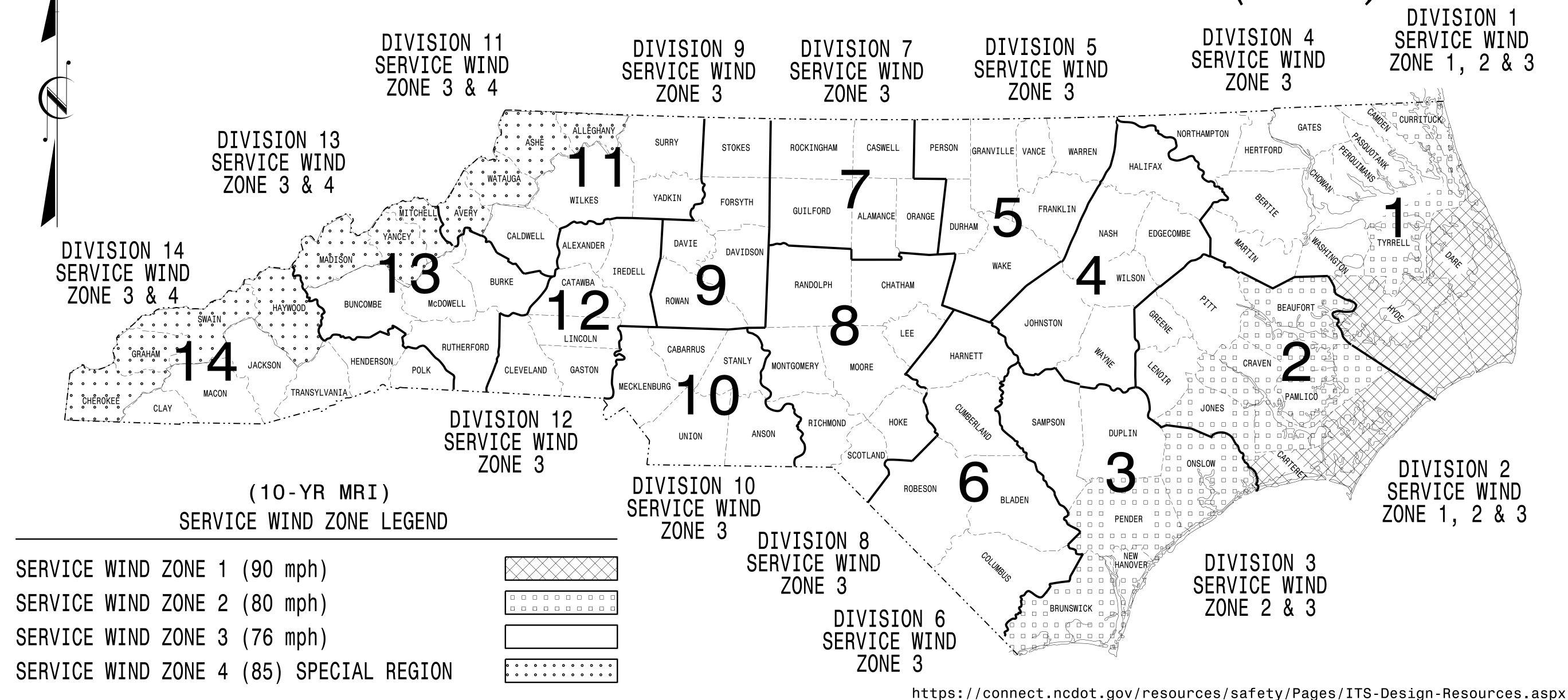
B. WALKER, P.E. – ITS AND SIGNALS STRUCTURAL ENGINEER



STATE OF NORTH CAROLINA DIVISION OF HIGHWAYS

PROJECT I.D. NO. SHEET NO Sig.M1B

STANDARD DRAWINGS FOR ALL METAL POLES (LRFD)





Designed in conformance with the latest 2020 Interim to the 1st Edition 2015

AASHTO LRFD

Sig. M

Sig. M 9

Standard Specifications for Highway Signs, Luminaires, and Traffic Signals

INDEX OF PLANS **DRAWING** NUMBER

NUMBER	DESCRIPTION						
Sig. M 1A	Statewide Wind Zone Map (700-yr MRI)						
Sig. M 1B	Statewide Wind Zone Map (10-yr MRI)						
Sig. M 2	Typical Fabrication Details-All Metal Poles						
Sig. M 3	Typical Fabrication Details-Strain Poles						
Sig. M 4	Typical Fabrication Details-Mast Arm Poles						
Sig. M 5	Typical Fabrication Details-Mast Arm Connection						
Sig. M 6	Typical Fabrication Details-Strain Pole Attachments						
Sig. M 7	Construction Details-Foundations						
Sig. M 8	Standard Strain Pole Foundation-All Soil Conditions						

Typical Fabrication Details-CCTV Camera Poles

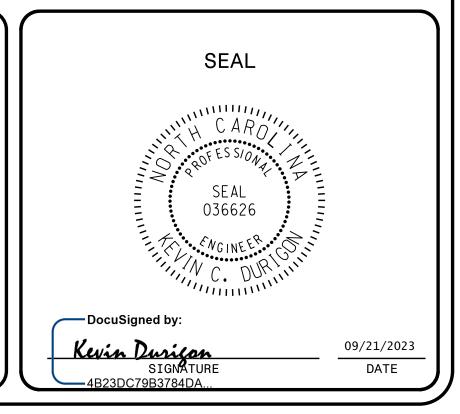
NCDOT CONTACTS:

MOBILITY AND SAFETY DIVISION -TRANSPORTATION SYSTEMS MANAGEMENT AND OPERATIONS UNIT

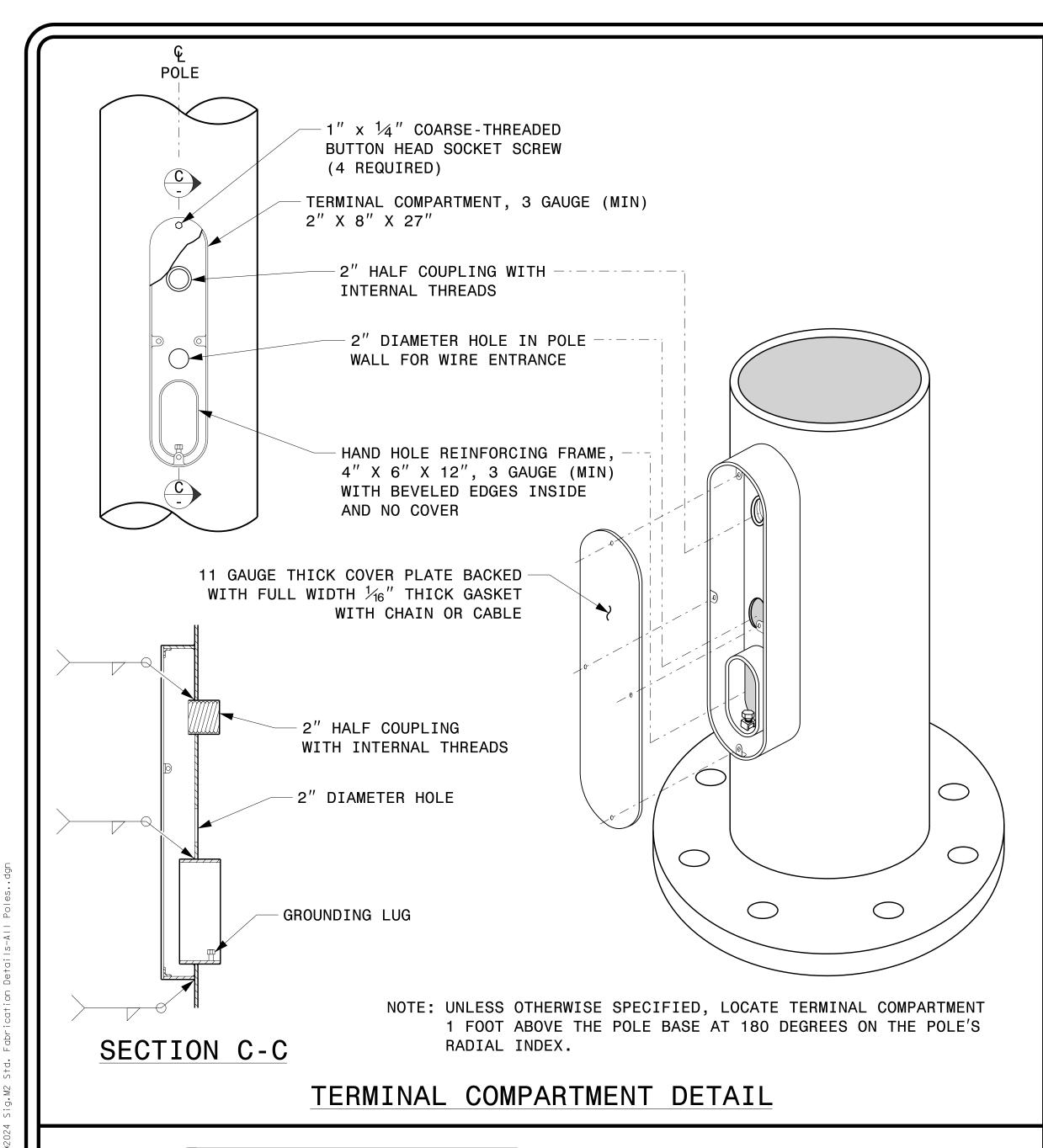
D.Y. ISHAK – STATE SIGNALS ENGINEER

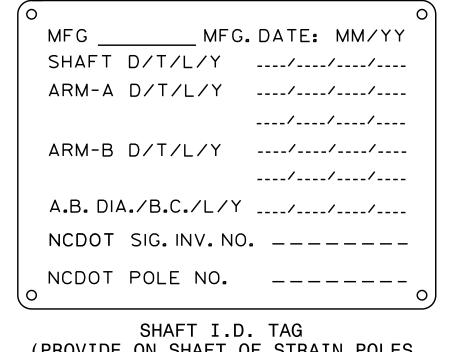
K. DURIGON, P.E. – ITS AND SIGNALS STRUCTURAL ENGINEER

B. WALKER, P.E. – ITS AND SIGNALS STRUCTURAL ENGINEER









MFG. DATE: MM/YY SECTION D/T/L/Y ----/---NCDOT SIG. INV. NO. _____ NCDOT POLE NO. _____

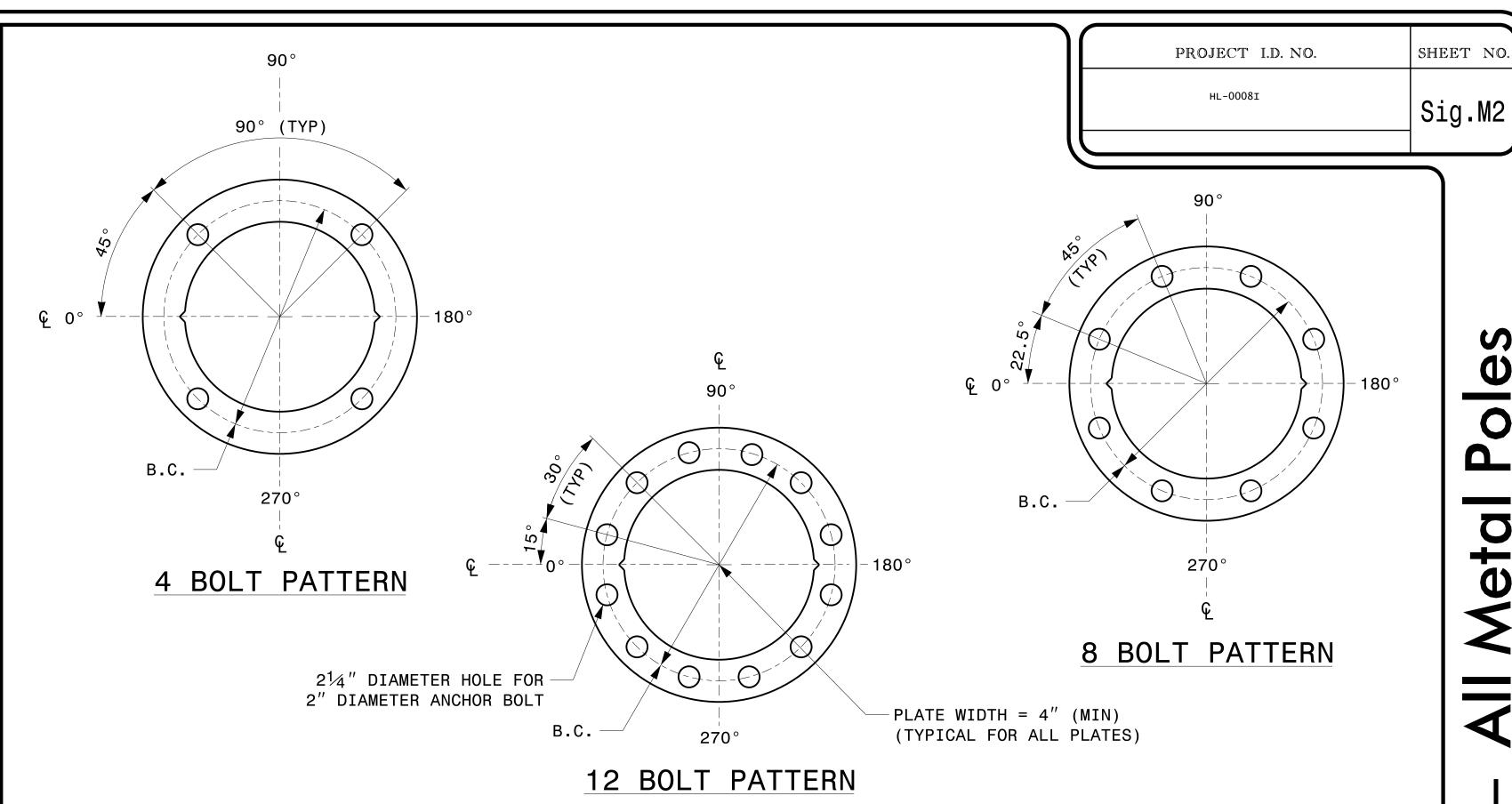
ARM I.D. TAG (PROVIDE ON EACH SECTION OF `A MULTI-SECTION MAST ARM)

(PROVIDE ON SHAFT OF STRAIN POLES AND MAST ARM POLE SHAFT)

NOTES:

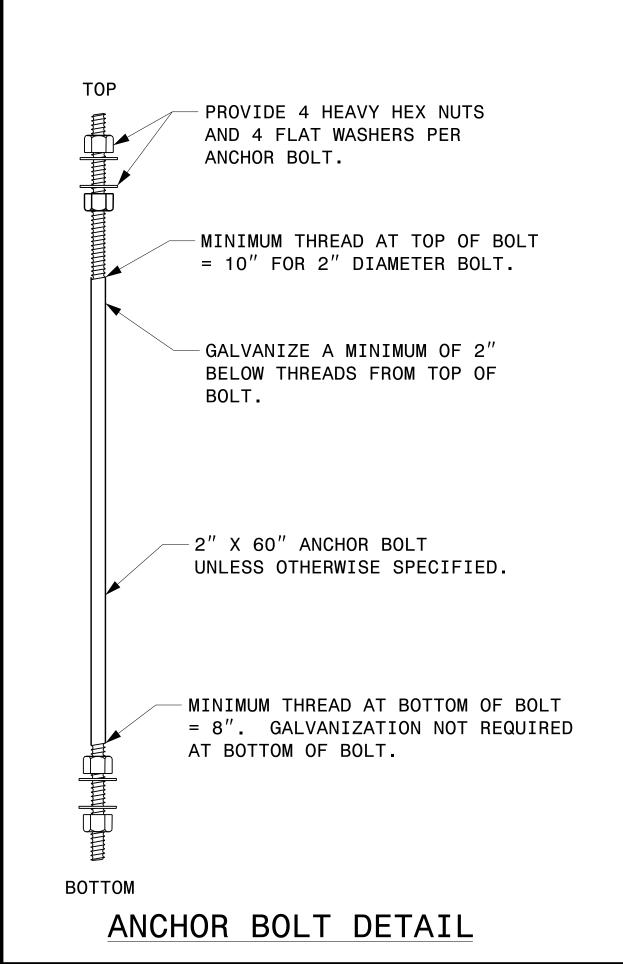
- 1. D = DIAMETER, T = THICKNESS, L = LENGTH, Y = YIELD STRENGTH
- 2. A.B. = ANCHOR BOLT
- 3. B.C. = BOLT CIRCLE OF ANCHOR BOLTS
- 4. IF STANDARD DESIGN, INCLUDE CASE NUMBER IN ADDITION TO
- POLE NUMBER ON "NCDOT POLE NO." LINE.
- 5. SIGNAL INV. NUMBER AND POLE I.D. NUMBER. SEE DRAWING M3 AND M4 FOR MOUNTING POSITIONS OF I.D. TAGS.

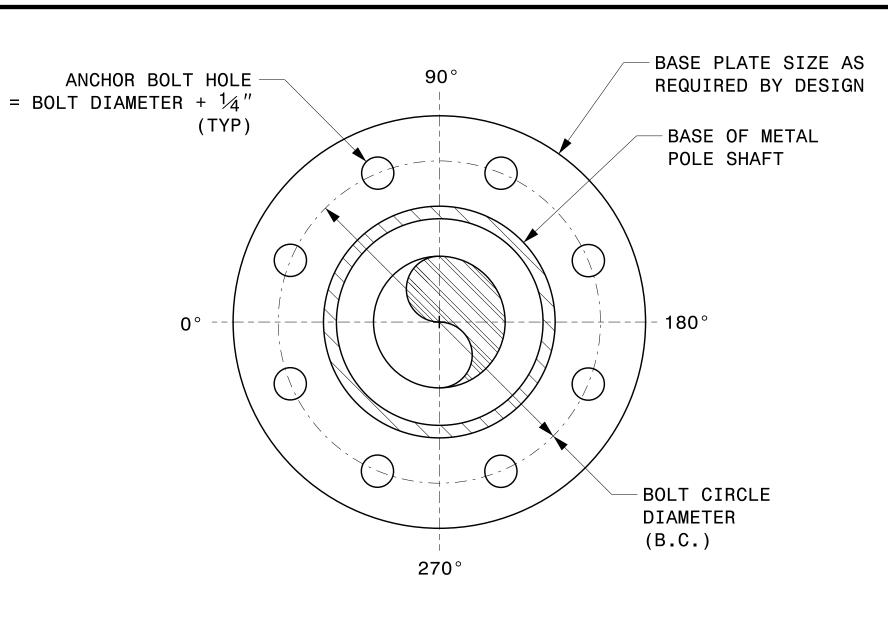




CONSTRUCT TEMPLATES AND PLATES FROM 1/4" (MIN) THICK STEEL. GALVANIZING IS NOT REQUIRED.

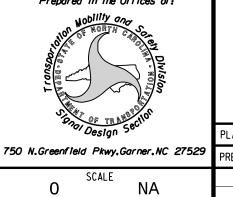
BASE PLATE TEMPLATE AND ANCHOR BOLT LOCK PLATE DETAILS





NOTE: BASE PLATE MAY BE CIRCULAR, OCTAGONAL, SQUARE OR RECTANGULAR IN SHAPE.

TYPICAL BASE PLATE DETAIL



NONE

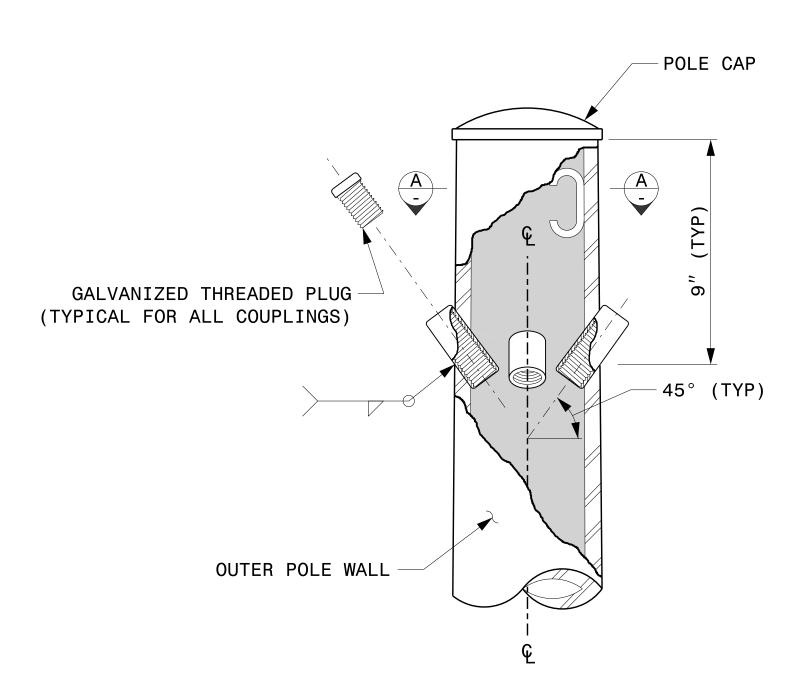
Typical Fabrication Details All Metal Poles

PLAN DATE: SEPTEMBER 2023 DESIGNED BY: C.F.ANDREWS PREPARED BY: K.C. DURIGON REVIEWED BY: D.C. SARKAR

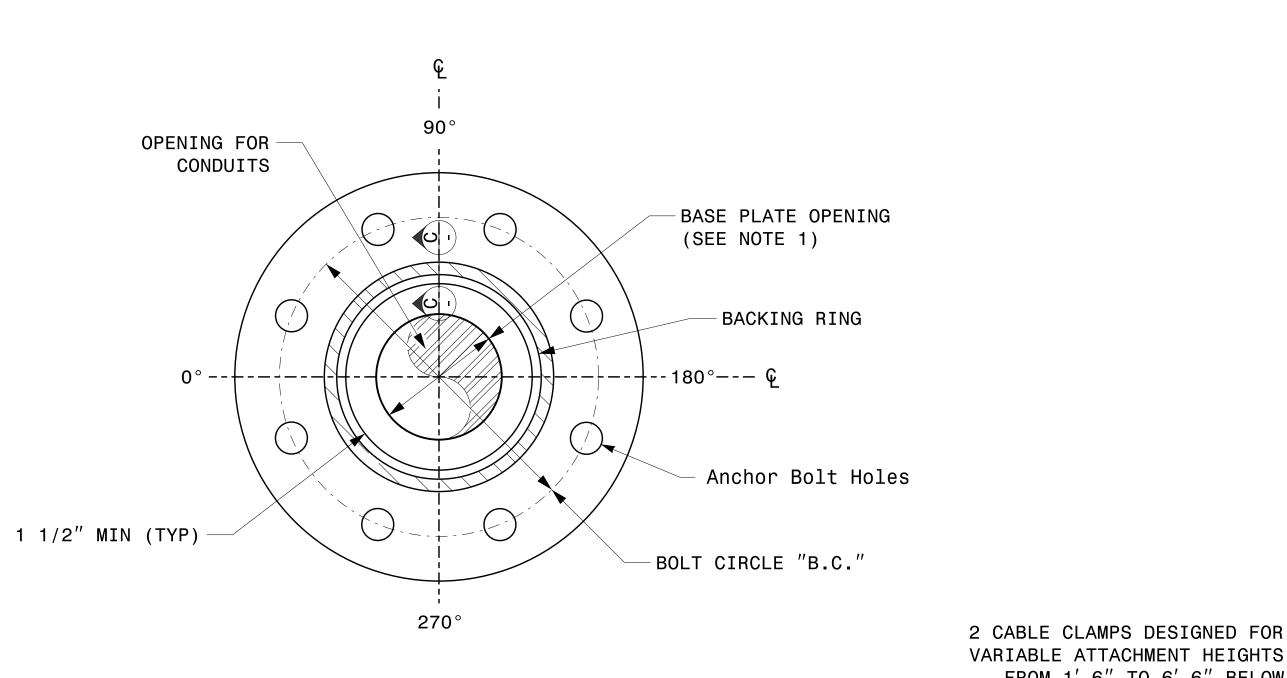
SEAL DocuSigned by: Kevin Durigan 09/21/2023

1. OPENING IN POLE BASE PLATE SHALL BE EQUAL TO POLE BASE INSIDE DIAMETER MINUS $3\frac{1}{2}''$ BUT SHALL NOT BE LESS THAN $8\frac{1}{2}$ ".

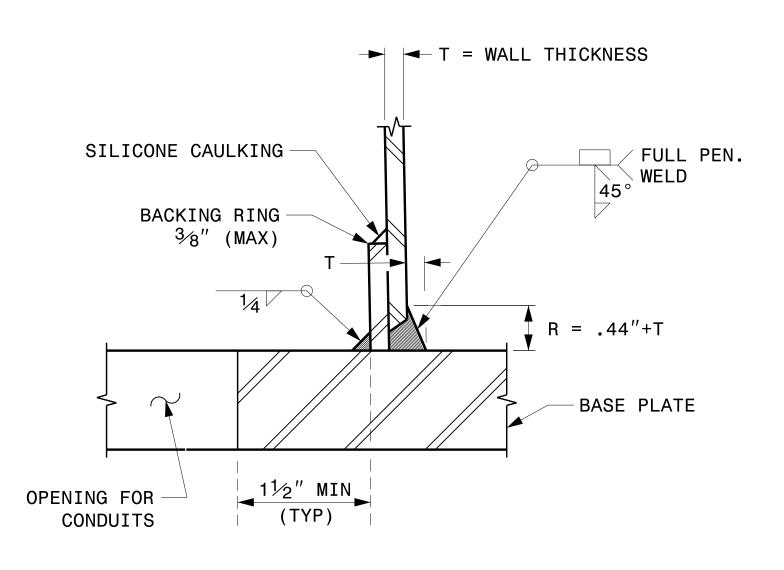
PROJECT I.D. NO. SHEET NO HL-0008I Sig.M3



CABLE ENTRANCES AT TOP OF POLE

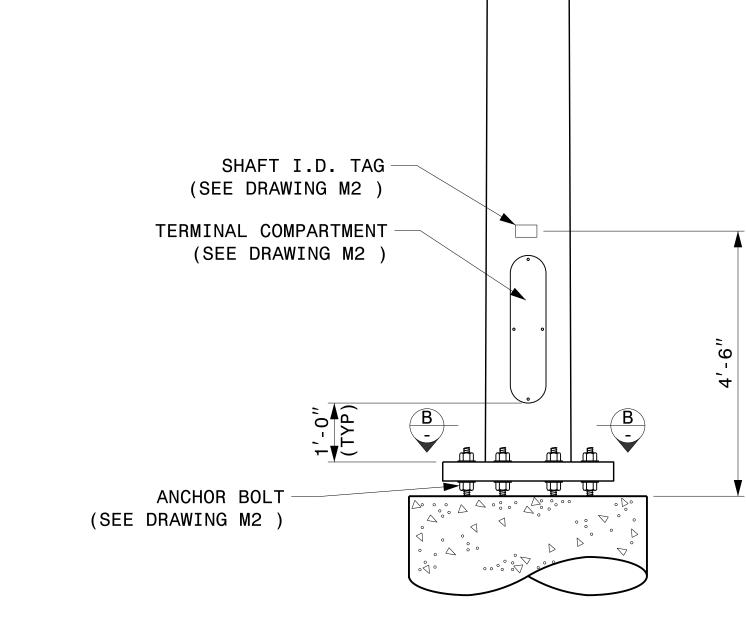


SECTION B-B POLE BASE PLATE DETAILS (8 AND 12 BOLT PATTERN)



SECTION C-C (POLE ATTACHMENT TO BASE PLATE)

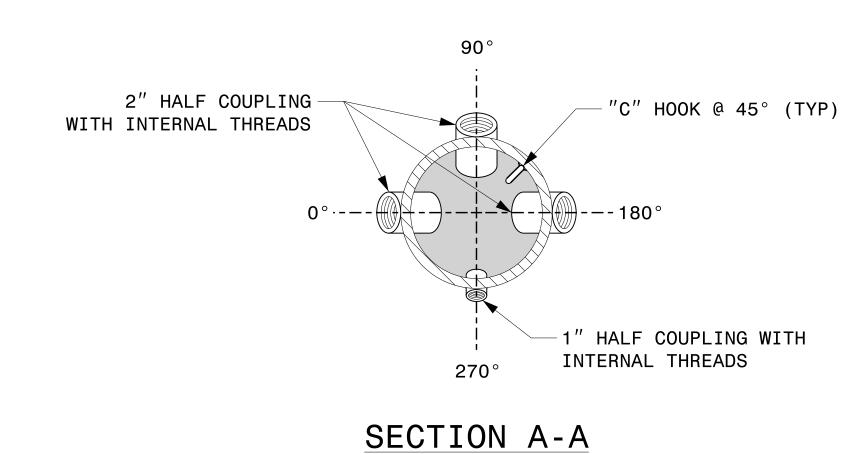
FULL-PENETRATION GROOVE WELD DETAIL



FROM 1'-6" TO 6'-6" BELOW

THE TOP OF THE POLE

MONOTUBE STRAIN POLE



RADIAL ORIENTATION OF FACTORY INSTALLED ACCESSORIES AT TOP OF POLE

SEAL Typical Fabrication Details Strain Poles PLAN DATE: SEPTEMBER 2023 DESIGNED BY: K.C. DURIGON PREPARED BY: K.C. DURIGON REVIEWED BY: D.C. SARKAR Kevin Durison 09/21/2023 DATE

SHEET NO

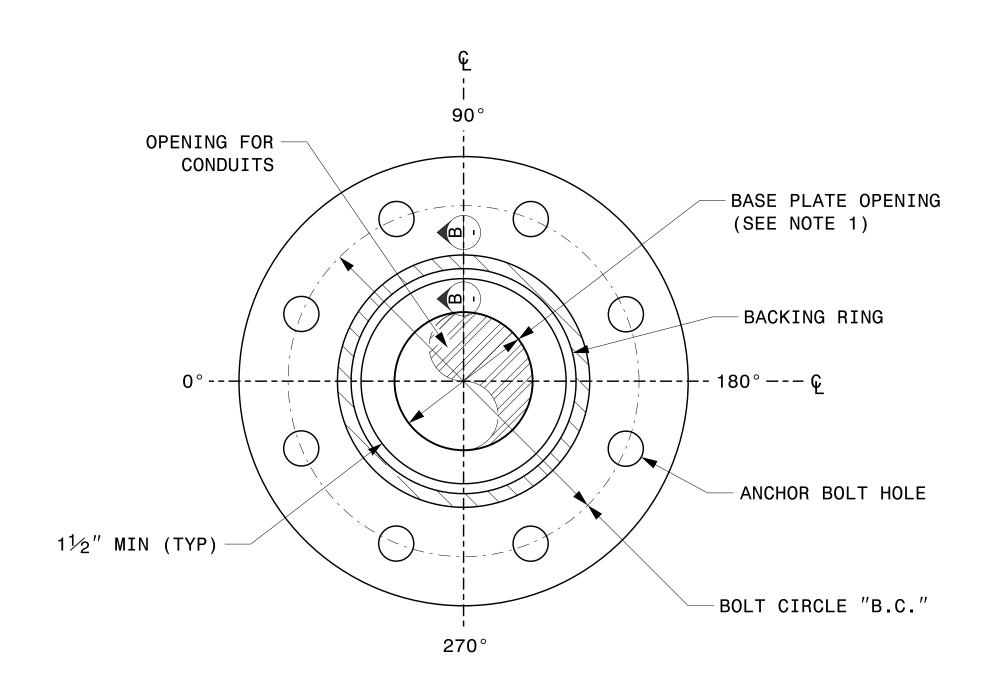
Sig.M4

PROJECT I.D. NO.

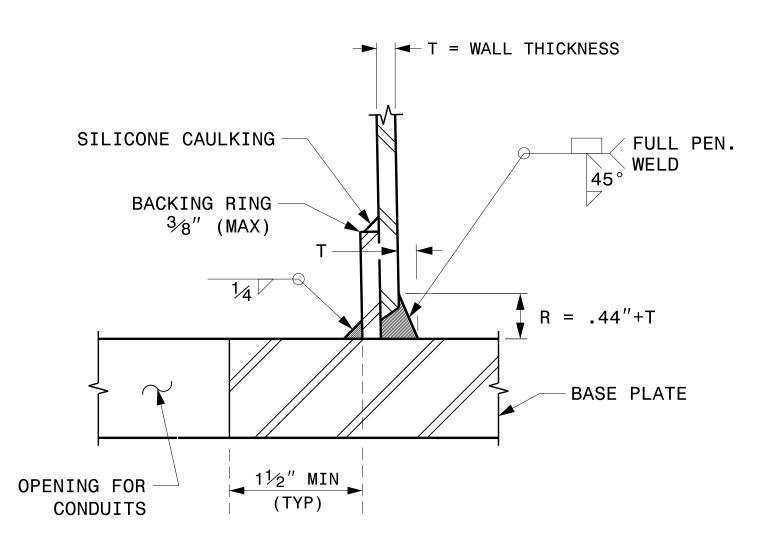
HL-0008I

NOTE:

1. OPENING IN POLE BASE PLATE SHALL BE EQUAL TO POLE BASE INSIDE DIAMETER MINUS 31/2" BUT SHALL NOT BE LESS THAN $8\frac{1}{2}$ ".

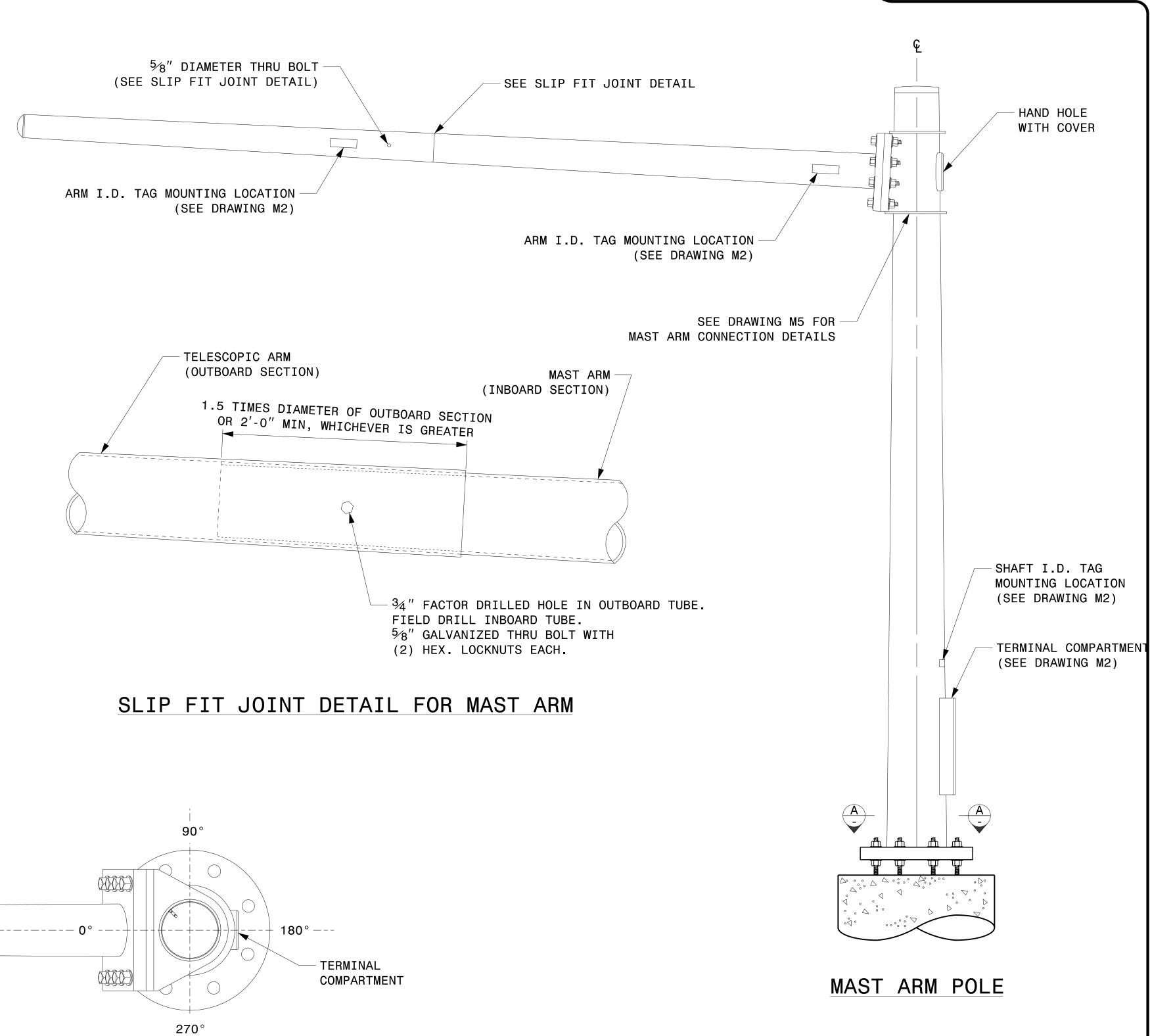


SECTION A-A POLE BASE PLATE DETAILS



SECTION B-B (POLE ATTACHMENT TO BASE PLATE)

FULL-PENETRATION GROOVE WELD DETAIL



MAST ARM RADIAL ORIENTATION

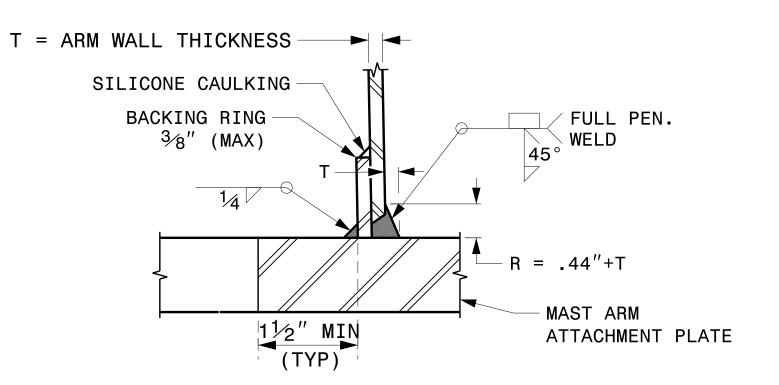
Typical Fabrication Details Mast Arm Poles

PLAN DATE: SEPTEMBER 2023 DESIGNED BY: K.C. DURIGON PREPARED BY: K.C. DURIGON REVIEWED BY: D.C. SARKAR

Kevin Durigan 09/21/2023 DATE

WELDED RING STIFFENED MAST ARM CONNECTION

PROJECT I.D. NO. HL-0008I



SECTION B-B FULL-PENETRATION GROOVE WELD DETAIL

-4" DIAMETER HOLE FOR

+ HARDENED FLAT WASHER

HIGH STRENGTH BOLT

- FULL-PENETRATION GROOVE WELD DETAIL (SEE SECTION B-B)

(TYP)

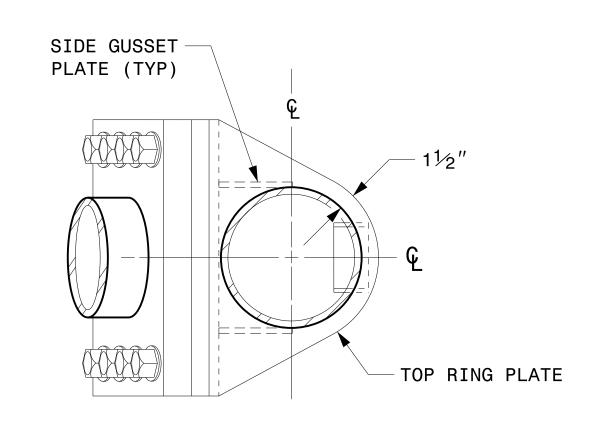
PLATE WIDTH

BOLT SPA.

FRONT ELEVATION VIEW

B|0

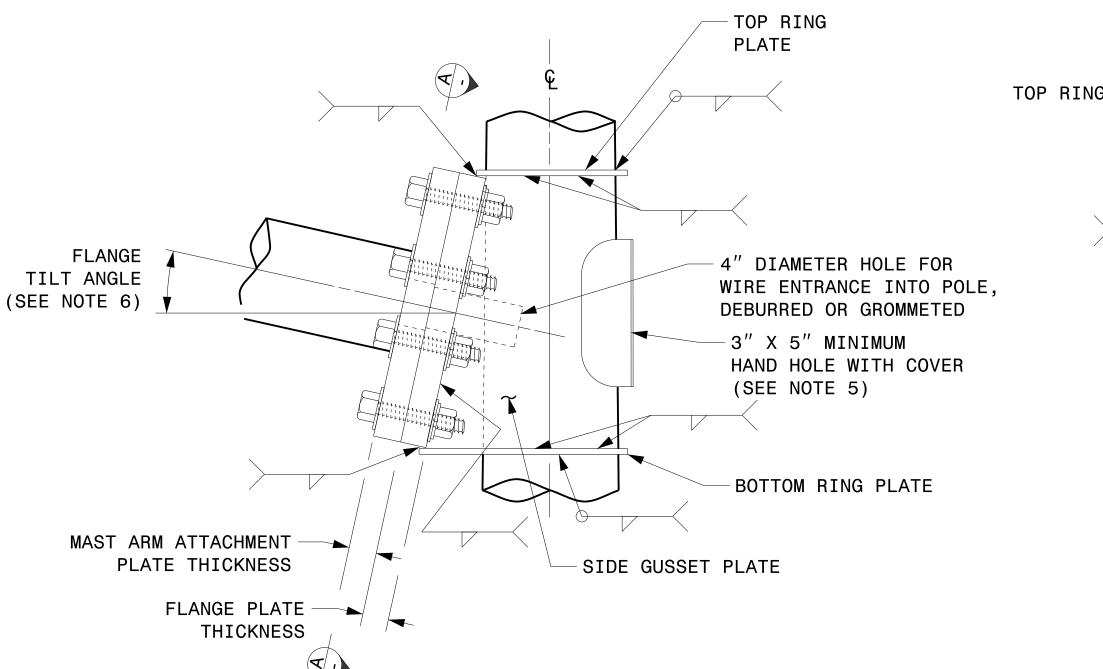
WIRE ENTRANCE INTO POLE, DEBURRED OR GROMMETED



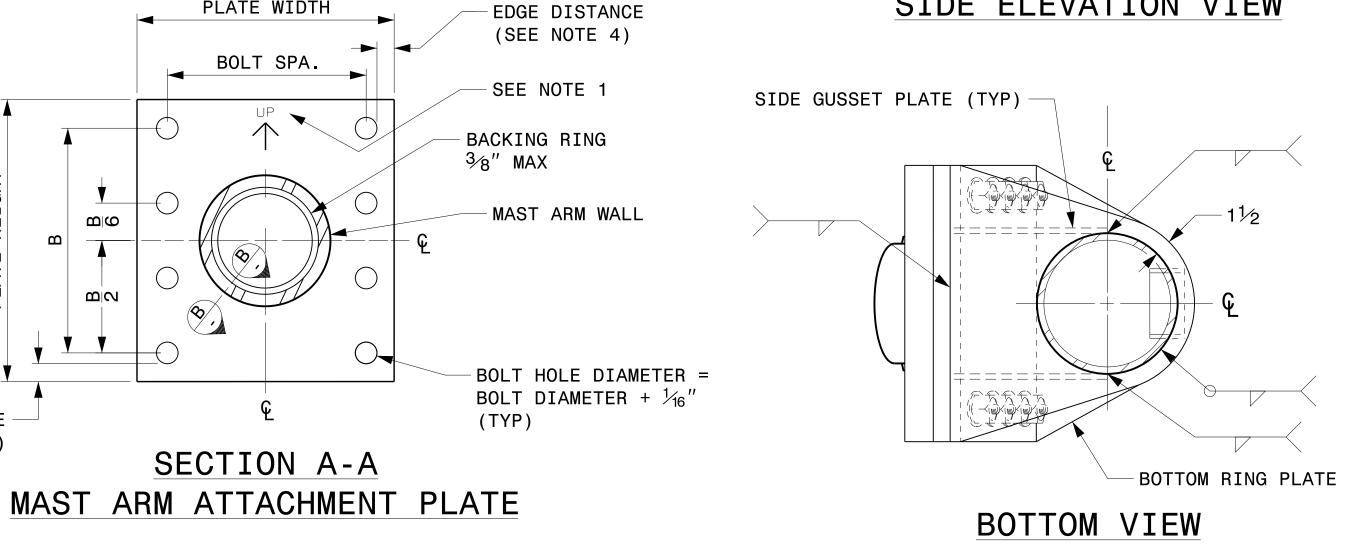
PLAN VIEW

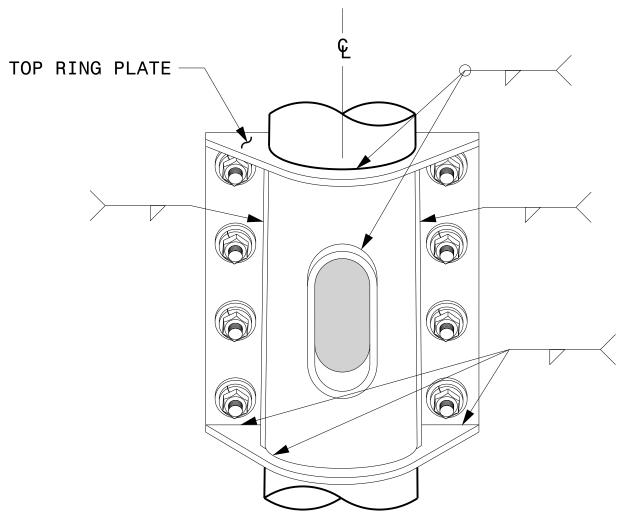
NOTES:

- 1. PROVIDE A PERMANENT MEANS OF IDENTIFICATION ABOVE THE MAST ARM TO INDICATE PROPER ATTACHMENT ORIENTATION OF THE MAST ARM.
- 2. DESIGNER WILL DETERMINE THE SIZE OF ALL STRUCTURAL COMPONENTS, PLATES, FASTENERS, AND WELDS SHOWN UNLESS THEY ARE ALREADY SPECIFIED.
- 3. FABRICATOR IS RESPONSIBLE FOR PROVIDING APPROPRIATE HOLES AT DRAINAGE POINTS TO DRAIN GALVANIZING MATERIALS.
- 4. FOR MINIMUM EDGE DISTANCE AND NOMINAL BOLT HOLE SIZE, FOLLOW THE LATEST AISC STEEL CONSTRUCTION MANUAL.
- 5. PROVIDE UPPER HANDHOLE AS NECESSARY WHEN SHAFT EXTENSIONS ARE REQUIRED FOR LUMINAIRE ARMS OR CAMERA. FOR POLES WITHOUT LUMINAIRES/CAMERA, WIRING CAN BE DONE THROUGH THE TOP OF POLE.
- 6. ALLOWABLE RANGE OF FLANGE TILT ANGLE WILL VARY FROM 0° TO AS REQUIRED.

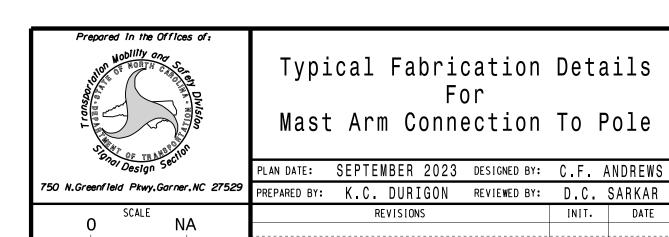


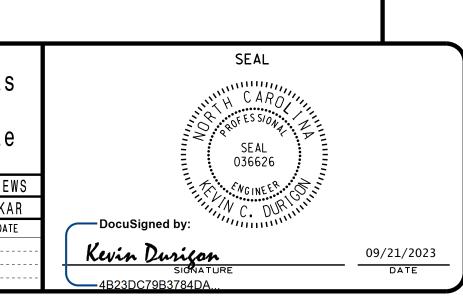
SIDE ELEVATION VIEW





BACK ELEVATION VIEW

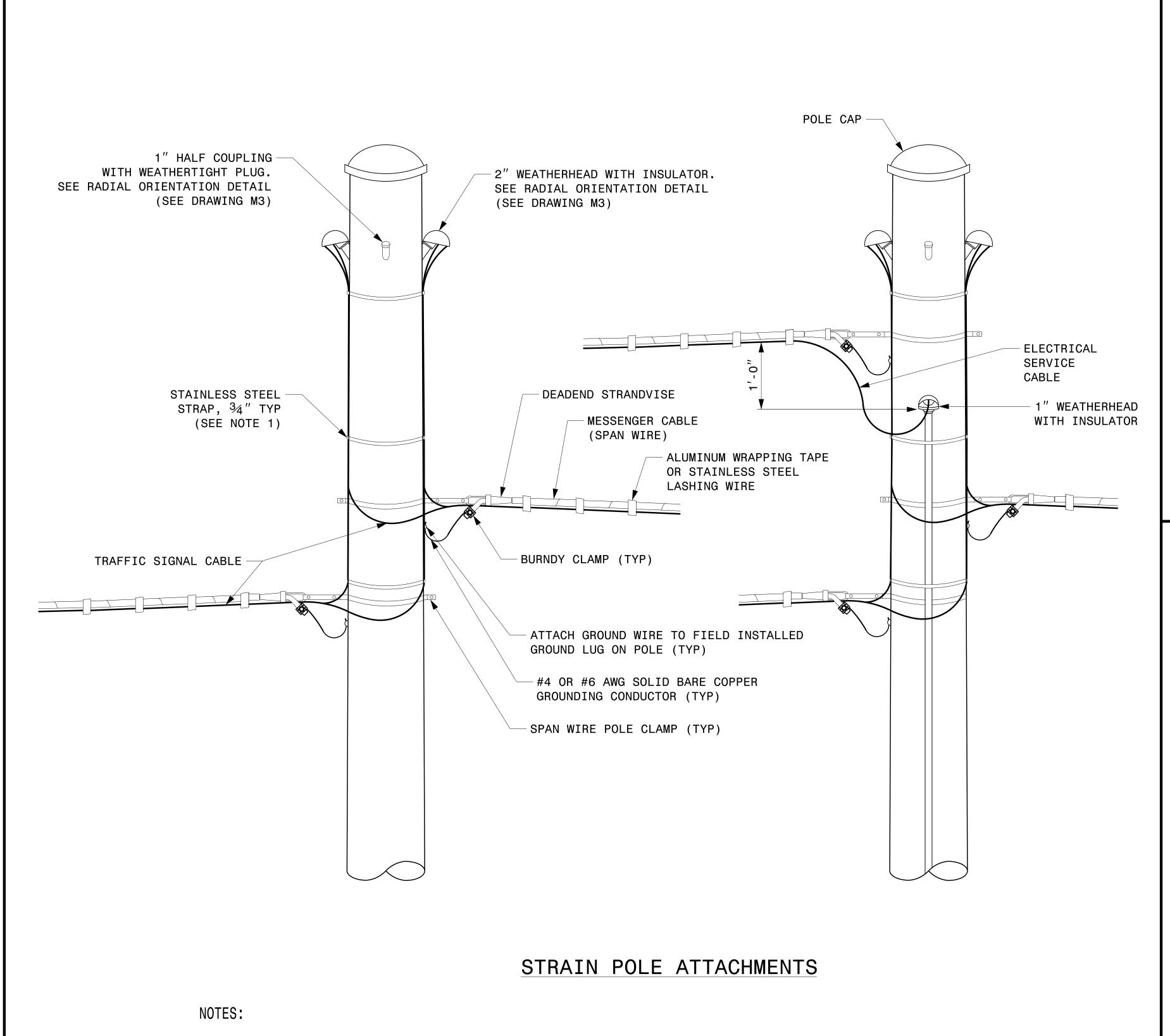




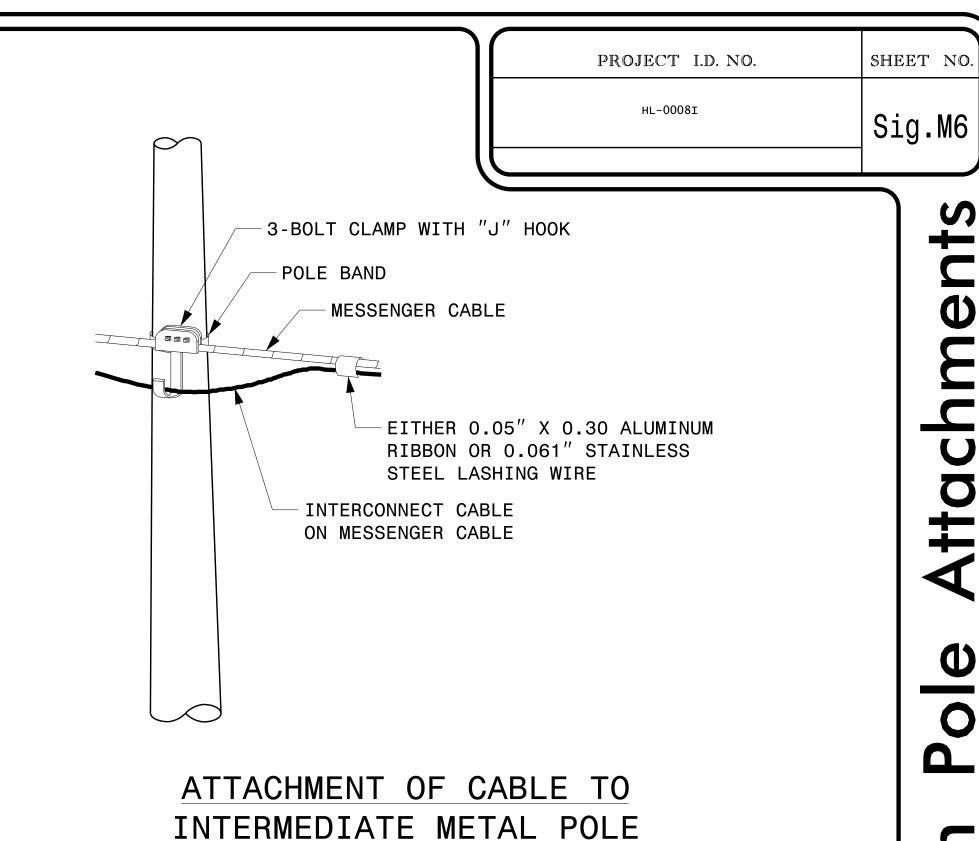
BACKING RING

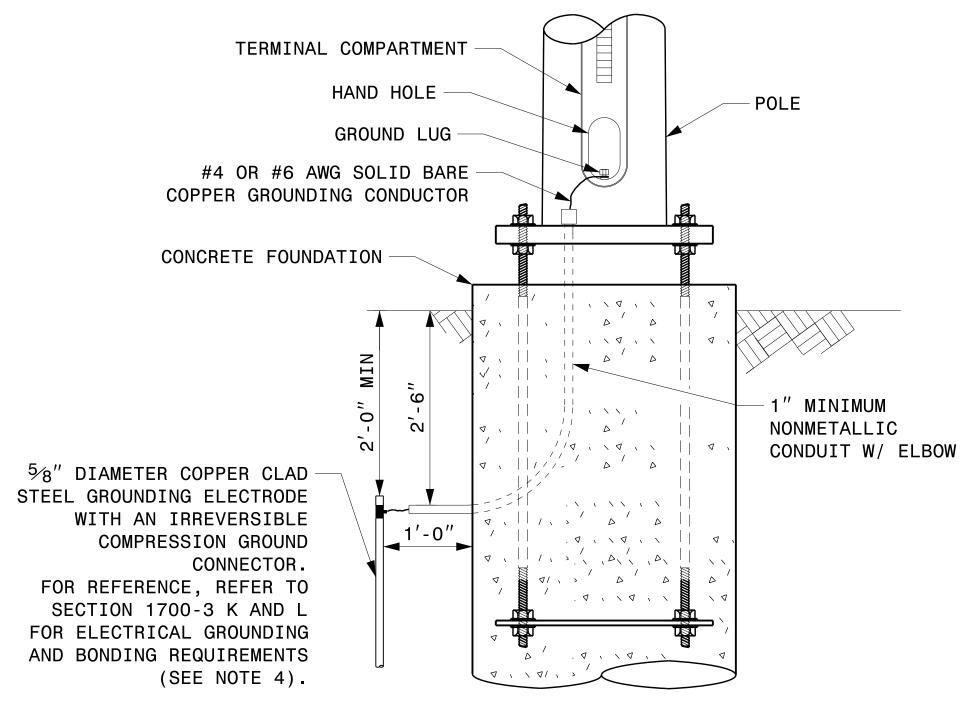
EDGE DISTANCE

(SEE NOTE 4)



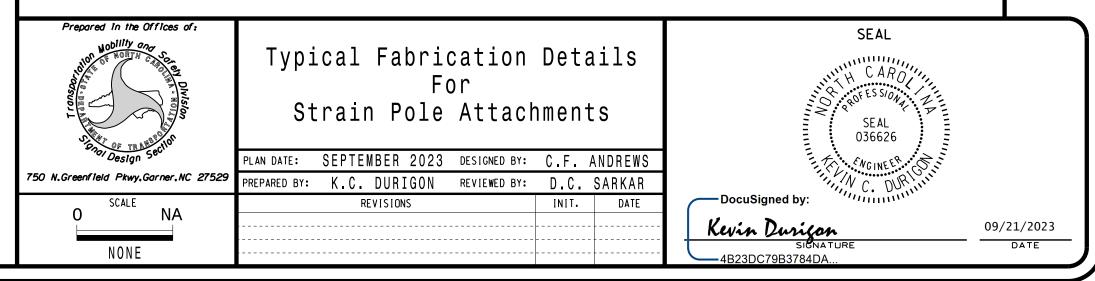
- 1. STRAP ALL SIGNAL CABLES TO THE SIDE OF THE POLE WITH $34^{\prime\prime}$ STAINLESS STEEL STRAPS WHEN THE DISTANCE BETWEEN SPAN WIRE ATTACHMENT CLAMP AND WEATHERHEADS EXCEEDS 3'-0''.
- 2. PROVIDE MINIMUM TWO SPAN WIRE POLE CLAMPS PER POLE.
- 3. IT IS PROHIBITED TO ATTACH TWO SPAN WIRES AT ONE POLE CLAMP.
- 4. FOR GENERAL REQUIREMENTS, REFER TO NCDOT STANDARD SPECIFICATIONS FOR ROADWAY AND STRUCTURES, JANUARY 2024.

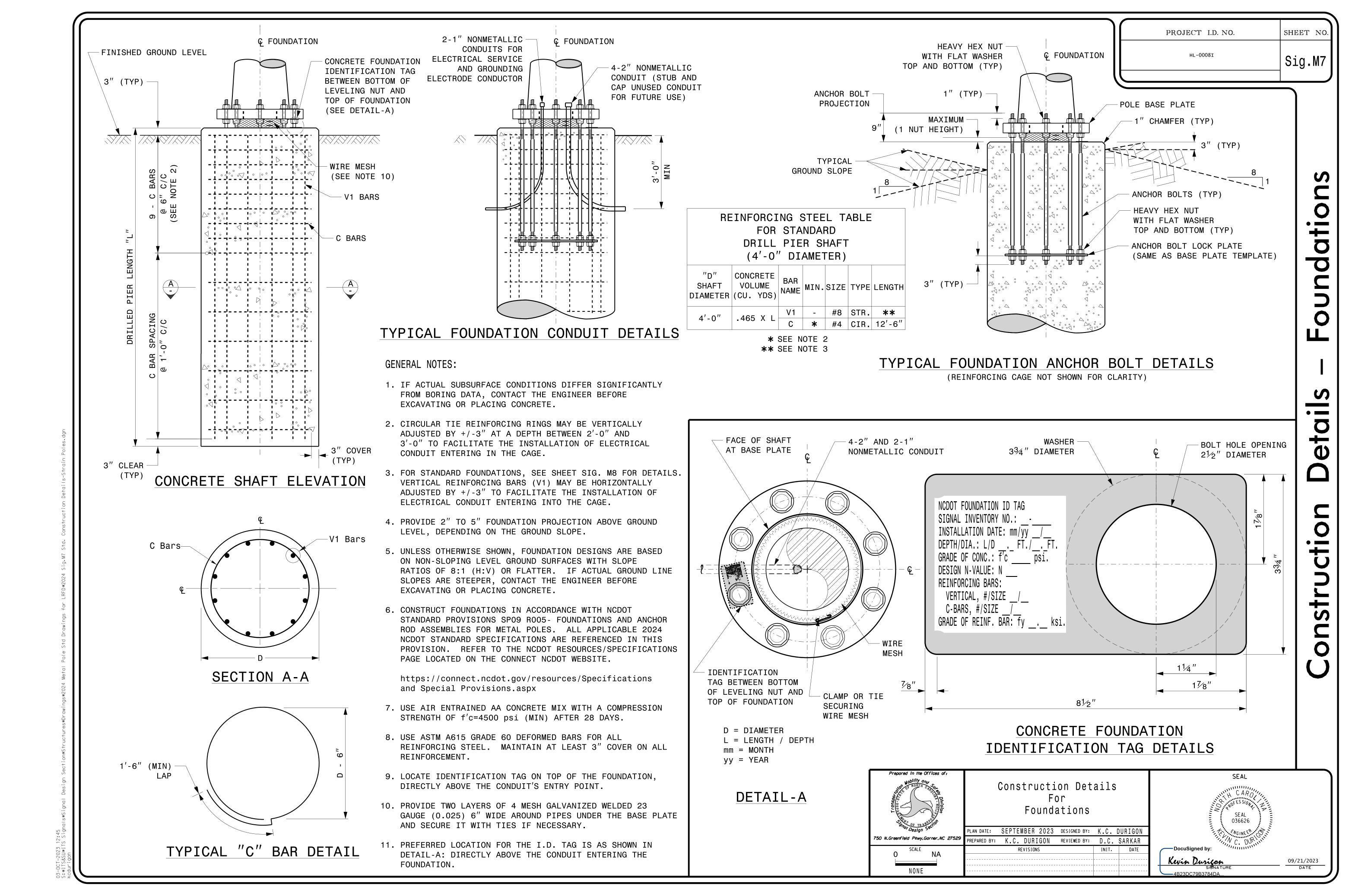




Str

METAL POLE GROUNDING DETAIL FOR STRAIN POLE AND MAST ARM





SOIL CONDITION

	S		ndari n po					NDARD meter Dri						Reinfor	cemen	t
		Base	Reaction	s at the	Pole Base	Clay			Sand		Longitudinal		Stirrups			
Case No.	Pole Height (Ft.)	Plate BC (In.)	Axial (kip)	Shear (kip)	Moment (ft–kip)	Medium N–Value 4–8	Stiff N–Value 9–15	Very Stiff N–Value 16–30		Loose N–Value 4–10	Medium N-Value 11-30	Dense N–Value >30	Bar Size (#)	Quantity (ea.)	Bar Size (#)	Spacing (in.)
S26L1	26	22	2	9	210	19.5	12.5	9	6.5	15.5	14.5	13	8	12	4	12
S26L2	26	23	2	10	240	19.5	12	9	6.5	15.5	14.5	13	8	12	4	12
S26L3	26	25	2	11	260	20.5	12	10	8	16	15	13	8	12	4	12
S30L1	30	22	2	9	230	19	11	9	7	15.5	14	12.5	8	12	4	12
S30L2	30	23	2	10	270	20	12	10	8	16	14.5	13	8	12	4	12
S30L3	30	25	2	11	290	21	12	10	8	17	15	13.5	8	12	4	12
S30H1	30	25	3	13	355	23	13	11	9	18	16.5	14.5	8	12	4	12
S30H2	30	29	3	15	405	25	14	11	9	19	17.5	15.5	8	14	4	12
S30H3	30	29	3	16	430	26	15	12	9	20	18	16	8	14	4	6
S35L1	35	22	3	8	260	19.5	12	10	8	15.5	14.5	13	8	12	4	12
S35L2	35	23	3	10	300	21	12	10	8	16.5	15	13.5	8	12	4	12
S35L3	35	25	3	10	320	21.5	13	10	8	17	15.5	14	8	12	4	12
S35H1	35	25	3	12	390	23.5	14	11	9	18	17	15	8	14	4	12
S35H2	35	29	4	14	460	26	15	12	9	20	18	16	8	14	4	6
S35H3	35	29	4	16	495	28.5	15	13.5	10	21.5	19	17	8	14	4	6

48" DIAMETER FOUNDATION CONCRETE VOLUME (CUBIC YARDS) = (0.465) x DRILLED PIER LENGTH

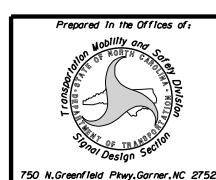
PROJECT I.D. NO. SHEET NO. HL-0008I Sig.M8

GENERAL NOTES:

- 1. VALUES SHOWN IN THE "REACTIONS AT THE POLE BASE" COLUMN REPRESENT THE MINIMUM ACCEPTABLE CAPACITY ALLOWED FOR DESIGN USING A COMBINED FORCE RATIO (CFR) OF 1.00.
- 2. USE CHAIRS AND SPACERS TO MAINTAIN PROPER CLEARANCE.
- 3. FOR FOUNDATION, ALWAYS USE AIR-ENTRAINED CONCRETE MIX.

FOUNDATION SELECTION:

- 1. PERFORM A STANDARD PENETRATION TEST AT EACH PROPOSED FOUNDATION SITE TO DETERMINE "N" VALUE.
- 2. SELECT THE APPROPRIATE WIND ZONE FROM M1 DRAWING.
- 3. SELECT THE SOIL TYPE (CLAY OR SAND) THAT BEST DESCRIBES THE SOIL CHARACTERISTICS.
- 4. GET THE APPROPRIATE STANDARD POLE CASE NUMBER FROM THE PLANS OR FROM THE ENGINEER.
- 5. SELECT THE APPROPRIATE COLUMN UNDER "STANDARD FOUNDATIONS" BASED ON SOIL TYPE AND "N" VALUE. SELECT THE APPROPRIATE ROW BASED ON THE POLE LOAD CASE.
- 6. THE FOUNDATION DEPTH IS THE VALUE SHOWN IN THE "STANDARD FOUNDATIONS" CATEGORY WHERE THE COLUMN AND THE ROW INTERSECT.
- 7. USE CONSTRUCTION PROCEDURES AND DESIGN METHODS PRESCRIBED BY FHWA-NHI-10-016 MANUAL FOR DRILLED SHAFTS.



Standard Strain Pole Foundation for All Soil Conditions

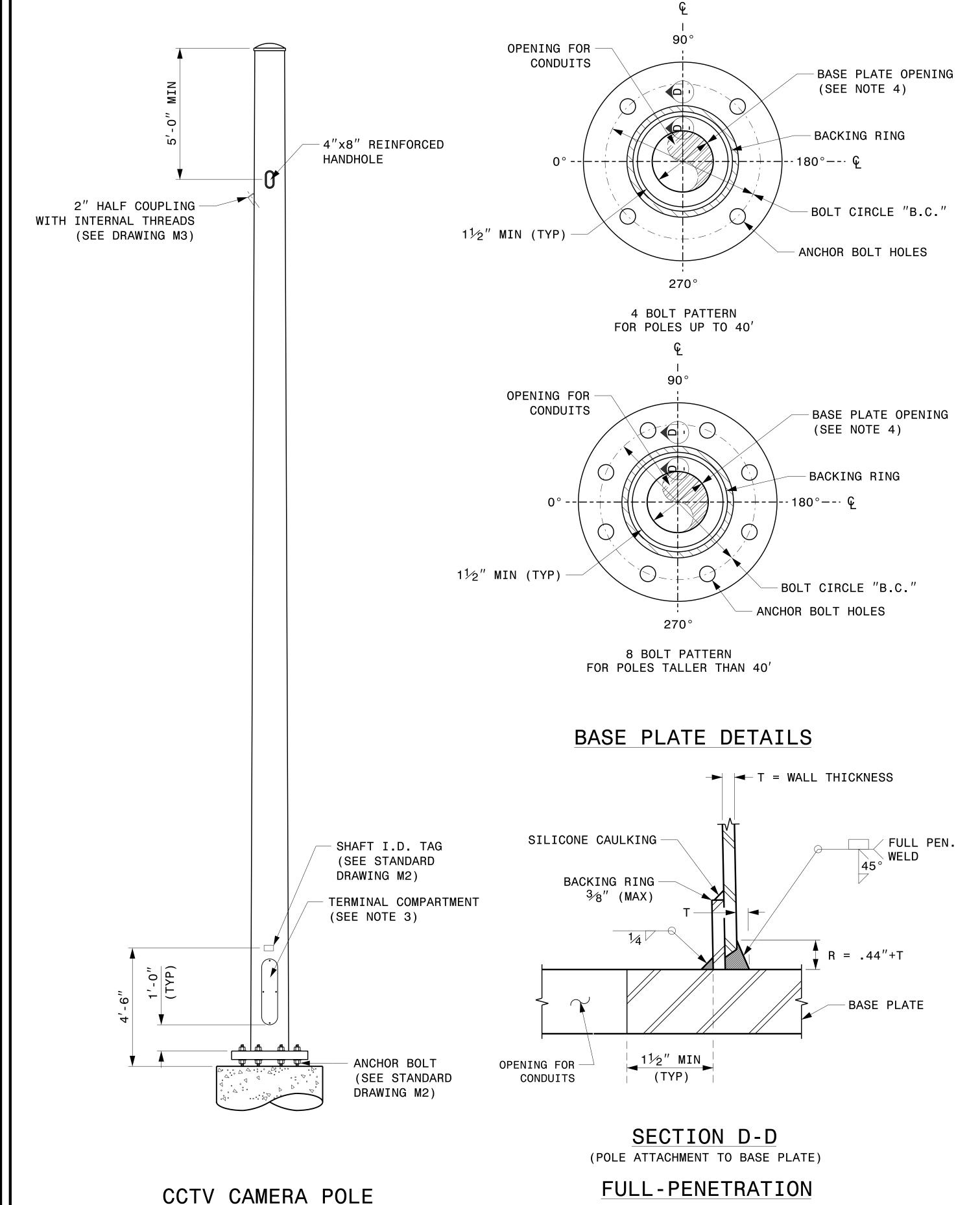
PLAN DATE: SEPTEMBER 2023 DESIGNED BY: K.C. DURIGON PREPARED BY: K.C. DURIGON REVIEWED BY: D.C. SARKAR

Kevin Durison

09/21/2023

NOTES:

- 1. THIS DRAWING PROVIDES BASIC DETAILS FOR CCTV POLES. PROJECT REQUIREMENTS MAY REQUIRE SPECIAL FACTORY PREPS THAT ARE NOT SHOWN ON THESE DETAILS.
- 2. DETAILS FOR INTERNAL CAMERA LOWERING SYSTEMS ARE NOT SHOWN.
- 3. POLE MOUNTED CABINETS MAY REQUIRE MODIFICATIONS TO THE LOWER HANDHOLE OPENING TO MOUNT CABINETS. 4" X 8" REINFORCED HANDHOLES ARE ACCEPTABLE OPTIONS, AND MAY BE PREFERRED.
- 4. OPENING IN POLE BASE SHALL BE EQUAL TO POLE BASE INSIDE DIAMETER MINUS $3\frac{1}{2}$ " BUT SHALL NOT BE LESS THAN $8\frac{1}{2}$ ".
- 5. USE COMPACT SECTION CRITERIA D/T RATIO PER AASHTO LTS-LRFD 1ST EDITION SECTION 5.7.2.



FULL-PENETRATION GROOVE WELD DETAIL 750 N.Greenfield Pkwy.Garner.NC 27529

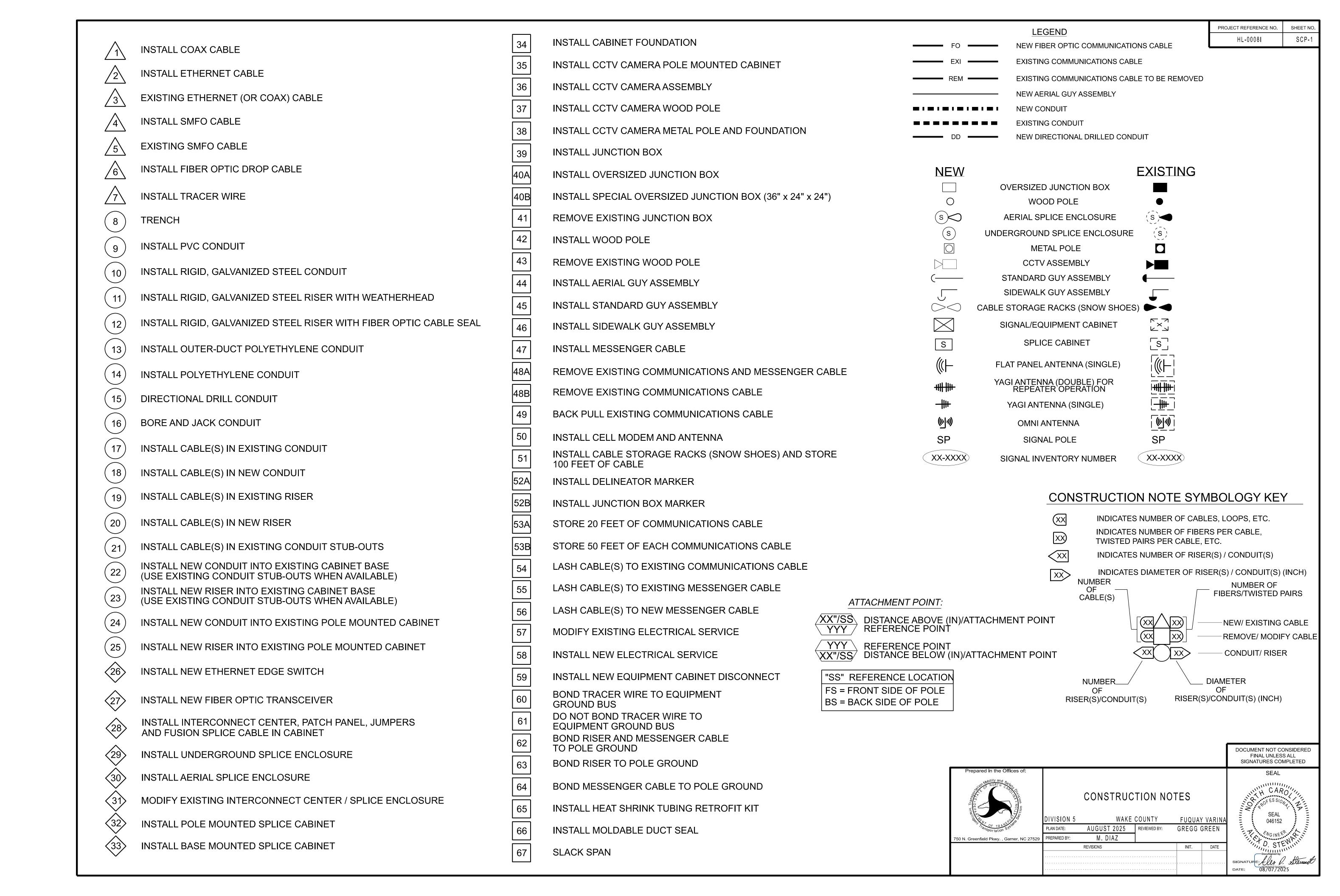
NONE

Typical Fabrication Details For CCTV Poles

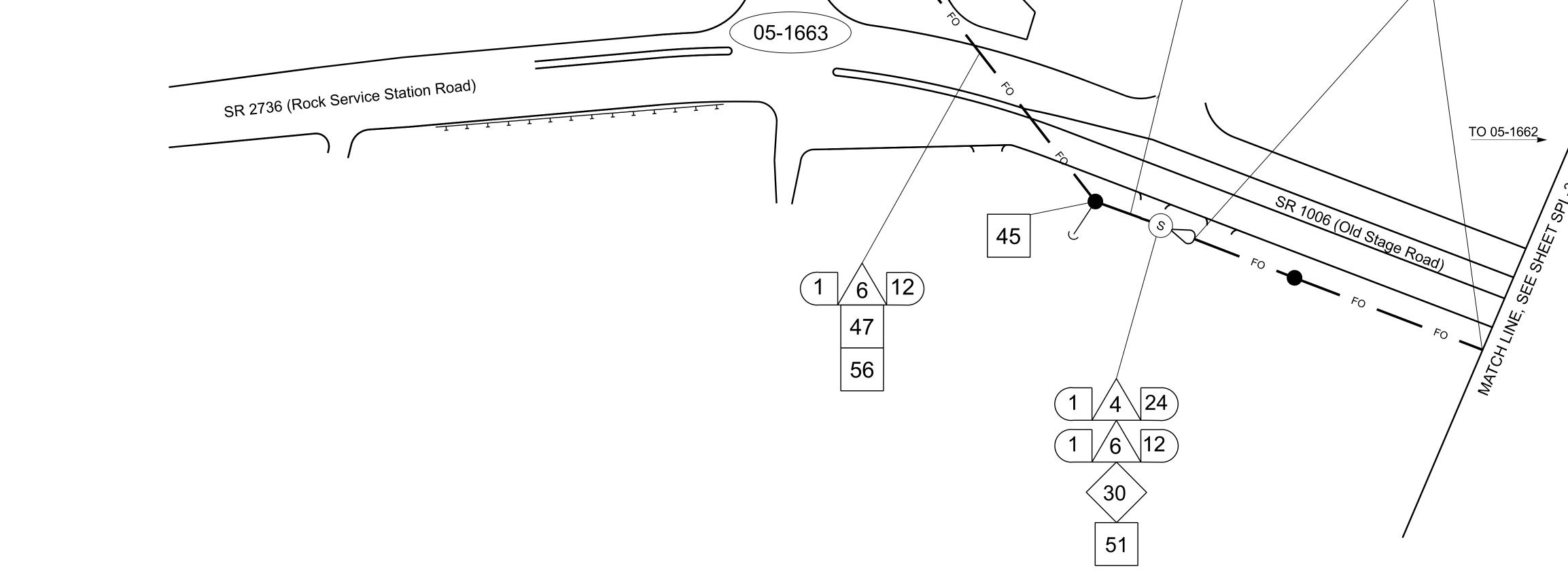
PLAN DATE: SEPTEMBER 2023 DESIGNED BY: K.C. DURIGON PREPARED BY: K.C. DURIGON REVIEWED BY: C.F. ANDREWS

SEAL Kevin Durison
SIGNATURE 09/21/2023

(NOT TO SCALE)

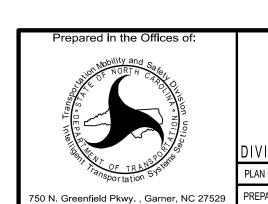


1 6 12 22 19 65 12/75 SIGNAL CABLE 56 33A 66



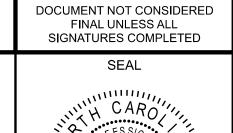
1. FIVE (5) DAYS PRIOR TO BEGINNING WORK ON THE SIGNAL SYSTEM, CONTACT THE DIVISION 5 TRAFFIC ENGINEER AT (919) 536-4000 TO ARRANGE FOR THE TOWN TO PROGRAM THE NEW FIELD ETHERNET SWITCHES WITH THE NECESSARY NETWORK CONFIGURATION DATA, INCLUDING BUT NOT LIMITED TO: THE PROJECT IP ADDRESS, DEFAULT GATEWAY, SUBNET MASK AND VLAN ID INFORMATION. NOTIFY THE DIVISION 5 TRAFFIC ENGINEER AFTER ALL WORK IS PERFORMED TO ENSURE THAT ALL FIBER CIRCUITS ARE FUNCTIONING PROPERLY. WORK IS NOT COMPLETE UNTIL THE SIGNALSYSTEM IS BACK UP AND OPERATIONAL.

2. ALL NCDOT ATTACHMENTS ARE 64" BELOW POWER, FRONT SIDE OF POLE, UNLESS OTHERWISE NOTED.

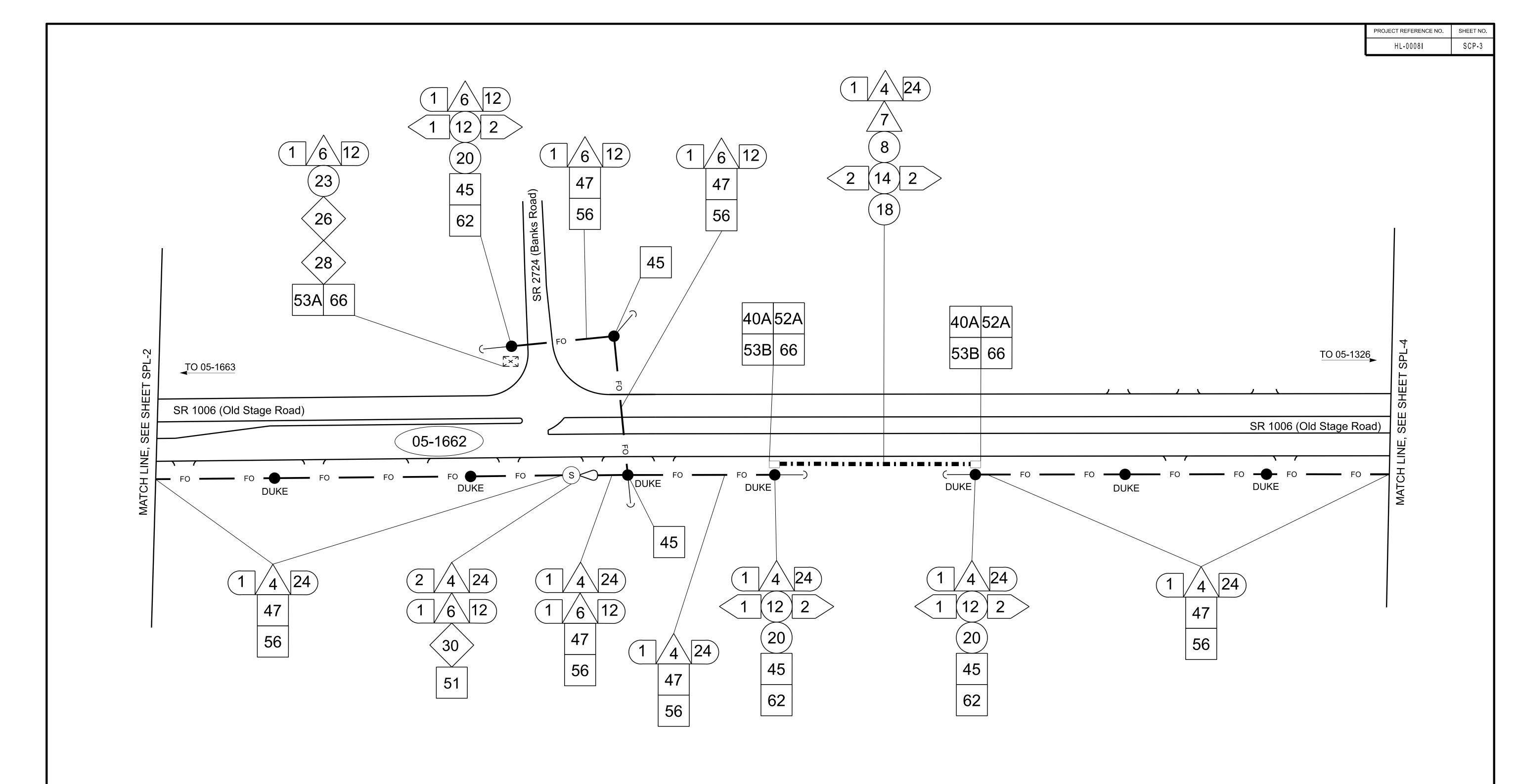


D05-17 Fuquay-Varina COMMUNICATIONS AND CABLE ROUTING PLANS

OF TRANSPORTATION STATES				DIVISION 5	WAKE C	COUNTY	FUQUAY	′ VARINA
				PLAN DATE:	AUGUST 2025	GREGG GREEN		
eenfield Pkwy. , Garner, NC 27529				PREPARED BY:	M. DIAZ			
$\overline{\ }$	_	SCALE			REVISIONS		INIT.	DATE
	0		N/A					

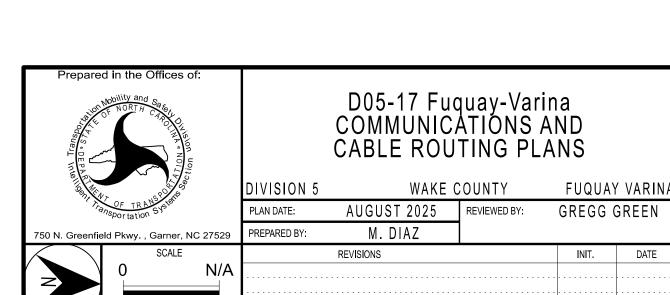






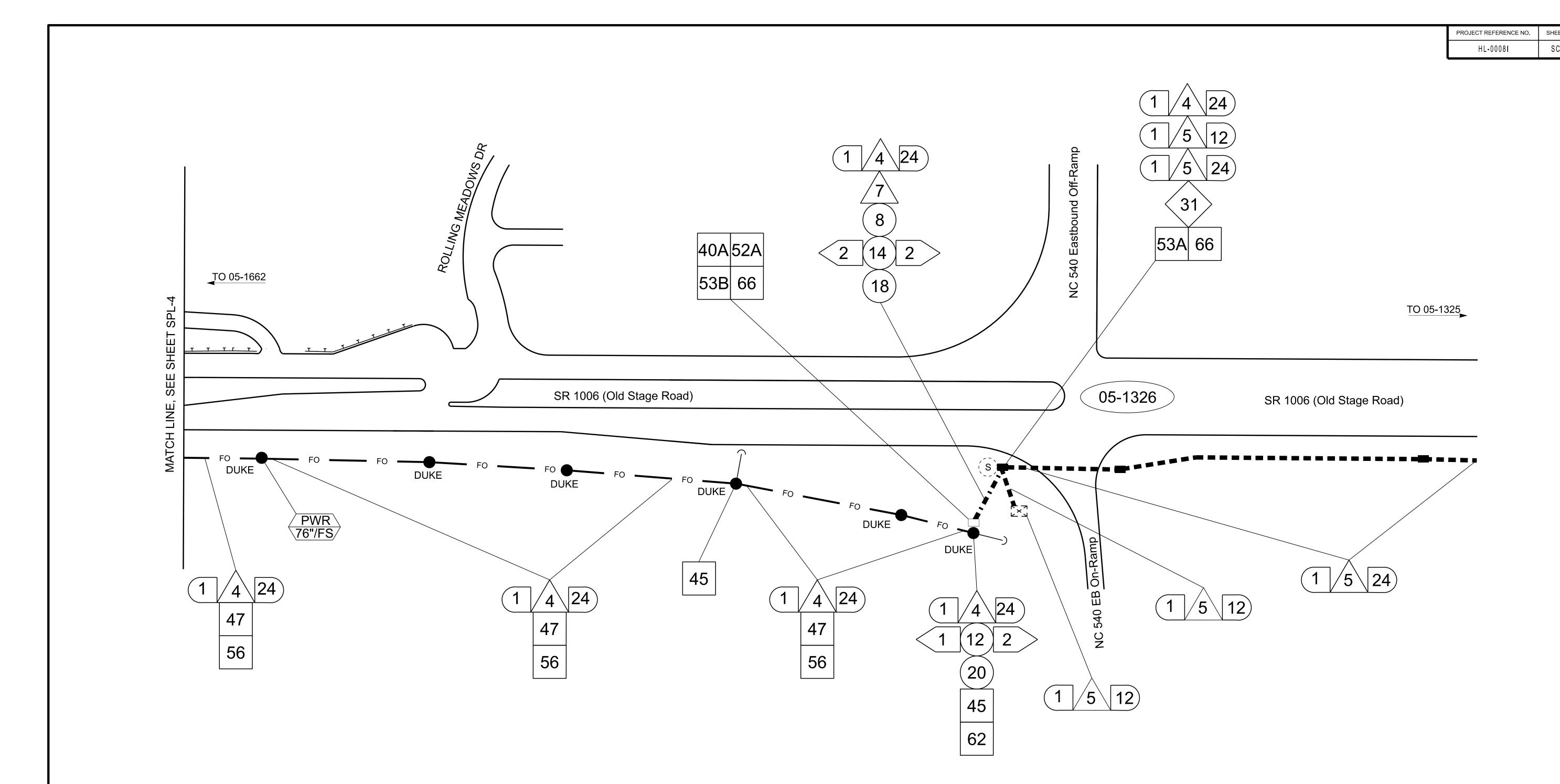
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2. ALL NCDOT ATTACHMENTS ARE 64" BELOW POWER, FRONT SIDE OF POLE, UNLESS OTHERWISE NOTED.



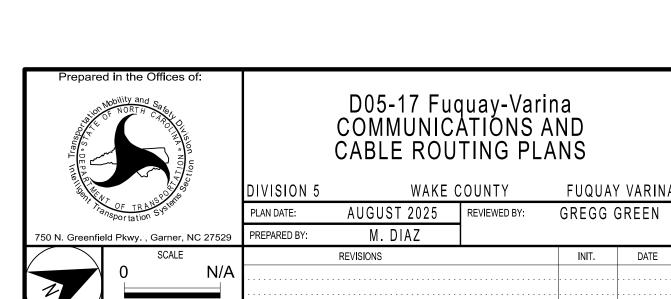
DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

PROJECT REFERENCE NO. 56 TO 05-1662 DUKE TO 05-1326 SR 1006 (Old Stage Road) SR 1006 (Old Stage Road) DUKE DUKE DUKE DÜKE 45 51 45 $\sqrt{4}$ 24 56 56 1. FIVE (5) DAYS PRIOR TO BEGINNING WORK ON THE SIGNAL SYSTEM, CONTACT THE DIVISION 5 TRAFFIC ENGINEER AT (919) 536-4000 TO ARRANGE FOR THE TOWN TO PROGRAM THE NEW FIELD ETHERNET DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SWITCHES WITH THE NECESSARY NETWORK CONFIGURATION DATA, INCLUDING BUT NOT LIMITED TO: THE PROJECT IP ADDRESS, DEFAULT GATEWAY, SUBNET MASK AND VLAN ID INFORMATION. NOTIFY THE D05-17 Fuquay-Varina COMMUNICATIONS AND CABLE ROUTING PLANS DIVISION 5 TRAFFIC ENGINEER AFTER ALL WORK IS PERFORMED TO ENSURE THAT ALL FIBER CIRCUITS ARE FUNCTIONING PROPERLY. WORK IS NOT COMPLETE UNTIL THE SIGNALSYSTEM IS BACK UP AND OPERATIONAL. WAKE COUNTY FUQUAY VARINA AUGUST 2025 REVIEWED BY: GREGG GREEN 2. ALL NCDOT ATTACHMENTS ARE 64" BELOW POWER, FRONT SIDE OF POLE, UNLESS OTHERWISE NOTED. M. DIAZ



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DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

PROJECT REFERENCE NO. SHEET NO.

HL-0008I SCP-6

AERIAL SPLICE ENCLOSURE

SR 1006/OLD STAGE RD AT

SR 2736/ROCK SERVICE STATION RD

SIG. INV. # 05–1663

Notes:

Unused fibers left coiled and stored in splice tray.
Unused Buffer Tubes left coiled and stored in splice tray.

COLOR CODE TIA/EIA 598-A

(1) BLUE (7) RED

(2) ORANGE (8) BLACK

(3) GREEN (9) YELLOW (4) BROWN (10) VIOLET

(4) BROWN (10) VIOLET(5) SLATE (11) ROSE

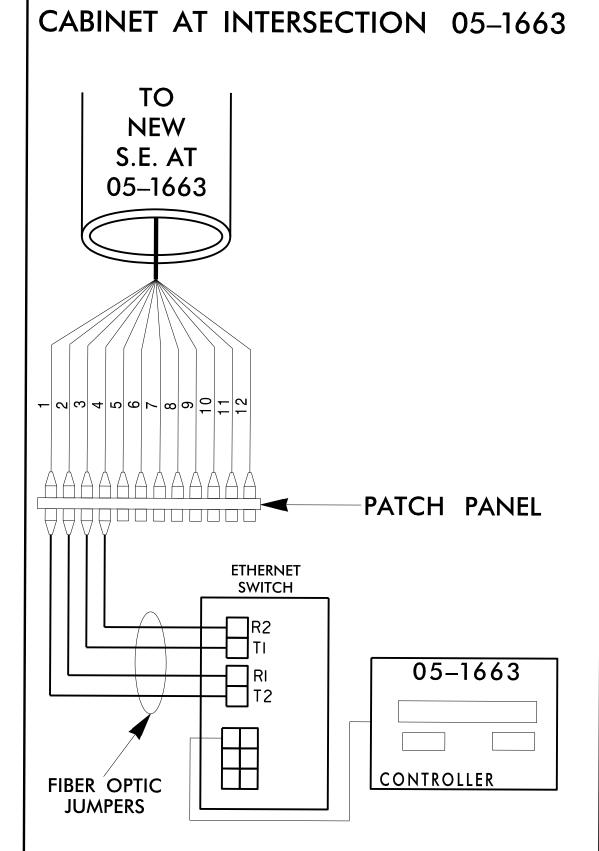
(6) WHITE (12) AQUA

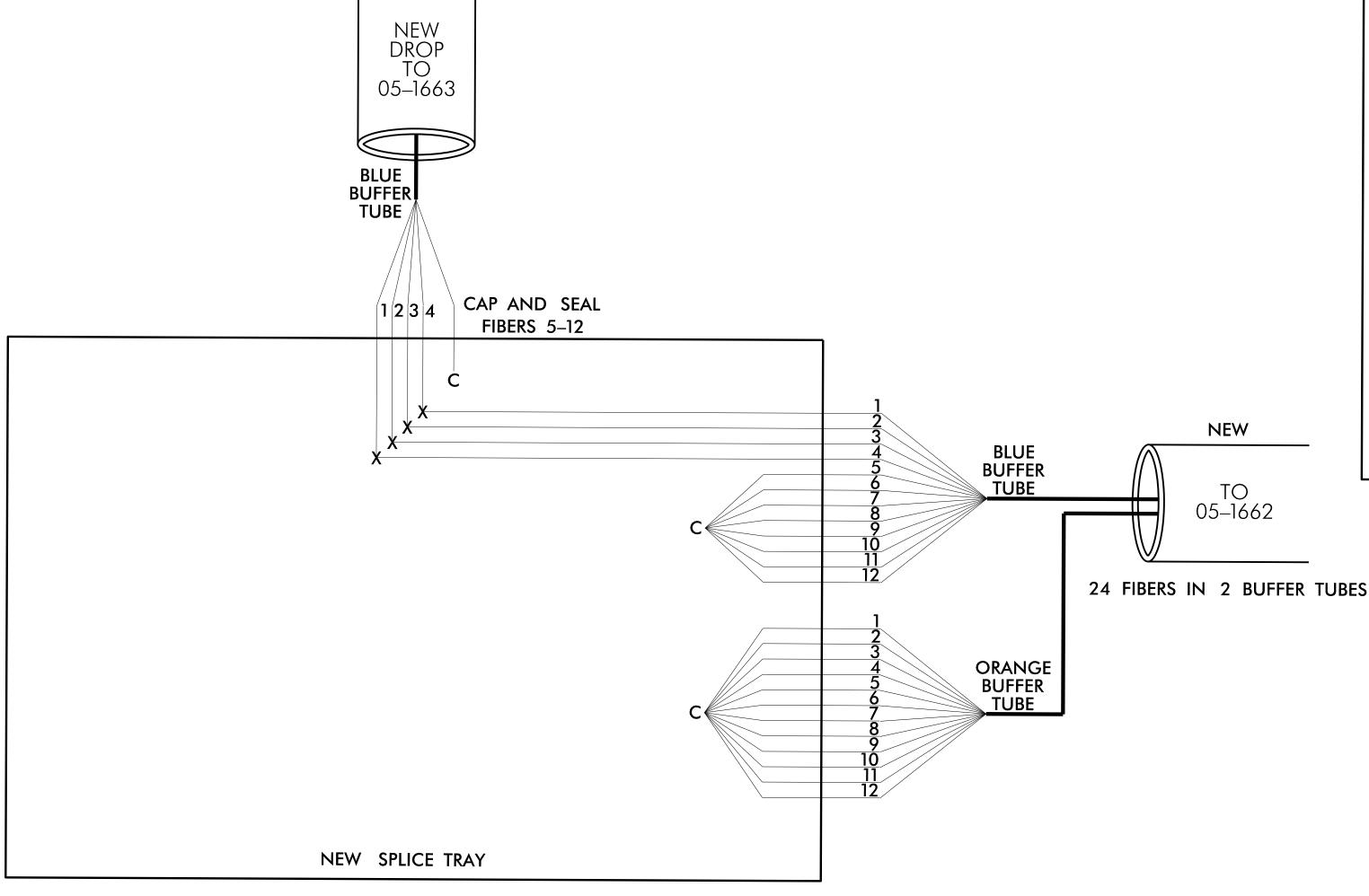
<u>LEGEND</u>

X = FUSION SPLICE

= EXISTING SPLICE

CAP IN TRAY





- 1. FIVE (5) DAYS PRIOR TO BEGINNING WORK ON THE SIGNAL SYSTEM, CONTACT THE DIVISION 5 TRAFFIC ENGINEER AT 919-536-4000 TO ARRANGE FOR THE TOWN TO PROGRAM THE NEW FIELD ETHERNET SWITCHES WITH THE NECESSARY NETWORK CONFIGURATION DATA, INCLUDING BUT NOT LIMITED TO: THE PROJECT IP ADDRESS, DEFAULT GATEWAY, SUBNET MASK AND VLAN ID INFORMATION. NOTIFY THE TRAFFIC ENGINEER AFTER ALL WORK IS PERFORMED TO ENSURE THAT ALL FIBER CIRCUITS ARE FUNCTIONING PROPERLY. WORK IS NOT COMPLETE UNTIL THE SIGNAL SYSTEM IS BACK UP AND OPERATIONAL
- 2. CONTRACTOR TO RECORD EXISTING SPLICE ARRANGEMENT FOR COMPARISON TO THE SUPPLIED SPLICE DETAILS. IF DISCREPANCIES EXIST, CONTACT THE ENGINEER TO DETERMINE HOW TO PROCEED WITH RESPLICING. PROVIDE AS-BUILT PLANS TO THE ENGINEER IF FINAL SPLICE ARRANGEMENT DIFFERS FROM THE SUPPLIED SPLICE DETAILS.
- 3. ETHERNET SWITCH TERMINATION CONFIGURATIONS ARE GENERIC. CONTRACTOR IS RESPONSIBLE FOR DETERMINING \\ ENSURING PROPER TERMINATIONS.
- 4. INCLUDE ON THE COVER OF EACH SPLICE TRAY THE FOLLOWING: REFERENCE SECTION 1731 "FIBER OPTIC SPLICE ENCLOSURE"
- 1) SPLICE LOCATION
- 2) DATE
- 3) COMPANY NAME
- 4) NAME OF INDIVIDUAL PERFORMING THE SPLICING

PRIOR TO INSTALLING THE COVER ON THE SPLICE TRAY TAKE A DIGITAL PHOTOGRAPH SHOWING THE SPLICE TRAY AND INFORMATION SHOWN ABOVE (1-4) AND SUBMIT PHOTOGRAPH ALONG WITH OTDR TEST RESULTS.

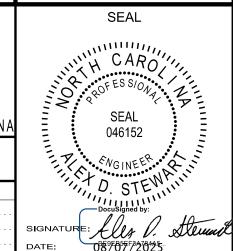
Prepared in the Offices of:

| Description | Prepared |

SPLICING DETAIL D05-17 Fuquay-Varina

DIVISION 5 WAKE COUNTY FUQUAY VARINA
PLAN DATE: AUGUST 2025 REVIEWED BY: GREGG GREEN
PREPARED BY: M. DIAZ

REVISIONS INIT. DATE



DOCUMENT NOT CONSIDERED

FINAL UNLESS ALL SIGNATURES COMPLETED

PROJECT REFERENCE NO.

PATCH PANEL

05-1662

CONTROLLER

CABINET AT INTERSECTION 05-1662

TO

NEW

S.E. AT

05–1662

FIBER OPTIC

JUMPERS

HHHHHHHHHH-

AERIAL SPLICE ENCLOSURE SR 1006/OLD STAGE RD AT SR 2724 BANKS RD SIG. INV. # 05–1662

Notes:

Unused fibers left coiled and stored in splice tray. Unused Buffer Tubes left coiled and stored in splice tray.

COLOR CODE TIA/EIA 598-A

- (1) BLUE (7) RED
- (2) ORANGE (8) BLACK (3) GREEN (9) YELLOW
- (4) BROWN (10) VIOLET
- (5) SLATE (11) ROSE (6) WHITE (12) AQUA

LEGEND

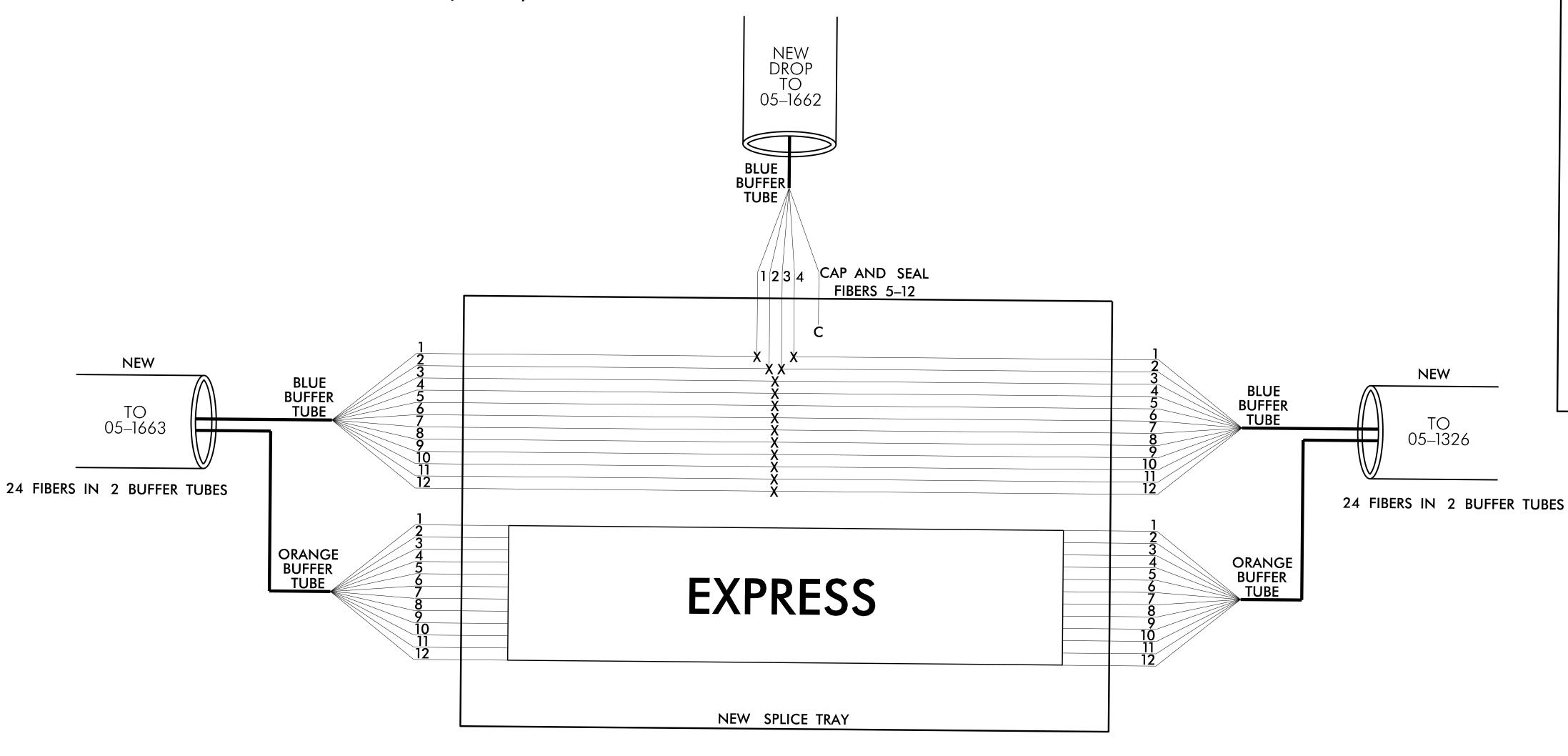
X = FUSION SPLICE

EXISTING SPLICE

C = CAP IN TRAY

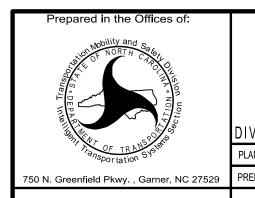
EXPRESS ALL FIBERS/ **EXPRESS BUFFER TUBES**

SPLICE = SPLICE ALL FIBERS/ **BUFFER TUBES**



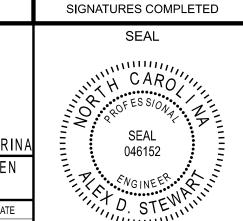
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SPLICING DETAIL D05-17 Fuquay-Varina

DIVISION 5 WAKE COUNTY FUQUAY VARINA GREGG GREEN AUGUST 2025 REVIEWED BY: PREPARED BY: M. DIAZ REVISIONS INIT. DATE SIGNATURE: Ally V. Allumit



DOCUMENT NOT CONSIDERED

FINAL UNLESS ALL

PROJECT REFERENCE NO.

-PATCH PANEL

05-1326

CONTROLLER

UNDERGROUND SPLICE ENCLOSURE SR 1006/OLD STAGE RD AT NC 540 EASTBOUND RAMPS SIG. INV. # 05–1326

Notes:

NEW

TO 05–1662

24 FIBERS IN 2 BUFFER TUBES

Unused fibers left coiled and stored in splice tray. Unused Buffer Tubes left coiled and stored in splice tray.

BLUE BUFFER

TUBE

ORANGE

BUFFER

TUBE

COLOR CODE TIA/EIA 598-A

(1) BLUE (7) RED

(2) ORANGE (8) BLACK

(3) GREEN (9) YELLOW (4) BROWN (10) VIOLET

(5) SLATE (11) ROSE

> **EXISTING** CAP AND SEALED FIBERS 5-12

(6) WHITE (12) AQUA

EXISTING DROP TO

05–1326

BLUE BUFFER TUBE

EXISTING SPLICE TRAY

CABINET AT INTERSECTION 05–1326 C = CAP IN TRAYTO **EXISTING** S.E. AT 05–1326 **ETHERNET EXISTING** FIBER OPTIC BLUE BUFFER TUBE **JUMPERS**

LEGEND

X = FUSION SPLICE

E = EXISTING SPLICE

ORANGE

BUFFER TUBE

TO 05–1325 24 FIBERS IN 2 BUFFER TUBES

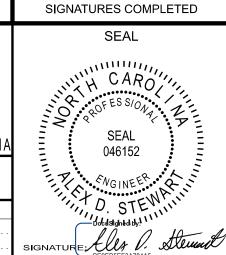
- 1. FIVE (5) DAYS PRIOR TO BEGINNING WORK ON THE SIGNAL SYSTEM, CONTACT THE DIVISION 5 TRAFFIC ENGINEER AT 919-536-4000 TO ARRANGE FOR THE TOWN TO PROGRAM THE NEW FIELD ETHERNET SWITCHES WITH THE NECESSARY NETWORK CONFIGURATION DATA, INCLUDING BUT NOT LIMITED TO: THE PROJECT IP ADDRESS, DEFAULT GATEWAY, SUBNET MASK AND VLAN ID INFORMATION. NOTIFY THE TRAFFIC ENGINEER AFTER ALL WORK IS PERFORMED TO ENSURE THAT ALL FIBER CIRCUITS ARE FUNCTIONING PROPERLY. WORK IS NOT COMPLETE UNTIL THE SIGNAL SYSTEM IS BACK UP AND OPERATIONAL
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SPLICING DETAIL D05-17 Fuquay-Varina

DIVISION 5 WAKE COUNTY FUQUAY VARINA GREGG GREEN AUGUST 2025 REVIEWED BY: PREPARED BY: M. DIAZ REVISIONS INIT. DATE



DOCUMENT NOT CONSIDERED

FINAL UNLESS ALL