

22-AUG-2025 10:31
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Fernando.Vazquez AT CHA-FAZ01EJZ

OUTPUT CHANNEL CONFIGURATION

Front Panel
Main Menu >Controller >More>Channels>Channels Config

Web Interface
Home >Controller >Advanced IO>Channels>Channel Configuration

Channel Configuration

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Phase Vehicle	1		X	X	1
2	Phase Vehicle	2		X		2
3	Overlap	7		X	X	3
4	Phase Vehicle	4		X		4
5	Phase Vehicle	5		X		5
6	Phase Vehicle	6		X	X	6
7	Phase Vehicle	7		X		7
8	Phase Vehicle	8		X	X	8
9	Overlap	1		X	X	9
10	Overlap	2		X	X	10
11	Overlap	3		X		11
12	Overlap	4		X		12
13	Phase Ped	2				13
14	Phase Ped	4				14
15	Phase Ped	6				15
16	Phase Ped	8				16
17	Overlap	5		X	X	17
18	Overlap	6		X		18

NOTICE OVERLAP 7
ASSIGNED TO CHANNEL 3



NOTICE FLASH RED



MAXTIME STARTUP AND SOFTWARE FLASH
PROGRAMMING DETAIL

Front Panel
Main Menu >Controller >Unit

Web Interface
Home >Controller >Unit

Modify parameters as shown below and save changes.

Start Up Parameters

StartUp Clearance Hold
6

Unit Flash Parameters

All Red Flash Exit Time
6

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE
SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

OVERLAP PROGRAMMING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	2	3	4	7
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal
Included Phases	8	6	4	1
Modifier Phases	1	-	-	-
Modifier Overlaps	-	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during
Ped Clearance Interval. Consult Ped Signal Module user's manual
for instructions on selecting this feature.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 14-0727T2
DESIGNED: Aug 2025
SEALED: 8/26/2025
REVISED: N/A

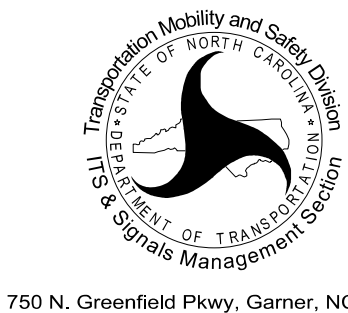
Electrical Detail - Sheet 1 of 2
Temporary Design 2 - TMP Ph2, S1

DOCUMENT NOT CONSIDERED
FINAL UNLESS ALL
SIGNATURES COMPLETED

Plans Prepared By:



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SR 1438 (Dillardtown Road)

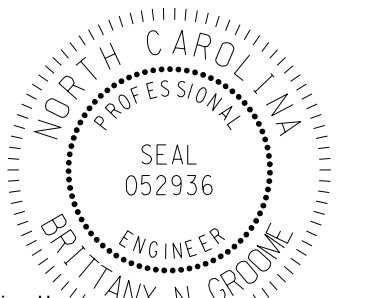
Division 14 Jackson County Sylva

PLAN DATE: August 2025 REVIEWED BY: ZM Esposito

PREPARED BY: DJ White REVIEWED BY: BN Groome

REVISIONS INIT. DATE

SEAL



SEAL
052936
ENGINEER
BRITTANY N. GROOME

Signed by: Brittany Groome 8/26/2025

DATE

SIG. INVENTORY NO. 14-0727T2