R-5600 Sig-33.3

MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

_AN

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases

for heads 11 and 51 to run protected turns only.

VEH DET PLAN 2: Disables phase 6 call on loop 1A

and reduces delay time for phase 1 call on loop 1A to 0 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	1	2	3	4	7	8
Туре	FYA 4 - Section	Normal	Normal			
Included Phases	-	6	-	-	4	4,5
Modifier Phases	1	4	5	4,5	-	-
Modifier Overlaps	-	-	-	-	-	-
Trail Green	0	0	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0	0.0	0.0
FYA PED Delay	0.0	0.0	0.0	7.0	0.0	0.0

NOTICE INCLUDED PHASE

MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 1A & 5A

Front Panel

Main Menu >Controller >Detector >Veh Det Plans

Web Interface

Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

1A

Detector	Call Phase	Delay
1	1	0.0
29	0	÷

Detector	Call Phase	Delay
15	5	0.0
31	0	<u>-</u>

MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Coordination >Patterns

Web Interface

Home >Controller >Coordination >Patterns

Pattern Parameters

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Pattern	Veh Det Plan	Overlap Plan			
*	2	2			

*The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 14-0411T3

DESIGNED: Aug 2025 SEALED: 8/26/2025 REVISED: N/A

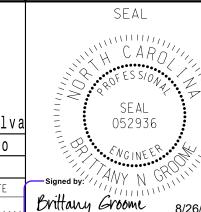
Electrical Detail - Sheet 3 of 3 Temporary Design 3 - TMP Ph1, S2, Part 2

NC 107 (E Main Street)

NC 116 (Webster Road) Alexander Street

Jackson County Division 14

Sylva PLAN DATE: August 2025 REVIEWED BY: ZM Esposito BN Groome PREPARED BY: DS Griffith REVIEWED BY: REVISIONS



Brittany Groome 8/26/2025

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL

SIGNATURES COMPLETED

