MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel Main Menu >Controller >Unit

Web Interface Home >Controller >Unit

Modify parameters as shown below and save changes.

Start Up Parameters

StartUp Clearance Hold

Unit Flash Parameters All Red Flash Exit Time

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	4	7	8
Type	FYA 4 - Section	Normal	Normal			
Included Phases	2	6	6	4	4	5
Modifier Phases	1	4	5	5	-	-
Modifier Overlap	-	-	-	-	-	-
Trail Green	0	0	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0	0.0	0.0
FYA PED Delay	0.0	0.0	0.0	7.0	0.0	0.0

OUTPUT CHANNEL CONFIGURATION

Front Panel

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channel Configuration

Channel Configuration

	Channel	Control Type	Control Source Flash Yellow	Flash Red	Flash Alt	MMU Channel
	1	Phase Vehicle	1	Χ	X	1
NOTICE OVERLAP 7	2	Phase Vehicle	2	X	X	2
ASSIGNED TO CHANNEL 3	3	Overlap	7	X	X	3
·	4	Phase Vehicle	4	Χ	X	4
	5	Phase Vehicle	5	X	X	5
NOTICE OVERLAP 8	6	Phase Vehicle	6	X	X	6
ASSIGNED TO CHANNEL 7	7	Overlap	8	Χ		7
NOTICE PHASE VEHICLE 3	8	Phase Vehicle	3	Χ		8
ASSIGNED TO CHANNEL 8	9	Overlap	1	X	X	9
	10	Overlap	2	X	X	10
	11	Overlap	3	X	X	11
	12	Overlap	4	X		12
	13	Phase Ped	2			13
	14	Phase Ped	4			14
	15	Phase Ped	6			15
	16	Phase Ped	8			16
	17	Overlap	5	Χ	X	17
	18	Overlap	6	X		18

NOTICE FLASHING RED

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 14-0411T2

DESIGNED: Aug 2025 SEALED: 8/26/2025 REVISED: N/A

Electrical Detail - Sheet 2 of 3

Temporary Design 2 - TMP Ph1, S2, Part 1 ELECTRICAL AND PROGRAMMING NC 107 (E Main Street)

REVISIONS

SR 116 (Webster Road) Alexander Street

Division 14 Jackson County PLAN DATE: August 2025 REVIEWED BY: ZM Esposito PREPARED BY: DS Griffith REVIEWED BY: BN Groome

INIT. DATE Brittany Groome

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL

SIGNATURES COMPLETED

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