

21-AUG-2025 1:24:31
Pw:\\pwdr\\jesg01-h08_115_Est+_01\\Documents\\NCDDT\\NCDDT\\Western\\D1\\V-Dn-Cai_ML\\NCDDT_16170_05600_NC170_JUS23BUS\\6-0_CAD_BIM\\6_2_Work_In-Progress\\R-5600_NCDDT_File_Structure\\Traffic\\Signal_Mangement\\14-1200\\14-1200T3-sm.ele.2025mdd.dgn
Fernando Vazquez AT CHA-FVAZUEZ

MAXTIME OVERLAP PROGRAMMING DETAIL

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	7
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal
Included Phases	2	8	6	1
Modifier Phases	1	1	5	-
Modifier Overlap	-	-	-	-
Trail Green	0	0	0	-
Trail Yellow	0.0	0.0	0.0	0
Trail Red	0.0	0.0	0.0	0.0

OUTPUT CHANNEL CONFIGURATION

Front Panel
Main Menu >Controller >More>Channels>Channels Config

Web Interface
Home >Controller >Advanced IO>Channels>Channel Configuration

Channel Configuration

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Phase Vehicle	1		X	X	1
2	Phase Vehicle	2		X		2
3	Overlap	7		X	X	3
4	Phase Vehicle	4		X		4
5	Phase Vehicle	5		X		5
6	Phase Vehicle	6		X	X	6
7	Phase Vehicle	7		X		7
8	Phase Vehicle	8		X	X	8
9	Overlap	1		X	X	9
10	Overlap	2		X	X	10
11	Overlap	3		X		11
12	Overlap	4		X		12
13	Phase Ped	2				13
14	Phase Ped	4				14
15	Phase Ped	6				15
16	Phase Ped	8				16
17	Overlap	5		X	X	17
18	Overlap	6		X		18

NOTICE OVERLAP 7
ASSIGNED TO CHANNEL 3



NOTICE FLASHING RED



MAXTIME STARTUP AND SOFTWARE FLASH
PROGRAMMING DETAIL

Front Panel
Main Menu>Controller>Unit

Web Interface
Home>Controller>Unit

Start Up Parameters

Startup Clearance Hold
6

Unit Flash Parameters

All Red Flash Exit Time
6

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE
SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 14-1200T3
DESIGNED: Aug 2025
SEALED: 8/26/2025
REVISED: N/A

Electrical Detail - Sheet 2 of 2
Temporary Design 3 - TMP Ph2, S1, Part 1



ELECTRICAL AND PROGRAMMING DETAILS FOR:		NC 107 (E. Main Street) at SR 1723 (Cliffside Drive) / Bryson Farm Supply Driveway Division 14 Jackson County Sylva	
PLAN DATE:	August 2025	REVIEWED BY:	ZM Esposito
PREPARED BY:	DS Griffith	REVIEWED BY:	BN Groome
REVISIONS		INIT.	DATE

DOCUMENT NOT CONSIDERED
FINAL UNLESS ALL
SIGNATURES COMPLETED

SEAL
NORTH CAROLINA
PROFESSIONAL
ENGINEER
SEAL
052936
BRITTANY N. GROOME

Signed by:
Brittany Groome
8/26/2025
DATE

SIG. INVENTORY NO. 14-1200T3