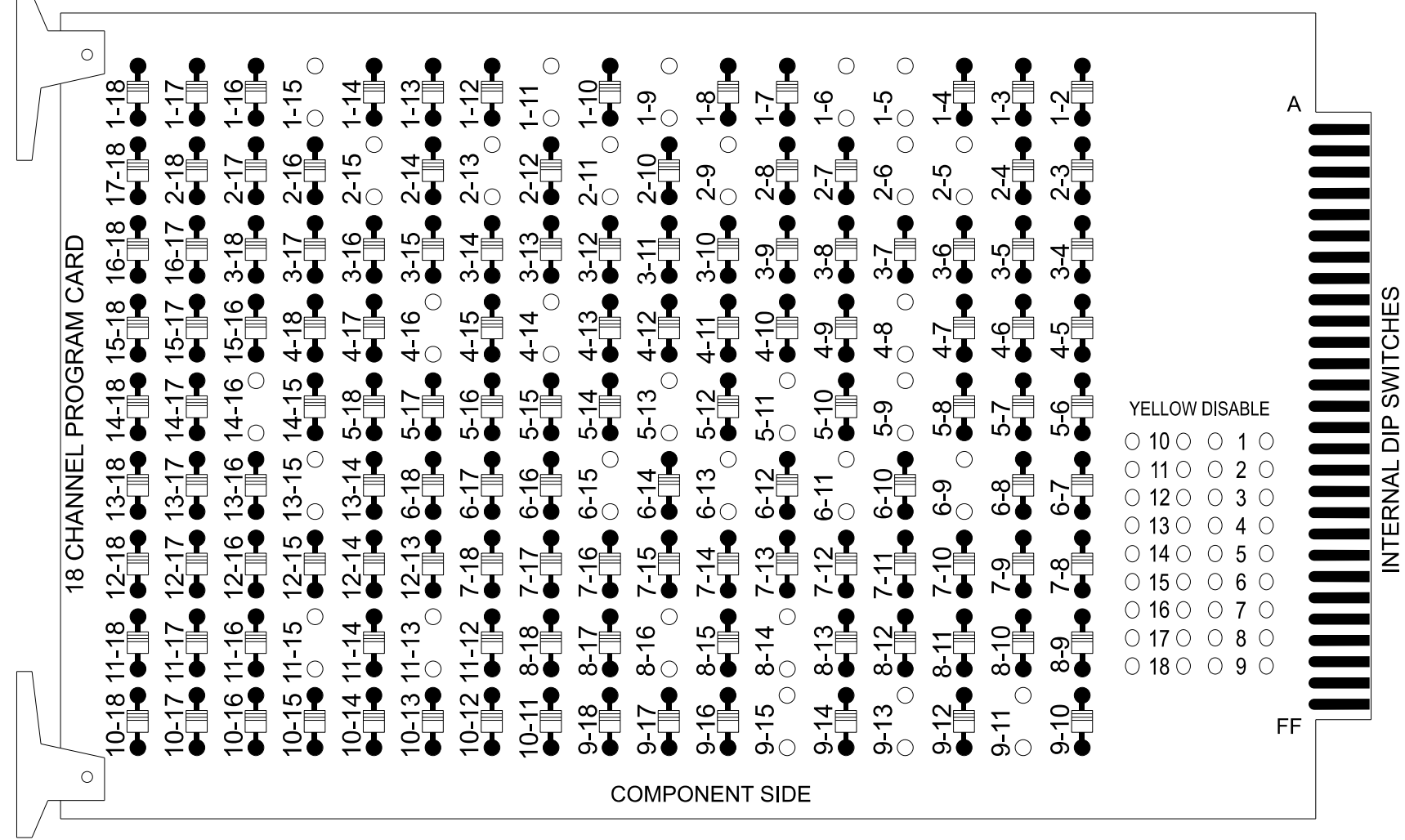


21-AUG-2025 11:17
D:\c:\pdr\desig\1-HDR_US-East_01\Documents\NCDOT\NCDOT_Western_01_v.Dn-Cai L.M.\NCDOT_6170_R5600_NC170_US23BUS\6_0_CAD_BIM\6_2_Work_Ln_Progress\R-5600_NCDOT_F11e_Structure\TrOfficSignal\Signal\Des\gnvSignal Management\14-0694\140694T4.sm.e_e_2025mcdcd.dgn
K.Jones AT CHA-K.JONES

18 CHANNEL IP CONFLICT MONITOR
PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

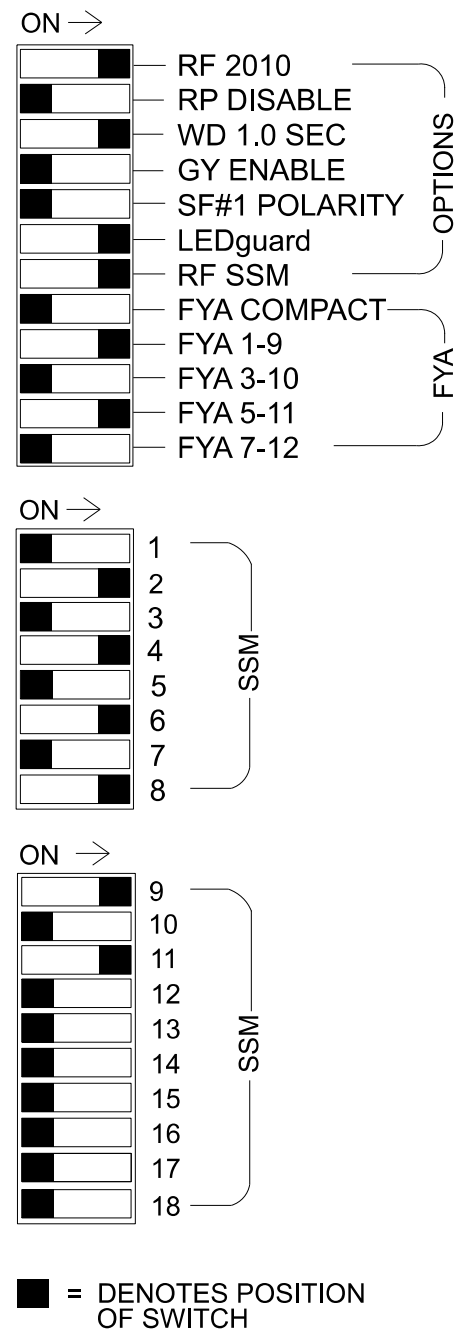
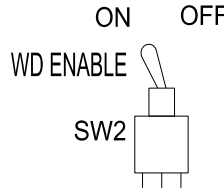
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 1-15, 2-5, 2-6, 2-9, 2-11, 2-13, 2-15, 4-8, 4-14, 4-16, 5-9, 5-11, 5-13, 6-9, 6-11, 6-13, 6-15, 8-14, 8-16, 9-11, 9-13, 9-15, 11-13, 11-15, 13-15, AND 14-16.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that the Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.



NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Program phases 4 and 8 for Dual Entry and Simultaneous Start.
- Program controller to start up in phase 2 Green No Walk and phase 6 Green No Walk.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
- The cabinet and controller are part of the NC 107 Time Based System.

EQUIPMENT INFORMATION

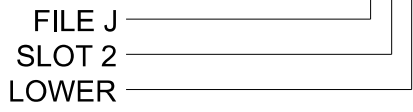
Controller.....2070LX
Cabinet.....332 w/ Aux
Software.....Q-Free MAXTIME
Cabinet Mount.....Base
Output File Positions.....18 With Aux. Output File
Load Switches Used.....S1, S2, S3, S5, S6, S7, S8, S9
S11, S12, AUX S1, AUX S4
Phases Used.....1, 2, 2PED, 4, 4PED, 5, 6, 6PED, 8, 8PED
Overlap "1".....*
Overlap "2".....NOT USED
Overlap "3".....*
Overlap "4".....NOT USED

*See overlap programming detail on sheet 2.

INPUT FILE CONNECTION & PROGRAMMING CHART

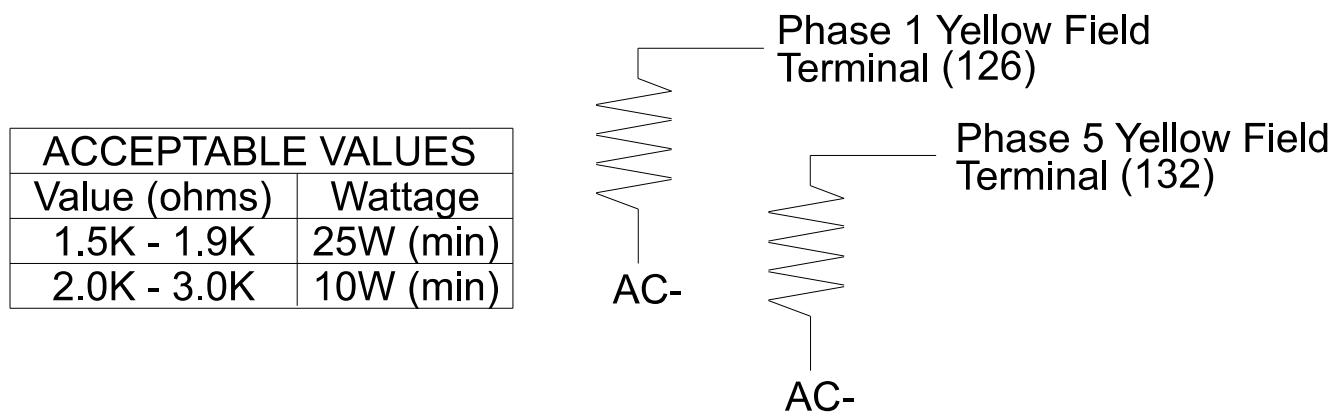
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
1A	TB2-1,2	I1U	56	18	1	1	15.0		X		X	
				-	29	6			X		X	
5A	TB3-1,2	J1U	55	17	15	5	15.0		X		X	
				-	31	2			X		X	
PED PUSH BUTTONS							NOTE: INSTALL DC ISOLATORS IN INPUT FILE SLOTS I12 AND I13.					
P21,P22	TB8-4,6	I12U	67	33	2	PED 2						
P41,P42	TB8-5,6	I12L	69	35	4	PED 4						
P61,P62	TB8-7,9	I13U	68	34	6	PED 6						
P81,P82	TB8-8,9	I13L	70	36	8	PED 8						

INPUT FILE POSITION LEGEND: J2L



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)



SPECIAL DETECTOR NOTE

- Install a multi-zone microwave detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.
- For Detection Zones 1A and 5A, the equipment placement is typical for a NCDOT installation.

Plans Prepared By:
DRMP
DRMP, INC.
8210 UNIVERSITY EXECUTIVE PARK DR.
SUITE 220
CHARLOTTE, NC 28262
NC LICENSE NO. F-1524 (704) 549-4260

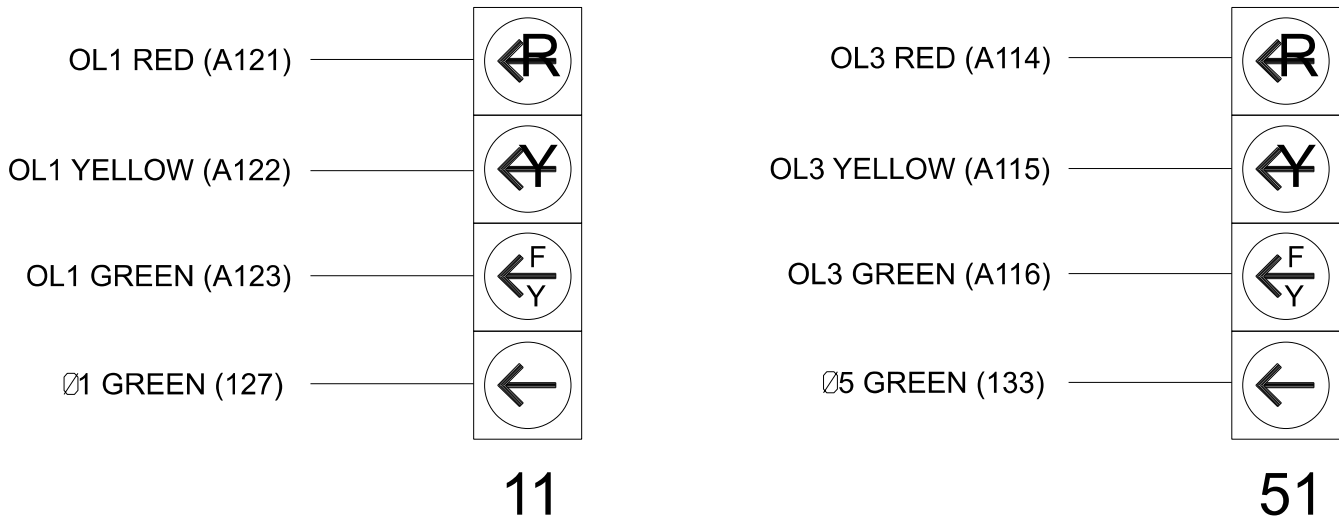
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	11	21,22	P21, P22	NU	41,42, 43	P41, P42	51	61,62	P61, P62	NU	81,82	P81, P82	11	NU	NU	51	NU	NU
RED		128			101			134			107							
YELLOW	*	129			102		*	135			108							
GREEN		130			103			136			109							
RED ARROW													A121			A114		
YELLOW ARROW													A122			A115		
FLASHING YELLOW ARROW													A123			A116		
GREEN ARROW	127						133											
Hand icon			113		104			119			110							
Walking person icon		115			106			121			112							

NU = Not Used
* Denotes install load resistor. See load resistor installation detail this sheet.
★ See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 14-0694T4
DESIGNED: Aug 2025
SEALED: 8/26/2025
REVISED: N/A

Electrical Detail - Sheet 1 of 2
Temporary Design 4 - TMP Ph3, S1

ELECTRICAL AND PROGRAMMING DETAILS FOR:		NC 107 (E. Main Street)		DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED	
at		SR 1352 (Walter Ashe Road) /		SEAL	
SR 1449 (Cope Creek Road)		Division 14 Jackson County Sylva		SEAL 052936	
PLAN DATE: August 2025		REVIEWED BY: BN Groome		ENGINEER	
PREPARED BY: KA Jones		REVIEWED BY: ZM Esposito		Signed by: Brittany N Groome	
REVISITONS		INIT.		DATE	
				8/26/2025	
				DATE	
				SIC. INVENTORY NO. 14-0694T4	