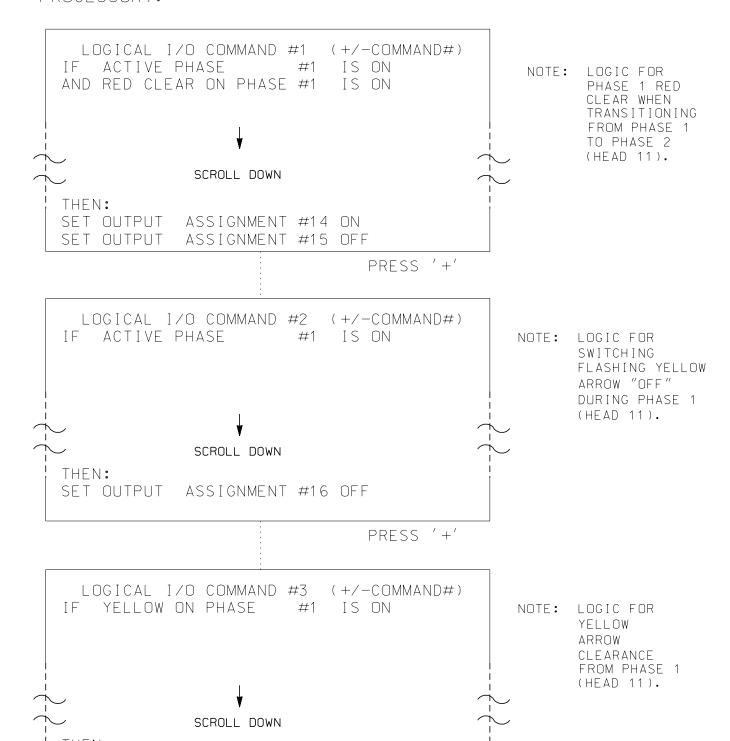
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

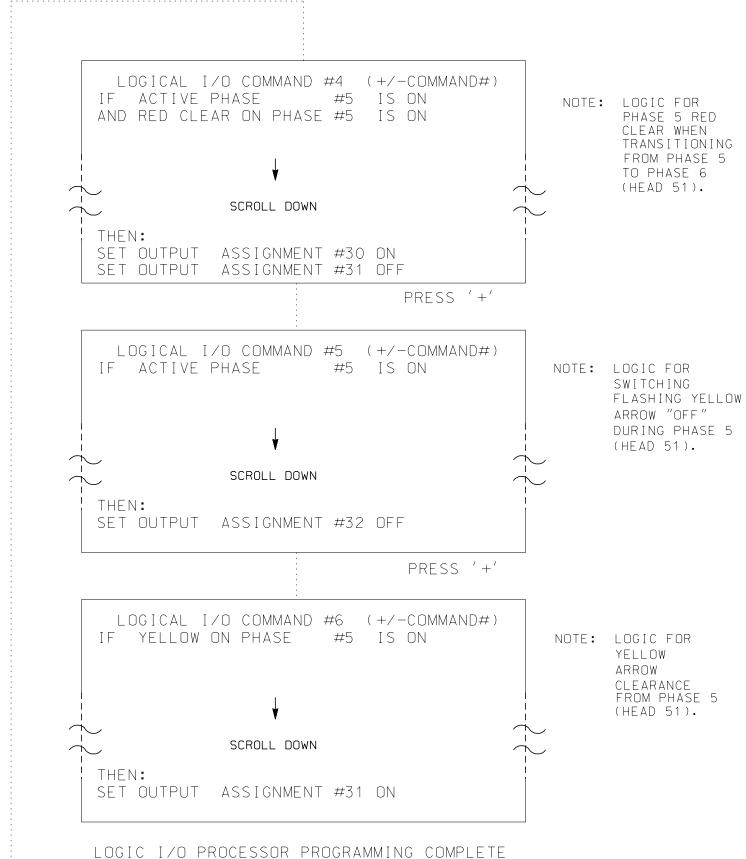
(Verify controller is programmed as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5 AND 6.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



PRESS '+'

SET OUTPUT ASSIGNMENT #15 ON



OUTPUT REFERENCE SCHEDULE

OUTPUT 14 = Overlap A Red OUTPUT 15 = Overlap A Yellow OUTPUT 16 = Overlap A Green OUTPUT 30 = Overlap C Red OUTPUT 31 = Overlap C Yellow OUTPUT 32 = Overlap C Green OUTPUT 33 = Phase 1 Green OUTPUT 34 = Phase 5 Green

Note: All outputs shown above have been remapped. See sheets 3 and 4 of this electrical detail.

TRANSITION OUT OF RED-RED FLASH SEQUENCE PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN PRESS '1' (PHASE CONTROL FUNCTIONS) TO GET TO PHASE CONTROL SET PAGE '1'

> PHASE CONTROL SET: PAGE 1 (NEXT: PAGES) l 12345678910111213141516 PHASE # PERMITED I XXXXXXXX PED RED CLEAR I STARTUP IN GREEN I STARTUP IN YEL | STARTUP RED CLR | FIRST PHASES | X X

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 14-0694T1 DESIGNED: Aug 2025 SEALED: 8/26/2025 REVISED: N/A

OVERLAP PROGRAMMING DETAIL

(Verify and update controller to be programmed as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS 12345678910111213141516 VEH OVL PARENTS: XX VEH OVL NOT VEH: | VEH OVL NOT PED: ; VEH OVL GRN EXT: STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW X GREEN NOTICE GREEN FLASH SELECT VEHICLE OVERLAP OPTIONS: (Y/N) NOTICE NO YELLOW IN CONTROLLER FLASH FLASH YELLOW IN CONTROLLER FLASH?...N GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (O=PARENT, 3-25.5 SEC)..0.0 RED CLEAR (0=PARENT, 0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0

PRESS '+' TWICE

NOTICE GREEN FLASH

NOTICE NO YELLOW IN CONTROLLER FLASH

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS PHASE: ¦12345678910111213141516 VEH OVL PARENTS: | XX VEH OVL NOT VEH: | VEH OVL NOT PED: | VEH OVL GRN EXT: | STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW X GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...N GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (O=PARENT,3-25.5 SEC)..O.O RED CLEAR (0=PARENT, 0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0

OVERLAP PROGRAMMING COMPLETE

PROJECT REFERENCE NO. SHEET NO. Sig-14.2 R-5600

CONTROLLER FLASH PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN PRESS '1' (PHASE CONTROL FUNCTIONS) TO GET TO PHASE CONTROL SET PAGE '1'

> PHASE CONTROL SET: PAGE 1 (NEXT: PAGES) l 12345678910111213141516 PHASE # PERMITED \perp XXXXXXXX FLASH ENTRY | X X YELLOW FLASH | WAG PHASES IX X X WAG OVERLAPS | X MANUAL CONTROL | XXXXXXXX

Note: Yellow Flash disabled for all phases.



ELECTRICAL AND PROGRAMMING DETAILS FOR:

NC 107 (E. Main Street)

SR 1352 (Walter Ashe Road) , SR 1449 (Cope Creek Road)

Sylva Division 14 Jackson County PLAN DATE: August 2025 REVIEWED BY: ZM Esposito PREPARED BY: F Vazquez REVIEWED BY: BN Groome

INIT. DATE Brittany Groome 8/26/2025 __1E09340E1094484... SIG. INVENTORY NO. [4-0694]

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL

SIGNATURES COMPLETED

CARN

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SEAL

Plans Prepared By: DRMP, INC. 8210 UNIVERSITY EXECUTIVE PARK DR. SUITE 220 CHARLOTTE, NC 28262 NC LICENSE NO. F-1524 (704) 549-4260

