

PROJECT REFERENCE NO.	SHEET NO.
U-5760	Sig. 6.2

MAXTIME DETECTOR PROGRAMMING DETAIL
FOR ALTERNATE PHASING
LOOPS 1A & 5A

Front Panel
Main Menu >Controller >Detector >Veh Det Plans

Web Interface
Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

1A

Plan 2		
Detector	Call Phase	Delay
1	1	0.0
29	0	3.0

5A

Detector	Call Phase	Delay
15	5	0.0
31	0	3.0

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO ENSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

OUTPUT CHANNEL CONFIGURATION

Front Panel
Main Menu >Controller >More>Channels>Channels Config

Web Interface
Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Phase Vehicle	1	-	X	X	1
2	Phase Vehicle	2	-	X	-	2
3	Overlap	7	-	X	X	3
4	Phase Vehicle	4	-	X	-	4
5	Phase Vehicle	5	-	X	-	5
6	Phase Vehicle	6	-	X	X	6
7	Phase Vehicle	7	-	X	-	7
8	Phase Vehicle	8	-	X	X	8
9	Overlap	1	-	X	X	9
10	Overlap	2	-	X	X	10
11	Overlap	3	-	X	-	11
12	Overlap	4	-	X	-	12
13	Phase Ped	2	-	-	-	13
14	Phase Ped	4	-	-	-	14
15	Phase Ped	6	-	-	-	15
16	Phase Ped	8	-	-	-	16
17	Overlap	5	-	X	X	17
18	Overlap	6	-	X	-	18

NOTICE: FLASH RED

NOTICE OVERLAP 7
ASSIGNED TO CHANNEL 3



MAXTIME OVERLAP PROGRAMMING DETAIL
FOR DEFAULT PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	5	7
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal
Included Phases	2	8	6	6	1
Modifier Phases	1	-	5	-	-
Modifier Overlaps	-	7	-	-	-
FYA Ped Delay	-	-	7	7	-
Trail Green	0	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0	0.0

NOTICE FYA
PED DELAY

MAXTIME OVERLAP PROGRAMMING DETAIL
FOR ALTERNATE PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	1	2	3	5	7
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	Normal
Included Phases	-	8	-	6	1
Modifier Phases	1	-	5	-	-
Modifier Overlaps	-	7	-	-	-
FYA Ped Delay	-	-	-	7	-
Trail Green	0	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0	0.0

NOTICE INCLUDED PHASE

NOTICE FYA
PED DELAY

MAXTIME STARTUP AND SOFTWARE FLASH
PROGRAMMING DETAIL

Front Panel
Main Menu >Controller >Unit

Web Interface
Home >Controller >Unit

Modify parameters as shown below and save changes.

Start Up Parameters

StartUp Clearance Hold
6

Unit Flash Parameters

All Red Flash Exit Time
6

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 09-0482T
DESIGNED: August 2024
SEALED: 07/14/2025
REVISED: N/A

Electrical Detail - Temporary Design
Sheet 2 of 3

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ELECTRICAL AND PROGRAMMING DETAILS FOR:		
Prepared For: Big Mill Farm Road / Sr 2649 (Hopkins Road) at Bluff School Road		
Division 9 Forsyth County Kernersville		
PLAN DATE: August 2024	REVIEWED BY: KP Baumann	
PREPARED BY: CF Davis	REVIEWED BY:	
REVISIONS	INIT.	DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED	
SEAL 	
DocuSigned by: 	7/14/2025
SIGNATURE	DATE
SIG. INVENTORY NO. 09-0482T	