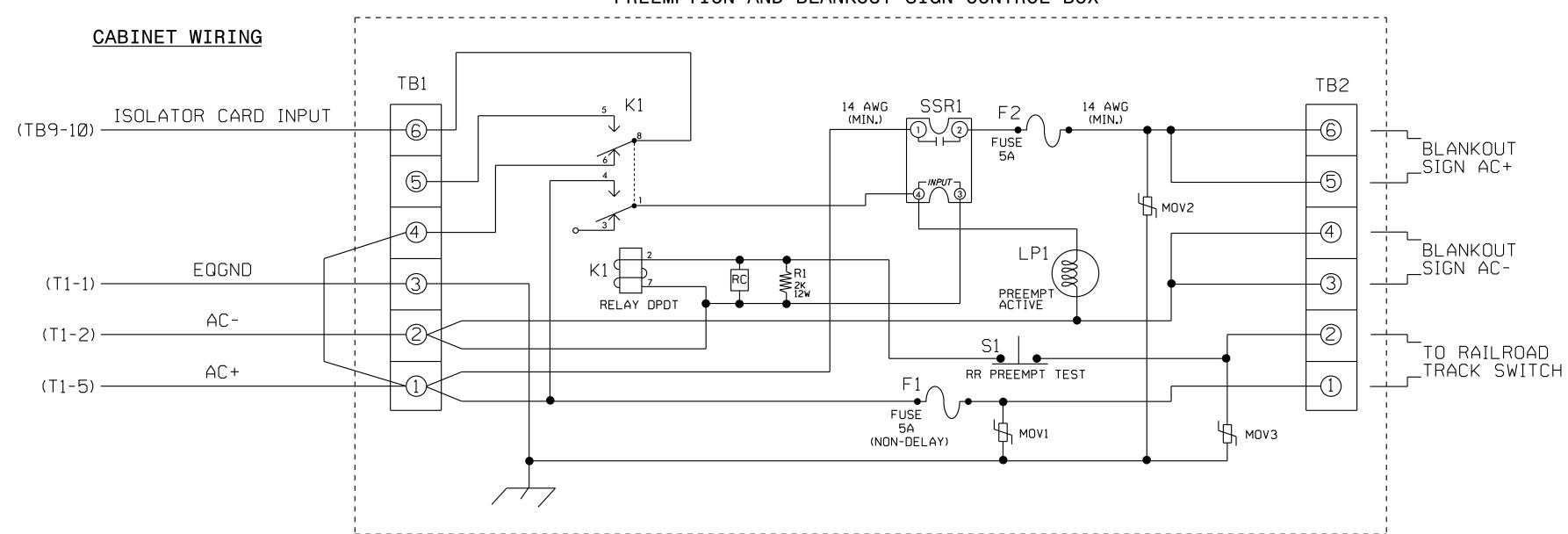
#### PROJECT REFERENCE NO. U-5760 Sig. 4.3

#### RAILROAD PREEMPTION WIRING DETAIL

(wire as shown below)

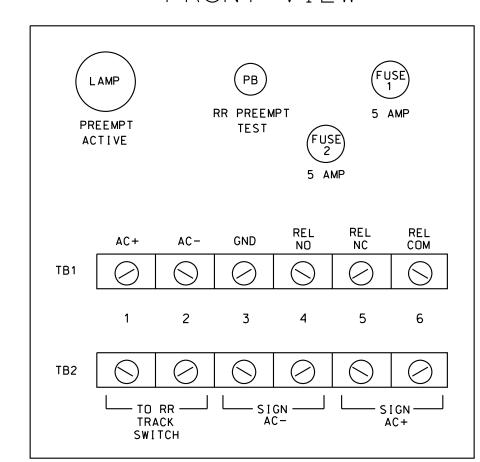
#### PREEMPTION AND BLANKOUT SIGN CONTROL BOX



#### **NOTES**

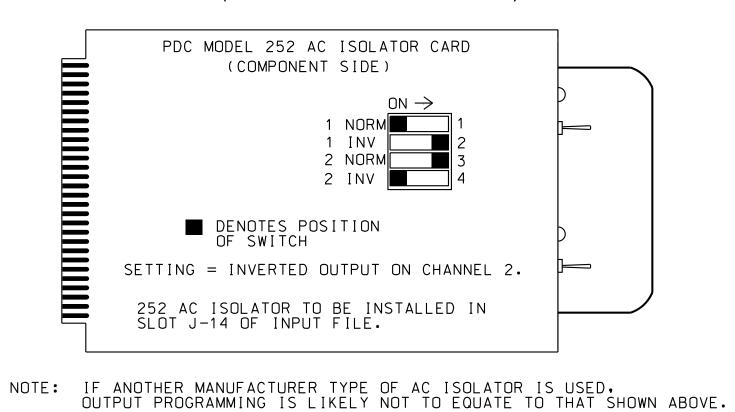
- 1. Relay K1 is shown in the energized (Preempt <u>not</u> active) normal operation state.
- 2. Relay K1 is a DPDT with 120VAC coil with octal base.
- 3. Relay SSR1 is a SPST (normally open) Solid State Relay with AC input and AC (25 amp) output.
- 4. AC Isolator Card shall activate preemption upon removal of AC+ from the input (as shown above). To accomplish this set invert dip switch on AC Isolator Card.
- 5. IMPORTANT!! A jumper must be added between input file terminals J14-E and J14-K if not already present. Also, terminal TB9-12 (on input panel) shall be connected to AC neutral (jumper may have to be added).

#### FRONT VIEW



#### PREEMPT 2 AC ISOLATOR (MODEL 252) OUTPUT PROGRAMMING DETAIL

(set DIP switches as shown below)



#### COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

Kimley » Horn NC License #F-0102 421 Fayetteville Street, Suite 600

# PLANS PREPARED IN THE OFFICE OF:

## Electrical Detail - Temporary Design 3 Sheet 3 of 3

NC 66/SR 2377 (W. Mountain St.) NC 66 (Old Hollow Road) and

SR 2649 (Hopkins Road) Forsyth County Kernersville Division 9 PLAN DATE: August 2024 REVIEWED BY: KP Baumann

REVIEWED BY: REVISIONS INIT. DATE



SIG. INVENTORY NO. 09-0567T3

## PREEMPTION PROGRAMMING

Front Panel Main Menu >Controller >Preemption >Preempt Phasing/Preempt Parameters

Web Interface Home >Controller >Preempt Configuration >Preempts

### **Preempt Configuration**

Preempt	1
Enabled	Enabled
Туре	Rail Road
Track Phases	4,7,39
Track Overlaps	2,4
Dwell Phases	2,6
Dwell Peds	÷
Dwell Overlaps	1
Cycling Phases	2,6,9
Cycling Peds	-
Cycling Overlaps	1,5,7
Exit Phases	4,8
Exit Overlaps	2,4
Delay	0
Call Ext Time	1:0
Max Presence	0
Max Pres Act	Terminate
Enter Min Green	1
Enter Walk	1
Enter Ped Clear	5
Enter Yellow Change	4:9
Enter Red Clear	3:3
Track Green	28
Track Yellow Clr	4.4
Track Red Clear	2:3
Dwell Green	0
Exit Min Green	255
Exit Yellow Change	25.5
Exit Red Clear	25.5
Exit Type	Exit Phases
Non Locking Memory	·
Not Ovrd Flash	Χ
Not Ovrd Nxt Pre	÷
Require All Red Entry	÷
Track Clear Ovrd	Х
Ped Clear During Yellow	Χ
Entry Omit OLTG	Χ
Track Reserve	Χ

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0567T3 DESIGNED: August 2024 SEALED: 07/14/2025 REVISED: N/A

> DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

PREPARED BY: CF Davis

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