PROJECT REFERENCE NO. Sig. 3.2

OVERLAP PROGRAMMING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	4	5	6	7
Туре	FYA 4 - Section	Normal	Normal	Normal			
Included Phases	2	4	6	8	1,9	7	3,9
Modifier Phases	1	3,9	5	7	÷	÷	÷
Modifier Overlaps	÷	÷	÷	<u>.</u>	÷	÷	-
Trail Green	0	0	0	0	0	0	0
Trail Yellow	0:0	0:0	0:0	0:0	0:0	0:0	0:0
Trail Red	0:0	0:0	0:0	0:0	0:0	0:0	0:0

LOGIC PROCESSOR PROGRAMMING

Front Panel

Main Menu >Controller >More >User Programs >Definition

Web Interface

Home >Controller >User Programs Configuration >User Programs Definition

Program 1

Statement	Result	Index	Operation	Parameter A	Index	Parameter B	Index	Delay	Ext
1	Phase Phase Omit	9	Result=!A	Preempt Status	1	<u> -</u>	-	0:0	0:0

LOGIC STATEMENT DESCRIPTION

Statement 1 Description: Omits phase 9 while not in preempt 1.

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO ENSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

SEQUENCE DETAIL

Front Panel

Main Menu >Controller >Sequence & Phs Config>Sequences

Web Interface

Home >Controller >Sequence

Sequence 1

Ring	Sequence Data
1	1,2,a,3,4,b,9,c
2	5,6,a,7,8,b,c
3	39,c,40,d

OUTPUT CHANNEL CONFIGURATION

Front Panel

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Phase Vehicle	1		Х	Х	1
2	Phase Vehicle	2	·	X		2
3	Overlap	7	·	Χ	Х	3
4	Phase Vehicle	4	·	X		4
5	Phase Vehicle	5		Х		5
6	Phase Vehicle	6	·	Х	Х	6
7	Phase Vehicle	7	·	Х		7
8	Phase Vehicle	8	·	Х	Х	8
9	Overlap	1	·	Х	Х	9
10	Overlap	2	·	X	Х	10
11	Overlap	3	·	Х		11
12	Overlap	4	·	X		1:2
13	Phase Ped	2	·	·		13
14	Phase Ped	4	·			14
15	Phase Ped	6				15
16	Phase Ped	8	·			16
17	Overlap	5		Χ	Х	1.7
18	Overlap	6		Х		18

NOTICE: FLASH RED

MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel

ASSIGNED TO CHANNEL 3

Main Menu >Controller >Unit

Web Interface

Home >Controller >Unit

Modify parameters as shown below and save changes.

Start Up Parameters

StartUp Clearance Hold

Unit Flash Parameters

All Red Flash Exit Time

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0567T2 DESIGNED: August 2024 SEALED: 07/14/2025 REVISED: N/A

Electrical Detail - Temporary Design 2 Sheet 2 of 3

NC 66/SR 2377 (W. Mountain St.) NC 66 (Old Hollow Road) and SR 2649 (Hopkins Road)

Forsyth County Kernersville Division 9 PLAN DATE: August 2024 REVIEWED BY: KP Baumann

PREPARED BY: CF Davis REVIEWED BY: REVISIONS INIT. DATE

SEAL 044434 * VGINEER SIGNATURE SIG. INVENTORY NO. 09-0567T2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

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