

(program controller as shown)

- Toggle to "Overlap G"

OVERLAP G

Select TMG VEH OVLP [G] and 'NORMAL'

```

TMG VEH OVL P...[G] TYPE: .....NORMAL
  PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED X . . . . .
LAG GRN 0.0 YEL 0.0 RED 0.0

```

Toggle Once

OVERLAP H

Select TMG VEH OVLP [H] and 'NORMAL'

```

TMG VEH OVLP...[H] TYPE: .....NORMAL
  PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . . . . X . . . . .
LAG GRN 0.0 YEL 0.0 RED 0.0

```

Toggle to "Overlap A"

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

```

TMG VEH OVLP...[A] TYPE: ....PPLT FYA
PROTECTED LEFT TURN....    PHASE      1
OPPOSING THROUGH.....    PHASE      2

FLASHING ARROW OUTPUT.....CH9  ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 1

```

Toggle Once

NOTICE ACTION
PLAN SF BIT "1"

OVERLAP B

Select TMG VEH OVLP [B] and 'PPLT FYA'

```

TMG VEH OVLP...[B] TYPE: ....[PPLT FYA]
PROTECTED LEFT TURN....    OVERLAP    G
OPPOSING THROUGH.....    PHASE      8

FLASHING ARROW OUTPUT....CH10 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 0

```

Toggle Two Times

OVERLAP D

Select TMG VEH OVLP [D] and 'PPLT FYA'

```

TMG VEH OVLP...[D] TYPE: ....[PPLT FYA]
PROTECTED LEFT TURN....    OVERLAP    H
OPPOSING THROUGH.....    PHASE      2

FLASHING ARROW OUTPUT....CH12 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 0

```

END PROGRAMMING

(program controller as shown)

1. From Main Menu select **2. CONTROLLER**
2. From CONTROLLER Submenu select **5. START/FLASH**

START/FLASH DATA																
-----START UP-----																
PHASE	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	6
	G					G										
	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P
OVERLAP	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
FLASH>MON.	X	NO	FL							0	ALL	RED.				6
PWR START SEQ.	0				1	MUTCD	➔	YES			Y-	G:				NO

Scroll down on this screen and set "Exit FL" to Green "G"

(program controller as shown)

To assign load switch S4 as OLG and load switch S10 as OLH, program LD SWITCH 3 as OVLP '7' TYPE '0' and LD SWITCH 7 as OVLP '8' TYPE '0' as shown below.

1. From Main Menu select **1. CONFIGURATION**
2. From CONFIGURATION Submenu select **3. LOAD SW ASSIGN**

	LD	SWITCH		ASSIGN		DIMMING				---FLASH---		
		PHASE /OVLP	TYPE	R	Y	G	D	PWR	AUT	TGR		
→	1	1	V	.	.	.	+	A	R	X		
	2	2	V	.	.	.	+	A	R	.		
	3	7	O	.	.	.	+	A	R	X		
	4	4	V	.	.	.	+	A	R	.		
→	5	5	V	.	.	.	-	A	R	.		
	6	6	V	.	.	.	-	A	R	X		
	7	8	O	.	.	.	+	A	R	.		
	8	8	V	.	.	.	-	A	R	X		
	9	1	O	.	.	.	+	A	R	X		
	10	2	O	.	.	.	+	A	R	X		
	11	3	O	.	.	.	-	A	R	.		
	12	4	O	.	.	.	-	A	R	.		
	13	2	P	.	.	.	+	A	.	.		
	14	4	P	.	.	.	-	A	.	.		
	15	6	P	.	.	.	+	A	.	.		
	16	8	P	.	.	.	-	A	.	.		

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 02-0921
DESIGNED: July 2025
SEALED: 07/22/2025
REVISED: N/A

Electrical Detail - Sheet 2 of 3

PROJECT REFERENCE NO.	SHEET NO.
U-5875	Sig. 11.2

**DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED**

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