Docusign Envelope ID: DAD087B5-1238-45EC-98DE-79E1BC94A667

PROJECT REFERENCE NO.	SHEET NO.
U-5875	Sig. 6.3

ECONOLITE ASC/3-2070
OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select `2. CONTROLLER`
2. From CONTROLLER Submenu select `2. VEHICLE OVERLAPS`
3. Press "Toggle" until positioned on Overlap E

OVERLAP E

Select TMG VEH OVLP [E] and 'NORMAL'

```
TMG VEH OVLP...[E] TYPE: .......NORMAL
  PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED X . . . . . . . . . . . . . . .

LAG GRN 0.0 YEL 0.0 RED 0.0
```

Toggle Once

OVERLAP F

Select TMG VEH OVLP [F] and 'NORMAL'

```
TMG VEH OVLP...[F] TYPE: .......NORMAL
  PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . X . . . . . . . . . . . . .

LAG GRN 0.0 YEL 0.0 RED 0.0
```

Toggle Once

OVERLAP G

Select TMG VEH OVLP [G] and 'NORMAL'

```
TMG VEH OVLP...[G] TYPE: .......NORMAL
  PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . . . X . . . . . . . . . . .

LAG GRN 0.0 YEL 0.0 RED 0.0
```

Toggle Once

OVERLAP H

Select TMG VEH OVLP [H] and 'NORMAL'

```
TMG VEH OVLP...[H] TYPE: .......NORMAL
  PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED X . . . . . . . . . . . . . . .

LAG GRN 0.0 YEL 0.0 RED 0.0
```

Toggle Once

OVERLAP I

Select TMG VEH OVLP [I] and 'NORMAL'

```
TMG VEH OVLP...[I] TYPE: .......NORMAL
  PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . X . . . . . . . . . . . . .

LAG GRN 0.0 YEL 0.0 RED 0.0
```

Press Toggle until
positioned over Overlap A

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

```
TMG VEH OVLP...[A] TYPE: .....PPLT FYA

PROTECTED RIGHT TURN....  OVERLAP  G
OPPOSING THROUGH.......   PHASE    4

FLASHING ARROW OUTPUT.....CH9 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 0
```

Toggle Once

OVERLAP B

Select TMG VEH OVLP [B] and 'PPLT FYA'

```
TMG VEH OVLP...[B] TYPE: .....PPLT FYA

PROTECTED RIGHT TURN....  OVERLAP  H
OPPOSING THROUGH.......   PHASE    3

FLASHING ARROW OUTPUT.....CH10 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 0
```

Toggle Once

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

```
TMG VEH OVLP...[C] TYPE: .....PPLT FYA

PROTECTED LEFT TURN....   PHASE    5
OPPOSING THROUGH.......   PHASE    6

FLASHING ARROW OUTPUT.....CH11 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 5
```

NOTICE ACTION PLAN SF BIT "5"

Toggle Once

OVERLAP D

Select TMG VEH OVLP [D] and 'PPLT FYA'

```
TMG VEH OVLP...[D] TYPE: .....PPLT FYA

PROTECTED RIGHT TURN....  OVERLAP  I
OPPOSING THROUGH.......   PHASE    2

FLASHING ARROW OUTPUT.....CH12 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 0
```

END PROGRAMMING

ECONOLITE ASC/3-2070
LOAD SWITCH AND
PED 3 PROGRAMMING ASSIGNMENT DETAIL

(program controller as shown)

1. From Main Menu select `6. DETECTORS`
2. From DETECTOR Submenu select `3. PED DETECTOR INPUT ASSIGNMENT`

```
PED DET PHASE ASSIGNMENT MODE:  NTCIP

   PHASE  1   2   3   4   5   6   7   8
DETECTOR  0   2   8   4   0   6   0   0

   PHASE  9  10  11  12  13  14  15  16
DETECTOR  0   0   0   0   0   0   0   0
```

NOTICE PED DETECTOR 8 ASSIGNED TO PHASE 3

To assign load switch S1 as OLG, program LD SWITCH 1 as OVLP '7' TYPE 'O' as shown below.
To assign load switch S4 as OLH, program LD SWITCH 3 as OVLP '8' TYPE 'O' as shown below.
To assign load switch S10 as OLI, program LD SWITCH 7 as OVLP '9' TYPE 'O' as shown below.
To reassign load switch S12 as PED 3, program LD SWITCH 16 as PHASE '3' TYPE 'P' as shown below.

1. From Main Menu select `1. CONFIGURATION`
2. From CONFIGURATION Submenu select `3. LOAD SW ASSIGN`

```
LD SWITCH ASSIGN
     PHASE        DIMMING ---FLASH---
     /OVLP  TYPE  R Y G D PWR AUT TGR

1      7     O    . . . +  A   R   X
2      2     V    . . . +  A   R   .
3      8     O    . . . +  A   R   X
4      4     V    . . . +  A   R   .
5      5     V    . . . -  A   R   .
6      6     V    . . . -  A   R   X
7      9     O    . . . -  A   R   .
8      8     V    . . . -  A   R   X
9      1     O    . . . +  A   R   X
10     2     O    . . . +  A   R   X
11     3     O    . . . -  A   R   .
12     4     O    . . . -  A   R   .
13     2     P    . . . +  A   .   .
14     4     P    . . . -  A   .   .
15     6     P    . . . +  A   .   .
16     3     P    . . . -  A   .   .
```

NOTICE PHASE 3 PED ASSIGNED TO LD SWITCH 12

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 02-0500
DESIGNED: July 2025
SEALED: 07/22/2025
REVISED: N/A

Signal Upgrade - Final
Electrical Detail - Sheet 2 of 4

RS&H
NC FIRM LICENSE No: F-0493
8521 SIX FORKS ROAD, SUITE 400
RALEIGH, NC 27615
(919) 926-4100

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared for the Offices of:
Transportation Mobility and Safety Division
ITS & Signals Management Section
750 N.Greenfield Pkwy,Garner,NC 27529

SR 1467 (Stantonsburg Road)
at
SR 1203 (Allen Road)

Division 02 Pitt County Greenville

PLAN DATE: July 2025	REVIEWED BY: S.G. Haynie
PREPARED BY: A.M. Kollar	REVIEWED BY:

REVISIONS	INIT.	DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL
SEAL 029531
STEVEN G. HAYNIE

DocuSigned by:
Steven G. Haynie 7/22/2025
D633DCC9A5D44B6...
SIGNATURE DATE

SIG. INVENTORY NO. 02-0500

7/22/2025
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