

(program controller as shown)

- OVERLAP E

```

TMG VEH OVLP...[E] TYPE: .....NORMAL
  PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . X . . . . . . . . . .
LAG GRN 0.0 YEL 0.0 RED 0.0

```

Toggle Once

OVERLAP F

```

TMG VEH OVLP...[F] TYPE: .....NORMAL
  PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . . . X . . . . . . . . . .
LAG GRN 0.0 YEL 0.0 RED 0.0

```

Toggle Once

OVERLAP G

```

TMG VEH OVLP...[C] TYPE: .....NORMAL
  PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . . . . X . . . . .
LAG GRN 0.0 YEL 0.0 RED 0.0

```

OVERLAP H

```

TMG VEH OVLP...[H] TYPE: .....NORMAL
  PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . X . . . . . . . . . .
LAG GRN 0.0 YEL 0.0 RED 0.0

```

Press Toggle until
positioned over Overlap A

OVERLAP A

```

TMG VEH OVLP...[A] TYPE: ....PPLT FYA
PROTECTED LEFT TURN...    PHASE    1
OPPOSING THROUGH.....    PHASE    2

FLASHING ARROW OUTPUT.....CH9  ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 1

```

NOTICE ACTION
PLAN SF BIT "1"

OVERLAP B

```

TMG VEH OVLP...[B] TYPE: ....[PPLT FYA]
PROTECTED RIGHT TURN.... OVERLAP   G
OPPOSING THROUGH.....  PHASE     6

FLASHING ARROW OUTPUT.....CH10 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE.....

```

Toggle Once

OVERLAP C

```

TMG VEH OVLP...[C] TYPE: ....[PPLT FYA]
PROTECTED RIGHT TURN.... OVERLAP H
OPPOSING THROUGH..... PHASE 2

FLASHING ARROW OUTPUT....CH11 ISOLATE

DELAY START OF: FYA..O.O CLEARANCE..O.O
ACTION PLAN SF BIT DISABLE.....

```

Toggle Once

OVERLAP D

```

TMG VEH OVLP...[D] TYPE: ....[PPLT FYA]
PROTECTED LEFT TURN.... PHASE 7
OPPOSING THROUGH..... PHASE 8

FLASHING ARROW OUTPUT.....CH12 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE.....

```

NOTICE ACTION
PLAN SF BIT "7"

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.



(program controller as shown)

To assign load switch S4 as OLG, program LD SWITCH 3 as OVLP '7' TYPE '0' as shown below.

To assign load switch S7 as OLH, program LD SWITCH 5 as OVLP '8' TYPE '0' as shown below.

1. From Main Menu select **1. CONFIGURATION**
2. From CONFIGURATION Submenu select **3. LOAD SW ASSIGN**

LD	SWITCH /OVLP	ASSIGN PHASE TYPE	DIMMING				---FLASH---		
			R	Y	G	D	PWR	AUT	TGR
1	1	V	.	.	.	+	A	R	X
2	2	V	.	.	.	+	A	R	.
3	7	O	.	.	.	+	A	R	X
4	4	V	.	.	.	+	A	R	.
5	8	O	.	.	.	-	A	R	.
6	6	V	.	.	.	-	A	R	X
7	7	V	.	.	.	-	A	R	.
8	8	V	.	.	.	-	A	R	X
9	1	O	.	.	.	+	A	R	X
10	2	O	.	.	.	-	A	R	X
11	3	O	.	.	.	+	A	R	.
12	4	O	.	.	.	-	A	R	.
13	2	P	.	.	.	+	A	.	.
14	4	P	.	.	.	+	A	.	.
15	6	P	.	.	.	-	A	.	.
16	8	P	.	.	.	-	A	.	.

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 02-0029
DESIGNED: July 2025
SEALED: 07/15/2025
REVISED: N/A

Signal Upgrade - Final
Electrical Detail - Sheet 2 of 5

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