

ECONOLITE ASC/3-2070
OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select
2. CONTROLLER
2. From CONTROLLER Submenu select
2. VEHICLE OVERLAPS

OVERLAP A

Select TMG VEH OVLP [A] and 'OTHER/ECONOLITE'

TMG VEH OVLP...[A] TYPE:OTHER/ECONOLITE

PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6

INCLUDED . X

PROTECT

PED PRTC

NOT OVLP

FLSH GRN . 1

LAG X PH

LAG 2 PH

LAG GRN 0.0 YEL 0.0 RED 0.0 ADV GRN 0.0

Toggle Twice

OVERLAP C

Select TMG VEH OVLP [C] and 'OTHER/ECONOLITE'

TMG VEH OVLP...[C] TYPE:OTHER/ECONOLITE

PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6

INCLUDED X

PROTECT

PED PRTC

NOT OVLP

FLSH GRN 1

LAG X PH

LAG 2 PH

LAG GRN 0.0 YEL 0.0 RED 0.0 ADV GRN 0.0

Toggle Once

OVERLAP D

Select TMG VEH OVLP [D] and 'OTHER/ECONOLITE'

TMG VEH OVLP...[D] TYPE:OTHER/ECONOLITE

PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6

INCLUDED X

PROTECT

PED PRTC

NOT OVLP

FLSH GRN 1

LAG X PH

LAG 2 PH

LAG GRN 0.0 YEL 0.0 RED 0.0 ADV GRN 0.0

END PROGRAMMING

ECONOLITE ASC/3-2070 STARTUP AND
SOFTWARE FLASH PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select
2. CONTROLLER
2. From CONTROLLER Submenu select
5. START/FLASH

START/FLASH DATA

-----START UP-----

1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6

PHASE G G

A B C D E F G H I J K L M N O P

OVERLAP X X X X X X X X X X X X X X X X

FLASH>MON. NO FL TIME.. 0 ALL RED... 6

PWR START SEQ.. 1 MUTCD➔ YES Y- G: NO

Scroll down on this screen and set "Exit FL" to Green "G"

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 02-0029T1
DESIGNED: July 2025
SEALED: 07/15/2025
REVISED: N/A

FLASHER CIRCUIT MODIFICATION DETAIL

In order to ensure that signals flash concurrently on the
Same approach, make the following flasher circuit changes:

1. On rear of PDA - remove wire from Term. T2-4 and terminate on T2-2.
2. On rear of PDA - remove wire from Term. T2-5 and terminate on T2-3.
3. Remove flasher unit 2.

The changes listed above ties all phases and overlaps to flasher unit 1.

ASC/3 FLASH SENSE INPUT CONTROL
FOR RED-RED FLASH

*The NCDOT default database is programmed to addresss Yellow-Red flash.
Logic Statement 100 must be modified as shown when running Red-Red flash.

1. From Main Menu select
1. CONFIGURATION
2. From CONFIGURATION Submenu select
8. LOGIC PROCESSOR
3. From LOGIC PROCESSOR Submenu select
2. LOGIC STATEMENTS

Change the "LP" to 100 and move the cursor down. Delete
the two "CTR-SET" statements by moving the cursor over
them and hitting the "C" key. then hit "ENTER", select
"LP SET CIB ON", hit "ENT", and then set the number to 427.

LP#:100 COPY FROM:100 ACTIVE: M FALSE

IF LP CIB CODE ON 331 F

THEN LP DELAY FOR 1.0 SECONDS

LP SET CIB ON 427

ELSE

THIS STATEMENT IS USED
TO CONTROL THE FLASH
SENSE INPUT WHEN RUNNING
RED-RED FLASH OPERATION.

Hit "ESC", then 1 for "LOGIC STATEMENT CONTROL",
next verify that LP#100 is ENABLED.

END PROGRAMMING

Temporary Signal - TMP Phase I & II
Electrical Detail - Sheet 2 of 2



US 13 (Dickinson Avenue) / US 264 Alt at SR 1203 (Allen Road) / US 264 Alt (Greenville Boulevard)	
Division 2	Pitt County Greenville
PLAN DATE: July 2025	REVIEWED BY: S.G. Haynie
PREPARED BY: A.M. Kollar	REVIEWED BY:
REVISIONS	INIT. DATE

