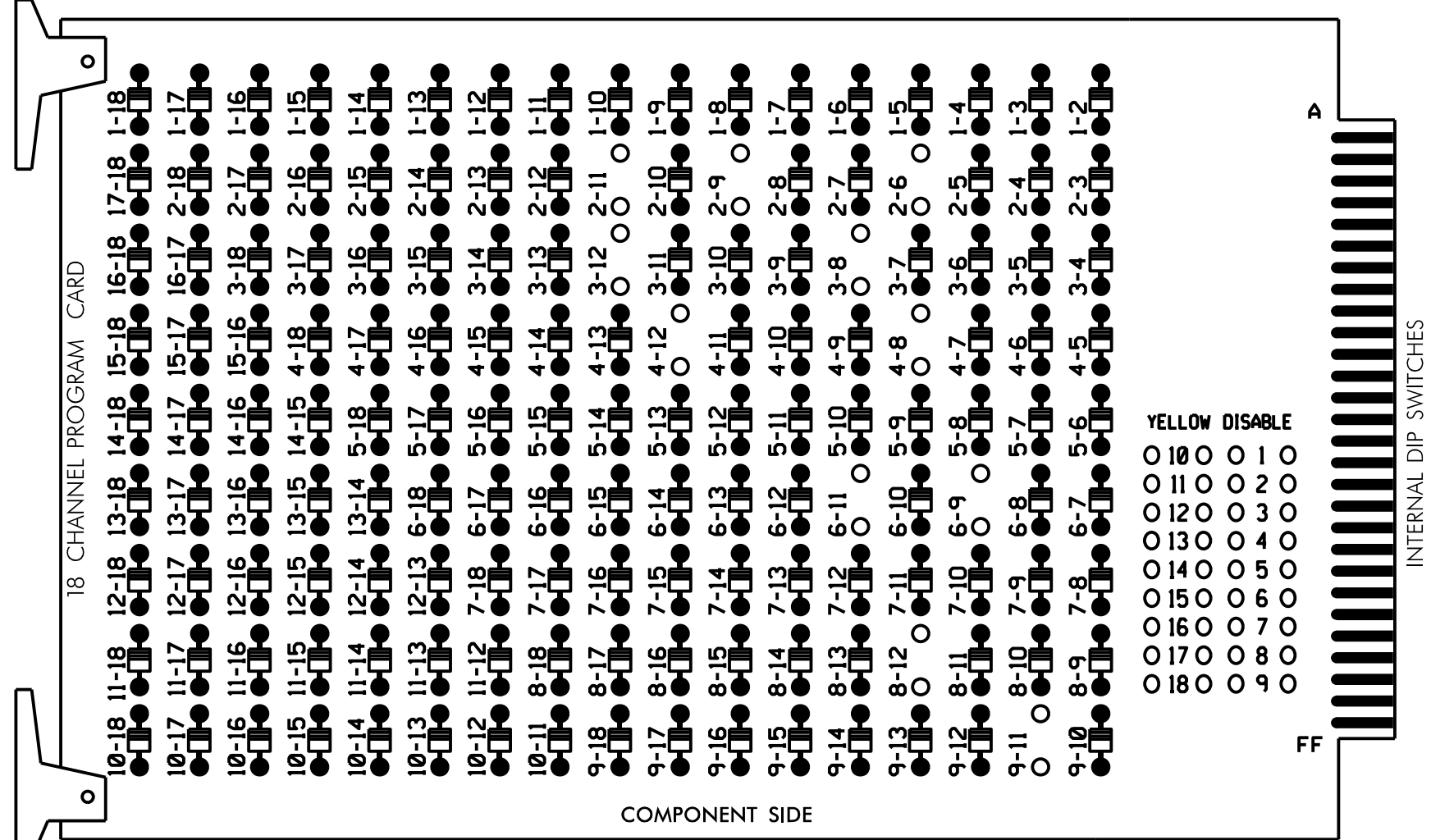
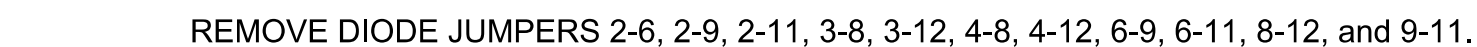


18 CHANNEL IP CONFLICT MONITOR

PROGRAMMING DETAIL

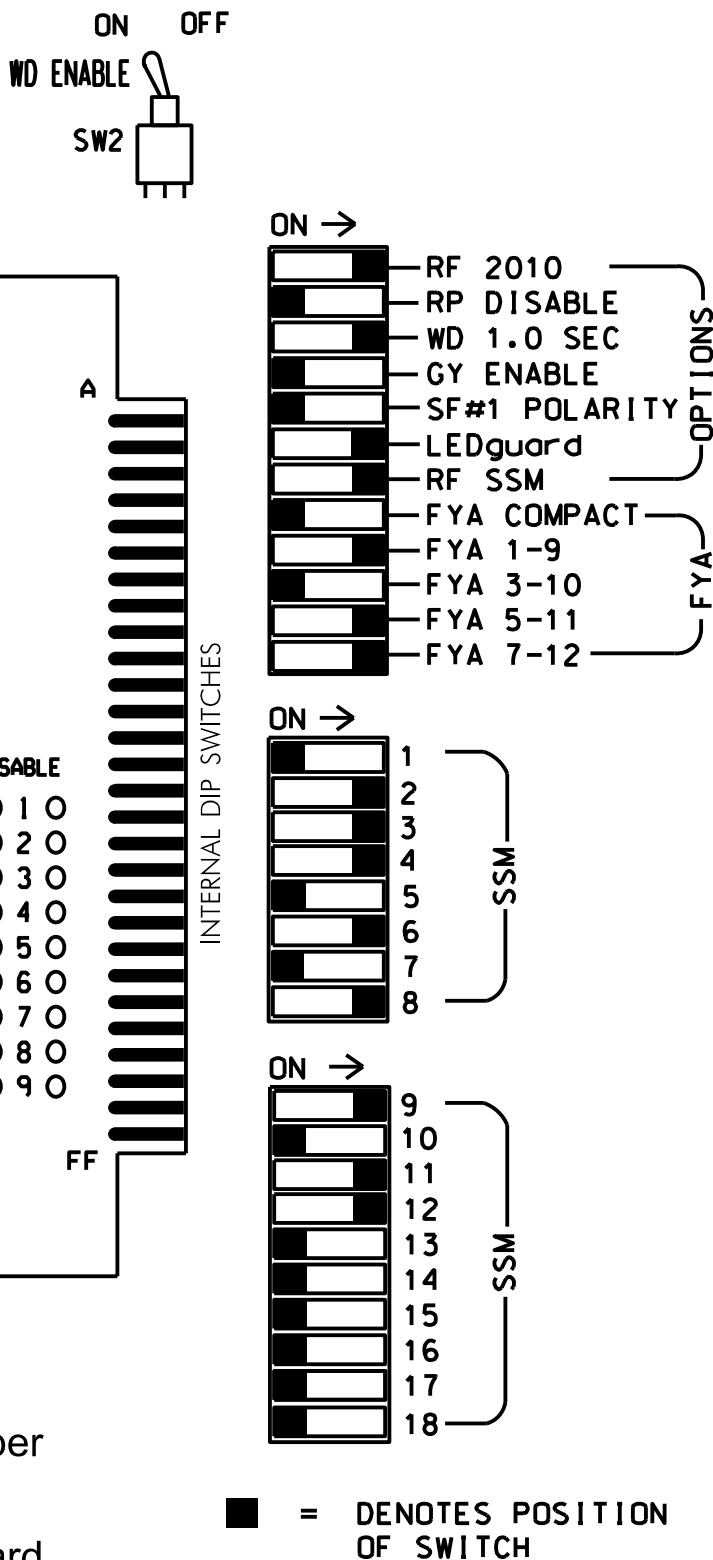
(remove jumpers and set switches as shown)



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that the Red Enable is active at all times during normal operation.



NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program phase 8 for Dual Entry.
3. Program controller to start up in phase 2 Green and 6 Green.
4. The cabinet and controller are part of the Greenville Signal System.

EQUIPMENT INFORMATION

```

CONTROLLER.....2070LX
CABINET.....332 W/AUX
SOFTWARE.....ECONOLITE ASC/3-2070
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S2,S4,S5,S8,S11,AUX S1,AUX S4,AUX S5
PHASES USED.....2,3,4,6,8
OVERLAP "A".....*
OVERLAP "B".....NOT USED
OVERLAP "C".....*
OVERLAP "D".....*

```

* See overlap programming detail on sheet 2

SIGNAL HEAD HOOK-UP CHART

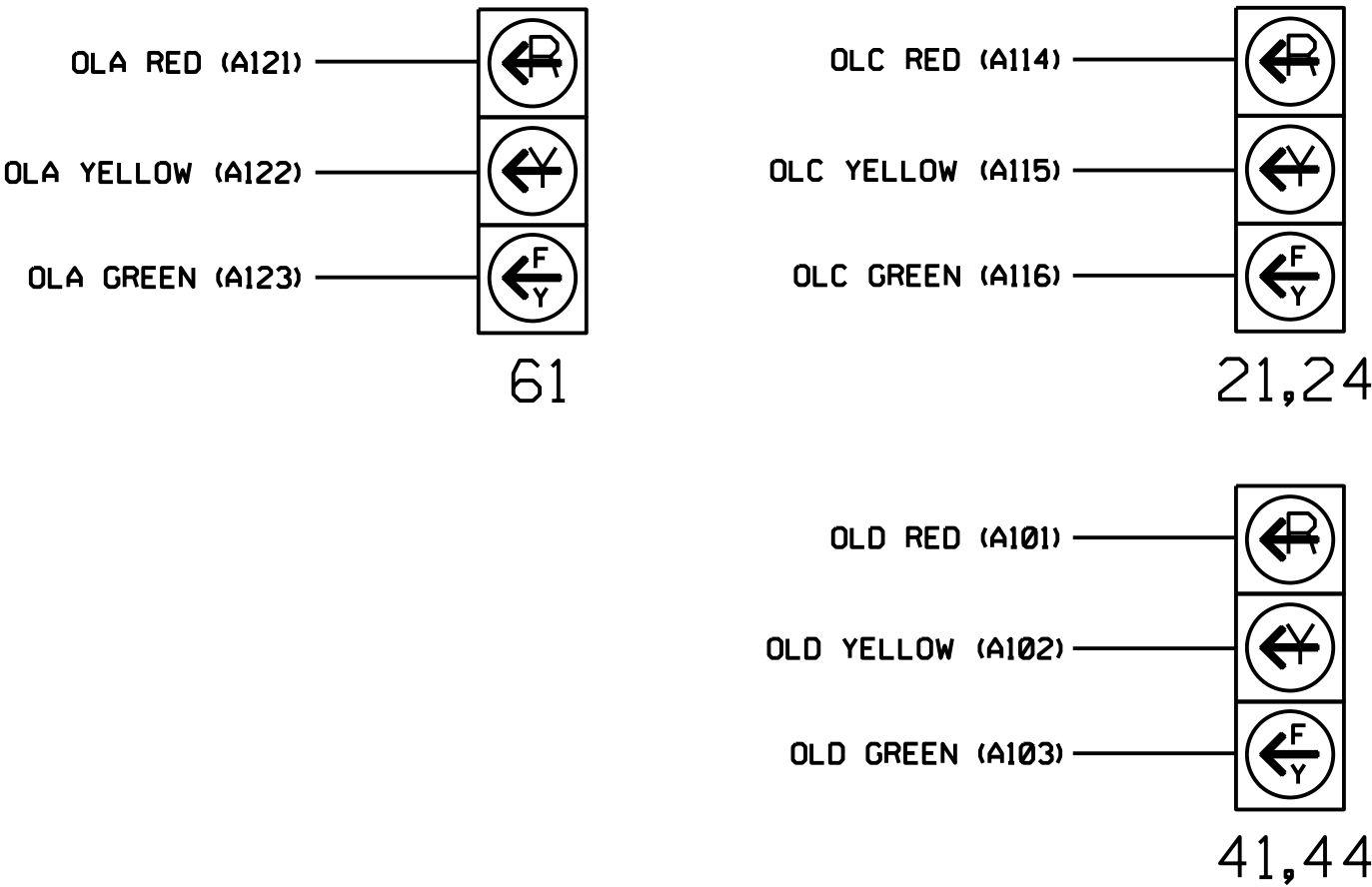
[illegible]

NU = Not Used

★ See pictorial of head wiring in detail this sheet.

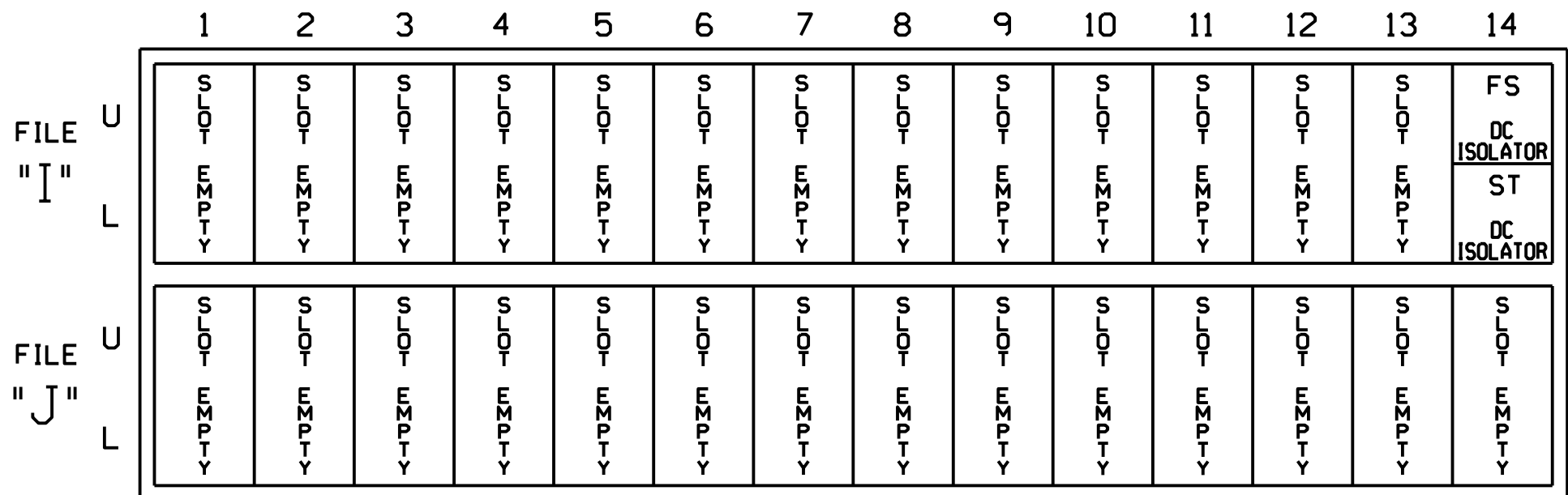
FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



INPUT FILE POSITION LAYOUT

(front view)



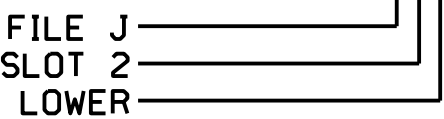
EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	DETECTOR TYPE
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INPUT FILE POSITION LEGEND: J2L



SPECIAL DETECTOR NOTE

Install a multizone microwave detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

Temporary Signal - TMP Phase I & II

Electrical Detail - Sheet 1 of 2

**DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED**