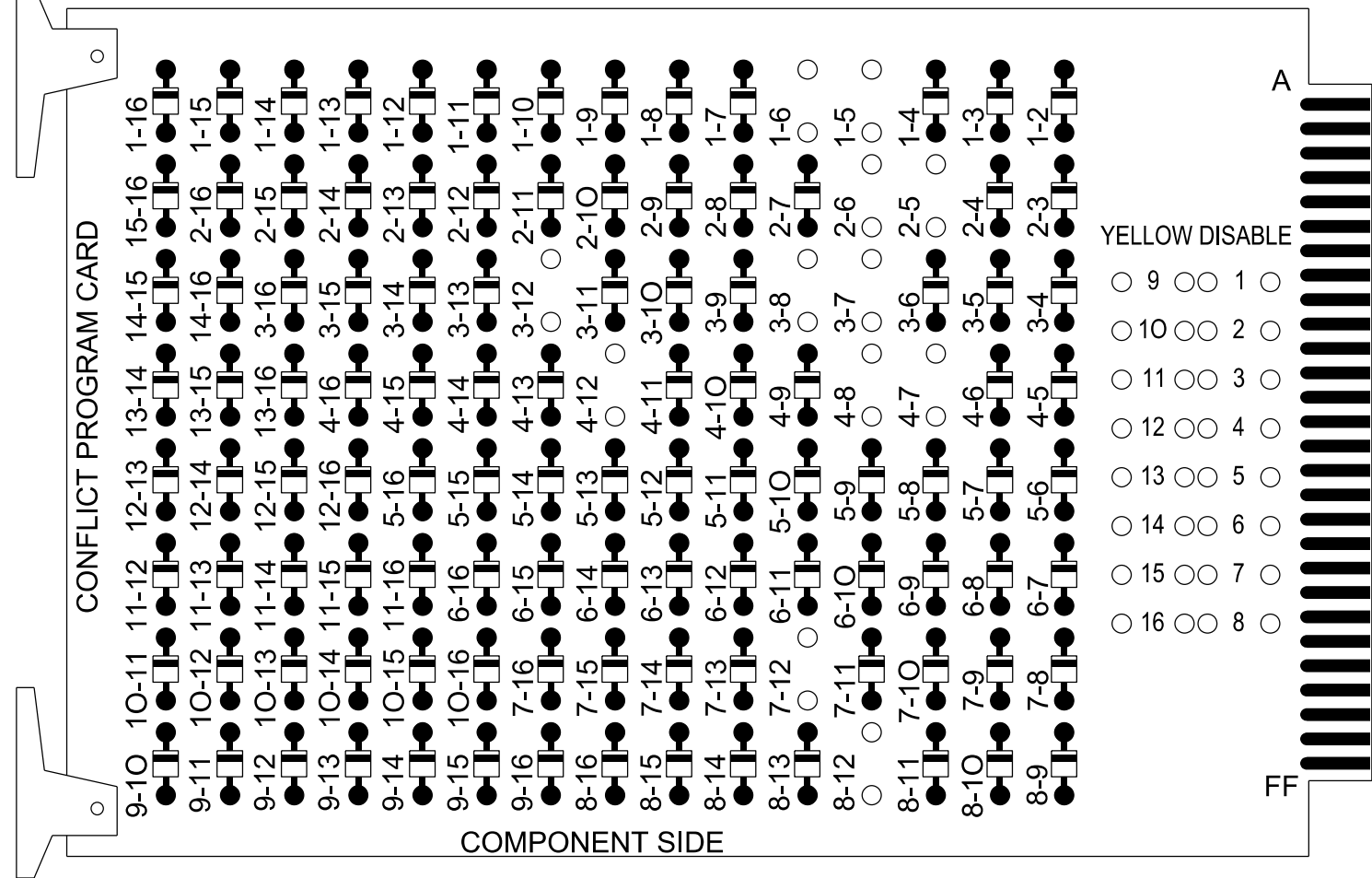


16 CHANNEL CONFLICT MONITOR
PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 2-5, 2-6, 3-7, 3-8, 3-12, 4-7, 4-8, 4-12, 7-12, and 8-12.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Make sure jumpers SEL2-SEL5 are present on the monitor board.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Program controller to start up in phase 2 Green No Walk and phase 6 Green No Walk.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

Controller.....2070LX
Cabinet.....332 w/ Aux
Software.....Q-Free MAXTIME
Cabinet Mount.....Base
Output File Positions.....18 With Aux. Output File
Load Switches Used.....S1,S2,S4,S5,S7,S8,S10,S11,AUXS5
Phases Used.....1,2,3,4,5,6,7,8
Overlap "4".....*

*See overlap programming detail on this sheet.

SIGNAL HEAD HOOK-UP CHART

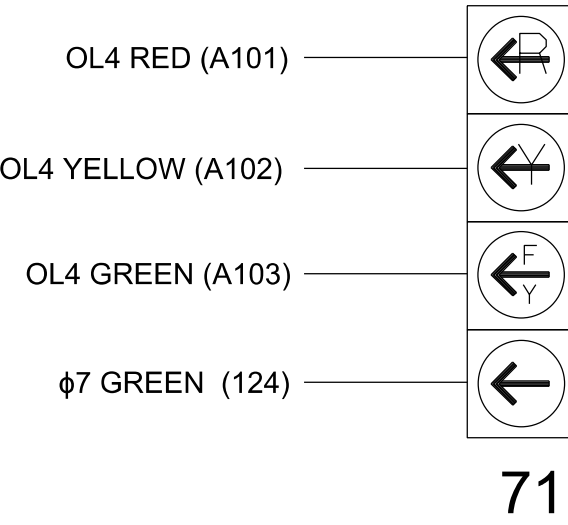
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	11	21,22	NU	22	31	41,42	NU	51	61,62	NU	71	81,82	NU	NU	NU	NU	NU	71
RED		128				101			134			107						
YELLOW		129				102			135		*	108						
GREEN		130				103			136			109						
RED ARROW	125				116			131										A101
YELLOW ARROW	126			117	117			132										A102
FLASHING YELLOW ARROW																		A103
GREEN ARROW	127			118	118			133			124							

NU = Not Used

- * Denotes install load resistor. See load resistor installation detail this sheet.
- * See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

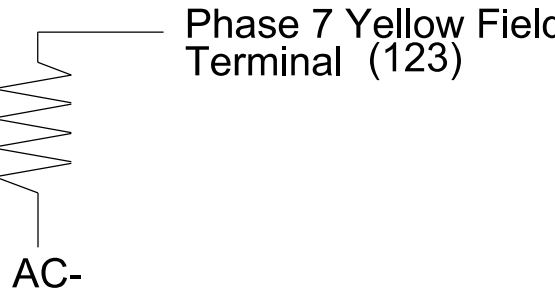
(wire signal heads as shown)



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

ACCEPTABLE VALUES	
Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



INPUT FILE POSITION LAYOUT

(front view)

	1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I" U	1A	2A	SLOT	SLOT	3A	4A	SLOT	SLOT	SLOT	SLOT	SLOT	SLOT	SLOT	FS
L	NOT USED	NOT USED	EMPTY	EMPTY	NOT USED	NOT USED	EMPTY	EMPTY	EMPTY	EMPTY	EMPTY	EMPTY	EMPTY	DC ISOLATOR
FILE "J" U	5A	6A	SLOT	SLOT	7A	8A	SLOT	SLOT	SLOT	SLOT	SLOT	SLOT	SLOT	FS
L	NOT USED	6B	EMPTY	EMPTY	NOT USED	NOT USED	EMPTY	EMPTY	EMPTY	EMPTY	EMPTY	EMPTY	EMPTY	DC ISOLATOR

EX. : 1A, 2A, ETC. = LOOP NO.'S

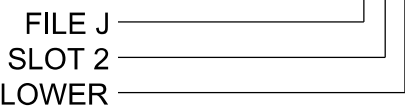
FS = FLASH SENSE
ST = STOP TIME

⊗ Remove jumper in place from J5-W to I8-W, on rear of input file.

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
1A	TB2-1,2	I1U	56	18	1	1	3.0		X		X	
2A	TB2-5,6	I2U	39	1	2	2			X		X	
3A	TB4-5,6	I5U	58	20	7	3	3.0		X		X	
4A	TB4-9,10	I6U	41	3	8	4	10.0		X		X	
5A	TB3-1,2	J1U	55	17	15	5	3.0		X		X	
6A	TB3-5,6	J2U	40	2	16	6			X		X	
6B	TB3-7,8	J2L	44	6	17	6			X		X	
7A	TB5-5,6	J5U	57	19	21	7	15.0		X		X	
				-	32	4	3.0		X		X	
8A	TB5-9,10	J6U	42	4	22	8			X		X	

INPUT FILE POSITION LEGEND: J2L



OVERLAP PROGRAMMING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	4
Type	FYA 4 - Section
Included Phases	8
Modifier Phases	7
Modifier Overlaps	-
Trail Green	0
Trail Yellow	0.0
Trail Red	0.0

MAXTIME STARTUP AND SOFTWARE FLASH
PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Unit

Web Interface

Home >Controller >Unit

Modify parameters as shown below and save changes.

Start Up Parameters

StartUp Clearance Hold
6

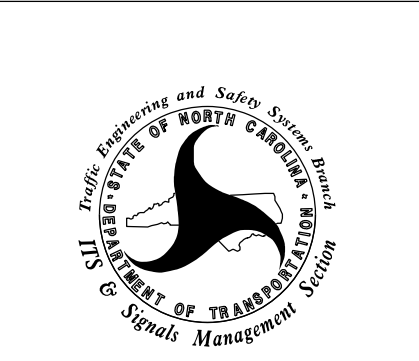
Unit Flash Parameters

All Red Flash Exit Time
6

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 01-0129
DESIGNED: October 2024
SEALED: 10/28/2024
REVISED: N/A

Electrical Detail

ELECTRICAL AND PROGRAMMING
DETAILS FOR:



750 N. Greenfield Pkwy, Garner, NC 27529

NC 46(Lawrenceville Rd
/Garysburg Rd) at
NC 48 (Pleasant Hill Rd
/Roanoke Rapids Rd)

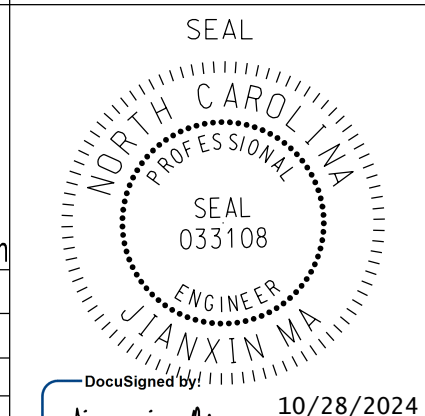
Division 1 Northampton County Gaston

PLAN DATE: October 2024 REVIEWED BY: J. L. Lewis

PREPARED BY: J. Ma REVIEWED BY:

REVISIONS INIT. DATE

DOCUMENT NOT CONSIDERED
FINAL UNLESS ALL
SIGNATURES COMPLETED



DocuSigned by: 10/28/2024

SIG. INVENTORY NO. 01-0129