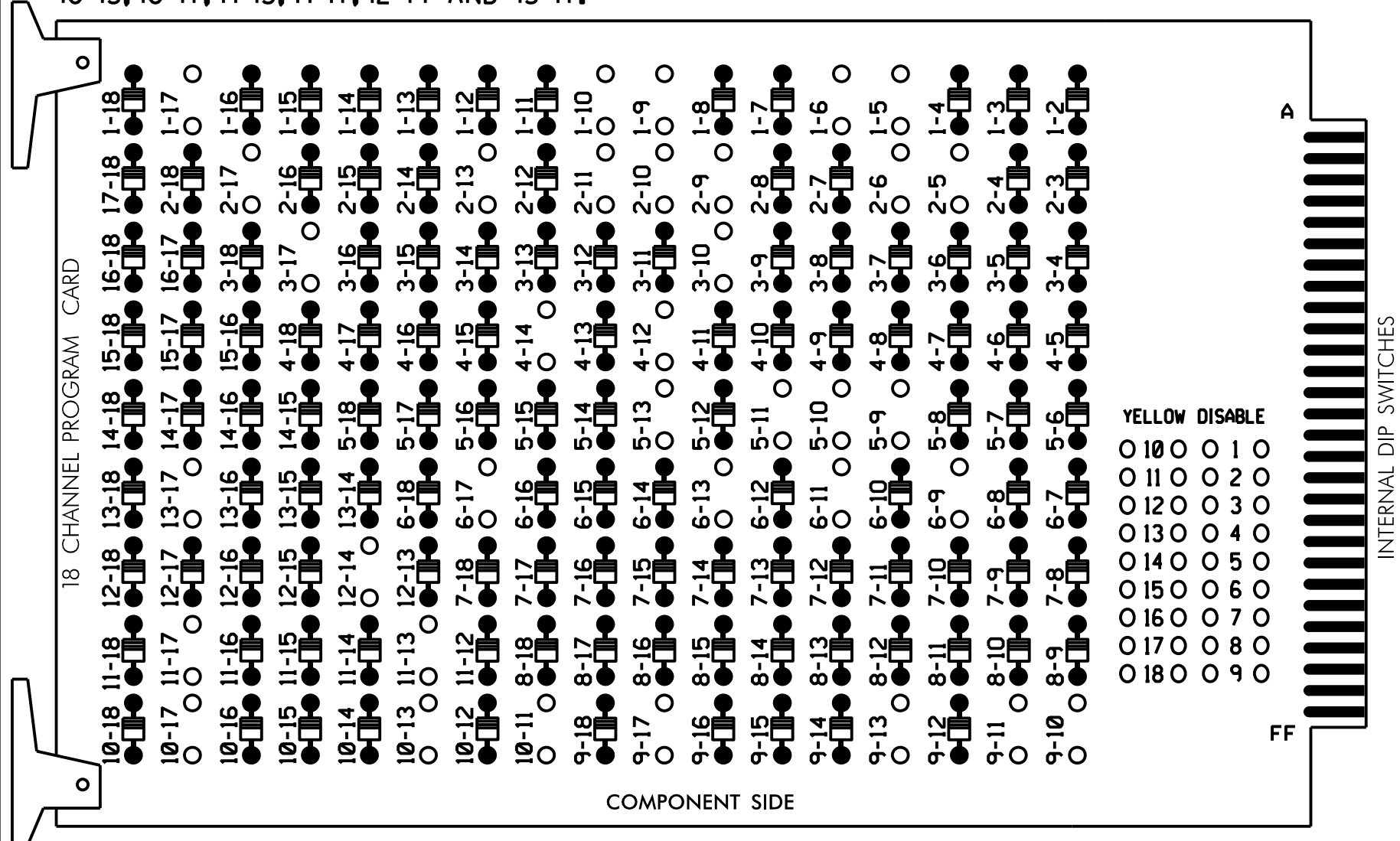


18 CHANNEL IP CONFLICT MONITOR

PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS I-5, I-6, I-9, I-10, I-17, 2-5, 2-6, 2-9, 2-10, 2-11, 2-13, 2-17, 3-10, 3-17, 4-12, 4-14, 5-9, 5-10, 5-11, 5-13, 6-9, 6-11, 6-13, 6-17, 9-10, 9-11, 9-13, 9-17, 10-11, 10-13, 10-17, 11-13, 11-17, 12-14 AND 13-17.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.

■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all Phases.
- Disable all phases for Startup in Green and Yellow Flash.
- Program phases 2 and 6 as First Phases.
- Program overlaps 1, 2 and 5 as Wag Overlaps.
- The cabinet and controller are part of the Jacksonville City Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070
CABINET.....332 W/ AUX
SOFTWAREECONOLITE OASIS
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS..18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S6,S7,S8,AUX S1
AUX S2,AUX S3,AUX S4,AUX S5
PHASES USED.....1,2,2 PED,3,4,4 PED,5,6
OVERLAP "A".....1+2
OVERLAP "B".....3+5
OVERLAP "C".....2
OVERLAP "D".....4
OVERLAP "E".....3+6

PROJECT REFERENCE NO.	SHEET NO.
U-5789	Sig. 2.2

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	OLE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	21,22	P21, P22	31	32,34	41,42	43,44, 45	P41, P42	51,52	61,62	NU	NU	NU	11	33	63	23,24	45
RED		128		116	116		101			134					A124	A111	A114	A101
YELLOW	*	129		117	117		102			135								
GREEN		130		118	118		103			136								
RED ARROW						101			131					A121				
YELLOW ARROW						102			132					A122	A125	A112	A115	A102
FLASHING YELLOW ARROW														A123	A126	A113	A116	A103
GREEN ARROW	127			118		103			133									
Hand icon			113					104										
Walking person icon			115					106										

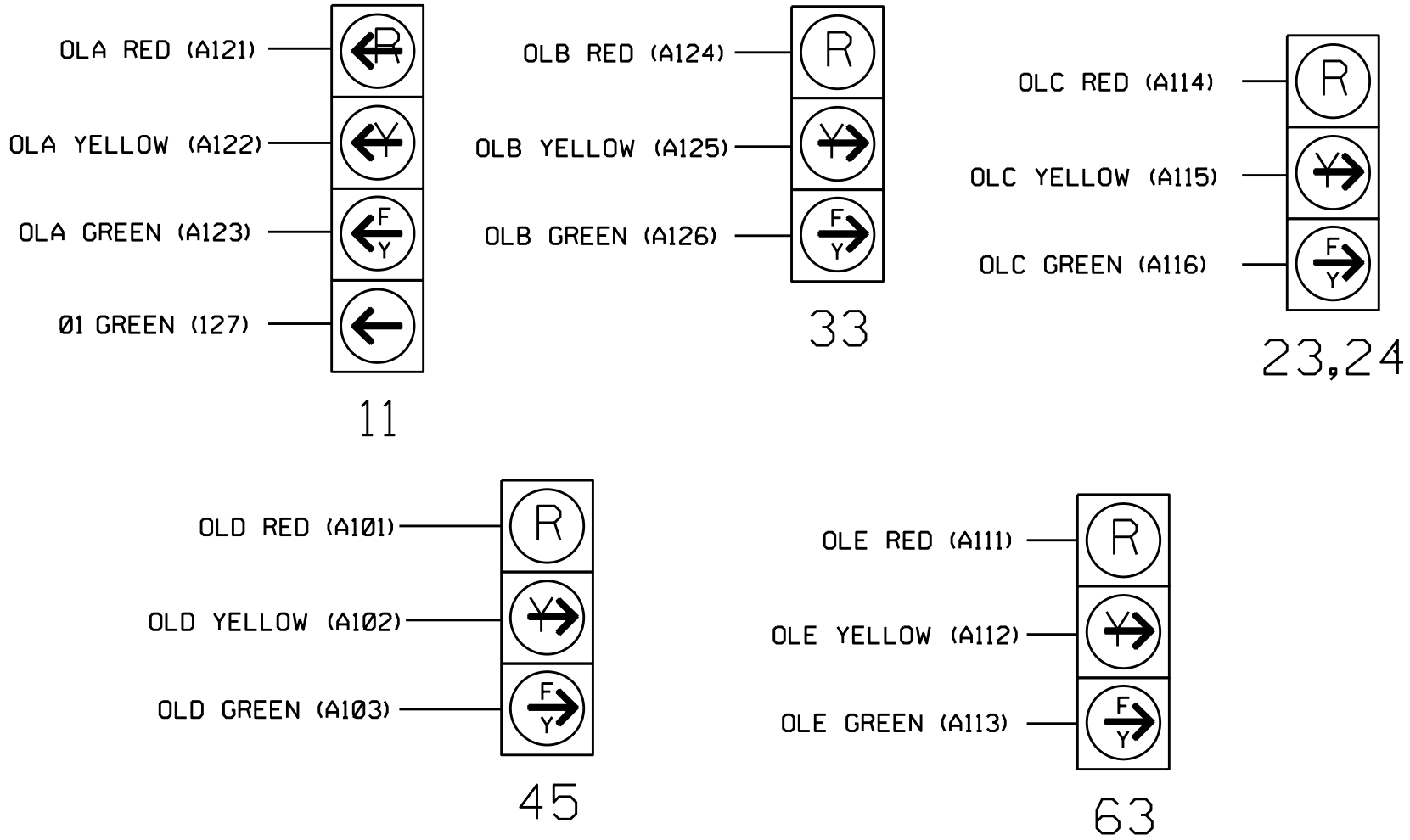
NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

- The sequence display for signal head 11 requires special logic programming. See sheet 3 for programming instructions.

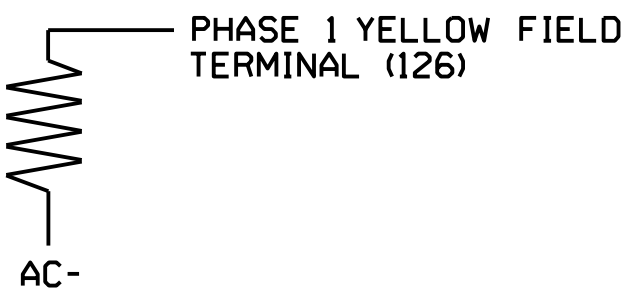
Project #: 230907

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-0822
DESIGNED: May 2025
SEALED: 07/07/2025
REVISED: N/A

DAVENPORT
HOME OFFICE:
119 BROOKSTOWN AVENUE, SUITE PH1
WINSTON-SALEM, NC 27101
336.744.1636 www.davenportworld.com
NCBELS FIRM LICENSE NO. C-2622

LOAD RESISTOR INSTALLATION DETAIL

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Note: Remove load resistors from terminals 132 and A124 if present.

SPECIAL DETECTOR NOTE

Install a multizone microve detection zone for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

For Detection Zone 1A, the equipment placement and slots reserved for wired inputs are typical for NCDOT installation. Inputs associated with these slots are compatible with the time of day instructions located on sheet 4 of this electrical detail.

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	11U	56	18	1	1	Y	Y			15
	-	J4U	48	10 ★	26	6	Y	Y			
	-	11U	56	18 ★	51	1	Y	Y			
PED PUSH BUTTONS											
P21,P22	TB8-4,6	112U	67	29	PED 2	2 PED					
P41, P42	TB8-5,6	112L	69	31	PED 4	4 PED					

NOTE: INSTALL DC ISOLATOR IN INPUT FILE SLOT 112.

¹Add jumper from I1-W to J4-W, on rear of input file.

★ See Input Page Assignment programming detail on sheet 4.

