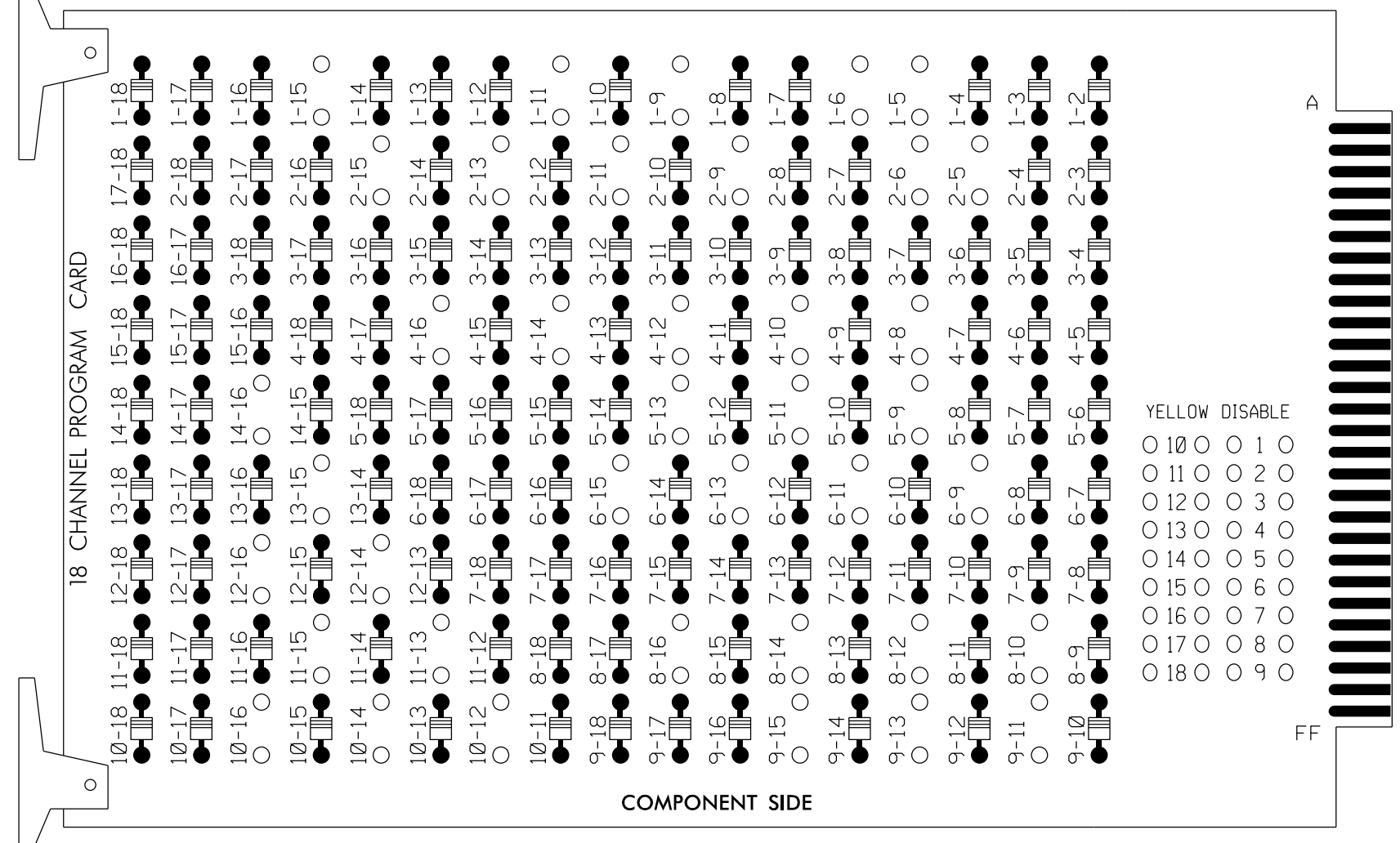


18 CHANNEL IP CONFLICT MONITOR

PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 1-15, 2-5, 2-6, 2-9, 2-11, 2-13, 2-15, 4-8, 4-10, 4-12, 4-14, 4-16, 5-9, 5-11, 5-13, 6-9, 6-11, 6-13, 6-15, 8-10, 8-12, 8-14, 8-16, 9-11, 9-13, 9-15, 10-12, 10-14, 10-16, 11-13, 11-15, 12-14, 12-16, 13-15 and 14-16.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Program controller to start up in phase 2 Green and 6 Green.
- The cabinet and controller are part of the High Point Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070LX
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE ASC/3-2070
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S3,S5,S6,S7,S8,S9,S11,S12,
 AUX S1,AUX S2,AUX S4,AUX S5
 PHASES USED.....1,2,2 PED,4,4 PED,5,6,6 PED,8,8 PED
 OVERLAP "A".....*
 OVERLAP "B".....*
 OVERLAP "C".....*
 OVERLAP "D".....*

* See overlap programming detail on sheet 2

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	11	21,22	P21, P22	NU	42, 43,44	P41, P42	43	51	61,62	P61, P62	NU	82, 83,84	P81, P82	11	81	NU	51	41	NU
RED	128				101		*		134			107							
YELLOW	*	129			102				135			108							
GREEN		130			103				136			109							
RED ARROW													A121	A124		A114	A101		
YELLOW ARROW							132						A122	A125		A115	A102		
FLASHING YELLOW ARROW													A123	A126		A116	A103		
GREEN ARROW	127						133	133											
Hand icon			113			104				119		110							
Person icon			115			106				121		112							

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 ★ See pictorial of head wiring in detail below.

INPUT FILE POSITION LAYOUT

(front view)

FILE	U	1	2	3	4	5	6	7	8	9	10	11	12	13	14
"I"	U	∅ 1	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	∅ 2 PED	∅ 6 PED	FS
	L	1A	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR
"J"	U	∅ 5	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	∅ 4 PED	∅ 8 PED	ST
	L	5A	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

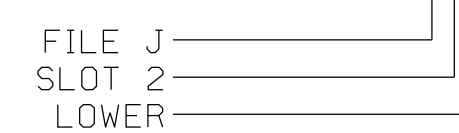
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	ADDED INITIAL	DETECTOR TYPE
1A ¹	TB2-1,2	I1U	56	1 ★	1	YES		15.0		N
	-	J4U	48	26 ★	6	YES		3.0		G
5A ²	TB3-1,2	J1U	55	5 ★	5	YES		15.0		N
	-	I4U	47	22 ★	2	YES		3.0		G
PED PUSH BUTTONS										
P21,P22	TB8-4,6	I12U	67	PED 2	2 PED					
P41,P42	TB8-5,6	I12L	69	PED 4	4 PED					
P61,P62	TB8-7,9	I13U	68	PED 6	6 PED					
P81,P82	TB8-8,9	I13L	70	PED 8	8 PED					

NOTE:
 INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

- Add jumper from I1-W to J4-W, on rear of input file.
- Add jumper from J1-W to I4-W, on rear of input file.

★ For the detectors to work as shown on the signal design plan. See the Vehicle Detector Setup Programming Detail for Alternate Phasing on sheet 2.

INPUT FILE POSITION LEGEND: J2L

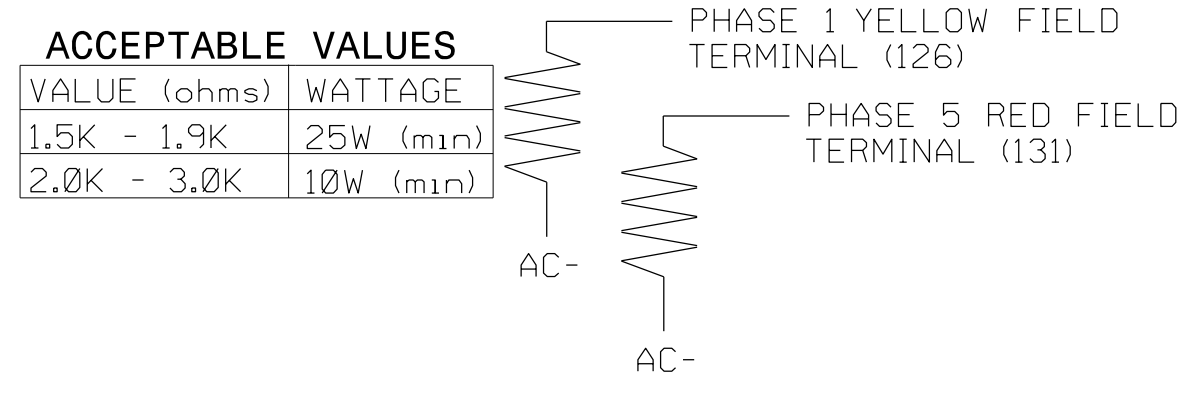


SPECIAL DETECTOR NOTE

- Install a non-intrusive detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.
- For detection areas 1A and 5A detector card placement and slots reserved for wired inputs are typical for a NCDOT installation. Inputs associated with these slots are compatible with time of day instructions located on sheets 2 and 3 of this electrical detail.

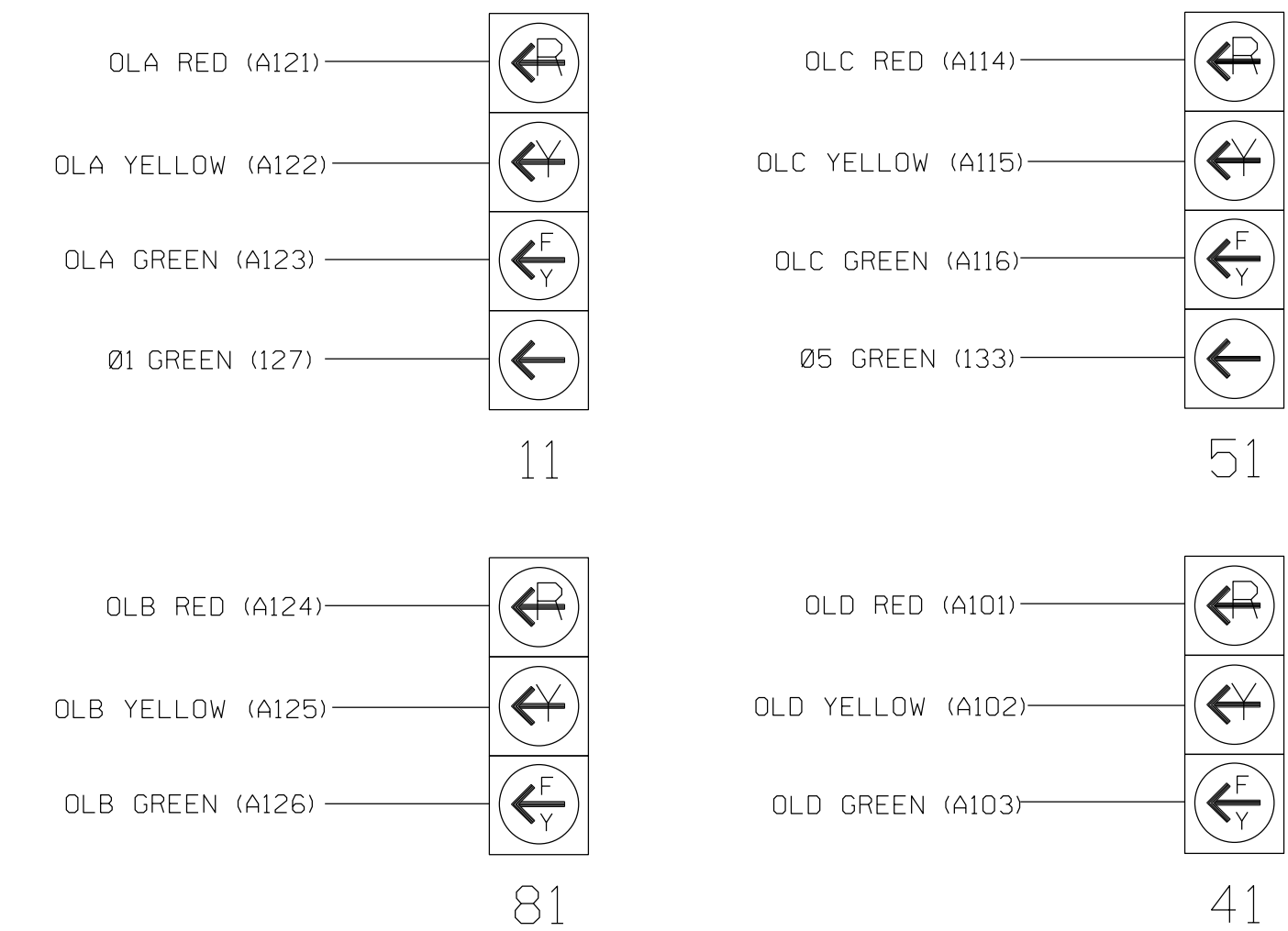
LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)



FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-0935T5
 DESIGNED: January 2025
 SEALED: 03-14-2025
 REVISED: N/A

Electrical Detail - Sheet 1 of 4 Temporary Design 5

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared for the Offices of: 	SR 1820 (Skeet Club Road) at SR 1818 (Johnson Street)		DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SEAL
	Division 7 PLAN DATE: January 2025 PREPARED BY: JT Stiff	Guilford County HIGH POINT REVIEWED BY: AM Encarnacion REVIEWED BY: PL Alexander	

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