

5/9/2025 11:34:37 AM susan.pennington K:\RAL_TPTD\ SIGNALS\011036730 U-4913A\51 - Signal Design\DRW\1 U-4913A-102050-2025e.dgn

MAXTIME OVERLAP PROGRAMMING DETAIL
FOR DEFAULT PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

| | |
|-------------------|-----------------|
| Overlap | 1 |
| Type | FYA 4 - Section |
| Included Phases | 2 |
| Modifier Phases | 1 |
| Modifier Overlaps | * |
| Trail Green | 0 |
| Trail Yellow | 0.0 |
| Trail Red | 0.0 |

MAXTIME OVERLAP PROGRAMMING DETAIL
FOR ALTERNATE PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

| | |
|-------------------|-----------------|
| Overlap | 1 |
| Type | FYA 4 - Section |
| Included Phases | * |
| Modifier Phases | 1 |
| Modifier Overlaps | * |
| Trail Green | 0 |
| Trail Yellow | 0.0 |
| Trail Red | 0.0 |

NOTICE INCLUDED PHASE

MAXTIME STARTUP AND SOFTWARE FLASH
PROGRAMMING DETAIL

Front Panel
Main Menu >Controller >Unit

Web Interface
Home >Controller >Unit

Modify parameters as shown below and save changes.

Start Up Parameters

| |
|------------------------|
| StartUp Clearance Hold |
| 6 |

Unit Flash Parameters

| |
|-------------------------|
| All Red Flash Exit Time |
| 6 |

MAXTIME DETECTOR PROGRAMMING DETAIL
FOR ALTERNATE PHASING LOOP 1A

Front Panel
Main Menu >Controller >Detector >Veh Det Plans

Web Interface
Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

| | | |
|----------|------------|-------|
| Detector | Call Phase | Delay |
| 1 | 1 | 3.0 |
| 29 | 0 | 0 |

1A

OUTPUT CHANNEL CONFIGURATION

Front Panel
Main Menu >Controller >More>Channels>Channels Config

Web Interface
Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration

| Channel | Control Type | Control Source | Flash Yellow | Flash Red | Flash Alt | MMU Channel |
|---------|---------------|----------------|--------------|-----------|-----------|-------------|
| 1 | Phase Vehicle | 1 | - | X | X | 1 |
| 2 | Phase Vehicle | 2 | - | X | - | 2 |
| 3 | Phase Vehicle | 3 | - | X | X | 3 |
| 4 | Phase Vehicle | 4 | - | X | - | 4 |
| 5 | Phase Vehicle | 5 | - | X | - | 5 |
| 6 | Phase Vehicle | 6 | - | X | X | 6 |
| 7 | Phase Vehicle | 7 | - | X | - | 7 |
| 8 | Phase Vehicle | 8 | - | X | X | 8 |
| 9 | Overlap | 1 | - | X | X | 9 |
| 10 | Overlap | 2 | - | X | X | 10 |
| 11 | Overlap | 3 | - | X | - | 11 |
| 12 | Overlap | 4 | - | X | - | 12 |
| 13 | Phase Ped | 2 | - | - | - | 13 |
| 14 | Phase Ped | 4 | - | - | - | 14 |
| 15 | Phase Ped | 6 | - | - | - | 15 |
| 16 | Phase Ped | 8 | - | - | - | 16 |
| 17 | Overlap | 5 | - | X | X | 17 |
| 18 | Overlap | 6 | - | X | - | 18 |

NOTICE: FLASH RED

MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING

ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING

OVERLAP PLAN

1

VEH DET PLAN

1

ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING

2

2

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases for head 11 to run protected turns only.

VEH DET PLAN 2: Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 3 seconds.

MAXTIME ALTERNATE PHASING PATTERN
PROGRAMMING DETAIL

Front Panel
Main Menu >Controller >Coordination >Patterns

Web Interface
Home >Controller >Coordination >Patterns

Pattern Parameters

| | | |
|---------|--------------|--------------|
| Pattern | Veh Det Plan | Overlap Plan |
| * | 2 | 2 |

*The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 10-2050
DESIGNED: February 2025
SEALED: 05/12/2025
REVISED: N/A

Electrical Detail - Sheet 2 of 2

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SR 3174 (Idlewild Road)
at
I-485 Inner Loop Ramps

Division 10 Wecklenburg County Stallings

PLAN DATE: February 2025 REVIEWED BY: KP Baumann

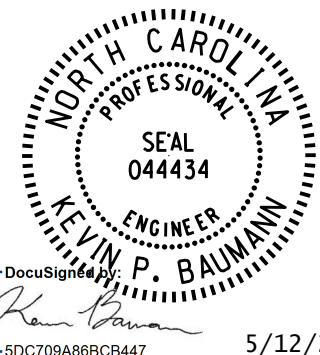
PREPARED BY: SP Pennington REVIEWED BY:

REVISIONS INIT. DATE

DATE

DOCUMENT NOT CONSIDERED
FINAL UNLESS ALL
SIGNATURES COMPLETED

SEAL



DATE

SIG. INVENTORY NO. 10-2050