U-5798B Sig. 4.3

MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP PLAN 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases

for heads 31 and 71 to run protected turns only.

VEH DET PLAN 2: Disables phase 8 call on loop 3A

and reduces delay time for phase 3 call on loop 3A to 3 seconds.

Disables phase 4 call on loop 7A and reduces delay time for phase 7 call on loop 7A to 3 seconds.

OUTPUT CHANNEL CONFIGURATION

Front Panel

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channel Configuration

Channel Configuration

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Phase Vehicle	1		X	Х	1
2	Phase Vehicle	2		Х		2
3	Phase Vehicle	3		Х	Х	3
4	Phase Vehicle	4		X		4
5	Phase Vehicle	5		Х		5
6	Phase Vehicle	6		Х	Х	6
7	Phase Vehicle	7		X		7
8	Phase Vehicle	8		X	Х	8
9	Overlap	1		X	Х	9
10	Overlap	2		X	Χ	10
11	Overlap	3		X		11
12	Overlap	4		X		12
13	Phase Ped	2				13
14	Phase Ped	4				14
15	Phase Ped	6				15
16	Phase Ped	8				16
17	Overlap	5		X	Х	17
18	Overlap	6		Х		18

MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 3A & 7A

Front Panel

Main Menu >Controller >Detector >Veh Det Plans

Web Interface

Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

3A

Detector	Call Phase	Delay
7	3	3:0
30	0	±

7A

Detector	Call Phase	Delay
21	7	3:0
32	0	<u>*</u>

MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Coordination >Patterns

Web Interface

Home >Controller >Coordination >Patterns

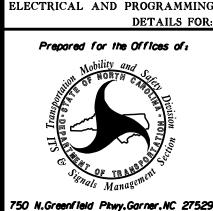
Pattern Parameters

Pattern	Veh Det Plan	Overlap Plan
*	2	2

*The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 06-1131
DESIGNED: June 2025
SEALED: 6/12/2025
REVISED: _____

Signal Upgrade - Final Electrical Detail - Sheet 3 of 3



SR 1102 (Gillis Hill Road) at SR 1112 (Stoney Point Road)

Division 6 Cumberland County Fayetteville
PLAN DATE: June 2025 REVIEWED BY: 0. Drobny
PREPARED BY: S.G. Haynie REVIEWED BY:
REVISIONS INIT. DATE

robny

T. DATE

Steven

Docusigned

Steven

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SEAL
029531

Docusigned of G. Haynic 6/12/2025

DOGSTENDATURE

1G. INVENTORY NO. 06-1131

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

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