

13-MAR-2025 17:32
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S1F4685 - AT L05491289

ECONOLITE ASC/3-2070
OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER
2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

Toggle to 'Overlap D'
OVERLAP D

Select TMG VEH OVLP [D] and 'NORMAL'

TMG VEH OVLP...[D] TYPE:NORMAL

PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6

INCLUDED . . . X X

LAG GRN 0.0 YEL 0.0 RED 0.0

END PROGRAMMING

ECONOLITE ASC/3-2070 STARTUP AND
SOFTWARE FLASH PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER
2. From CONTROLLER Submenu select 5. START/FLASH

START/FLASH DATA

-----START UP-----

1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6

PHASE G G

A B C D E F G H I J K L M N O P

OVERLAP X X X X X X X X X X X X X X X X

FLASH>MON. NO FL TIME.. 0 ALL RED... 6

PWR START SEQ.. 1 MUTCD> YES Y- G: NO

Scroll down on this screen and set "Exit Fl" to Green "G"

ASC/3 FLASH SENSE INPUT CONTROL
FOR RED-RED FLASH

*The NCDOT default database is programmed to addresss Yellow-Red flash.
Logic Statement 100 must be modified as shown when running Red-Red flash.

1. From Main Menu select 1. CONFIGURATION
2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR
3. From LOGIC PROCESSOR Submenu select 2. LOGIC STATEMENTS

Change the "LP" to 100 and move the cursor down. Delete the two "CTR-SET" statements by moving the cursor over them and hitting the "C" key. then hit "ENTER", select "LP SET CIB ON", hit "ENT", and then set the number to 427.

LP#:100 COPY FROM:100 ACTIVE: M FALSE

IF LP CIB CODE ON 331 F

THEN LP DELAY FOR 1.0 SECONDS

LP SET CIB ON 427

ELSE

THIS STATEMENT IS USED
TO CONTROL THE FLASH
SENSE INPUT WHEN RUNNING
RED-RED FLASH OPERATION.

Hit "ESC", then 1 for "LOGIC STATEMENT CONTROL",
next verify that LP#100 is ENABLED.

END PROGRAMMING

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE
SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA – REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA – REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 07-1120
DESIGNED: January 2025
SEALED: 03-14-2025
REVISED: N/A

AtkinsRéalis

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Electrical Detail - Sheet 2 of 2

Prepared for the Offices of:

ALL Transportation Mobility and Safety Projects
EXCEPT OR TRANSPORTATION
& Signal Management Systems

ELECTRICAL AND PROGRAMMING
DETAILS FOR:

SR 1845 (Tyner Road)
at
Piedmont Triad Farmers Market

Division 7 Guilford County High Point

PLAN DATE: January 2025 REVIEWED BY: AM Encarnacion

PREPARED BY: JT Stiff REVIEWED BY: PL Alexander

REVISIONS INIT. DATE

750 N.Greenfield Pkwy,Garner,NC 27529

DOCUMENT NOT CONSIDERED
FINAL UNLESS ALL
SIGNATURES COMPLETED

SEAL

SEAL

044476

ENGINEER

Anthony Encarnacion

3/14/2025

SIGNATURE DATE

SIG. INVENTORY NO. 07-1120