(program controller as shown)

1. From Main Menu select 2. CONTROLLER

2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

Toggle to 'Overlap F' OVERLAP F

Select TMG VEH OVLP [F] and 'NORMAL'

```
TMG VEH OVLP...[F] TYPE: ......NORMAL
PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
LAG GRN 0.0 YEL 0.0 RED 0.0
                     Toggle Once
```

OVERLAP G

Select TMG VEH OVLP [G] and 'NORMAL'

```
TMG VEH OVLP...[G] TYPE: ......NORMAL
PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
LAG GRN 0.0 YEL 0.0 RED 0.0
```

OVERLAP H

Select TMG VEH OVLP [H] and 'NORMAL'

```
TMG VEH OVLP...[H] TYPE: ......NORMAL
 PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
LAG GRN 0.0 YEL 0.0 RED 0.0
                 Toggle to 'Overlap B'
```

OVERLAP B

Select TMG VEH OVLP [B] and 'OTHER/ECONOLITE'

```
TMG VEH OVLP...[B] TYPE: OTHER/ECONOLITE
PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
LAG GRN 0.0 YEL 0.0 RED 0.0 ADV GRN 0.0
```

Toggle Once

Toggle Once

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

```
TMG VEH OVLP...[C] TYPE: .... PPLT FYA
PROTECTED LEFT TURN.... OVERLAP G
OPPOSING THROUGH..... PHASE 2
FLASHING ARROW OUTPUT....CH11 ISOLATE
DELAY START OF: FYA..O.O CLEARANCE..O.O
ACTION PLAN SF BIT DISABLE..... 0
                           Toggle Once
```

OVERLAP D

Select TMG VEH OVLP [D] and 'PPLT FYA'

```
TMG VEH OVLP...[D] TYPE: .... PPLT FYA
PROTECTED LEFT TURN.... OVERLAP H
OPPOSING THROUGH..... PHASE 4
FLASHING ARROW OUTPUT....CH12 ISOLATE
DELAY START OF: FYA..O.O CLEARANCE..O.O
ACTION PLAN SF BIT DISABLE..... 0
```

END PROGRAMMING

PROJECT REFERENCE NO. SHEET NO. U-4758 | Sig. 22.2

## FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM, T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.
- THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

## COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø7-1118 DESIGNED: January 2025 SEALED: 03-14-2025 REVISED: N/A

Electrical Detail - Sheet 2 of 3

ELECTRICAL AND PROGRAMMING Prepared for the Offices of: 750 N.Greenfield Pkwy,Garner,NC 27529 SR 1850 (Sandy Ridge Road)

SR 1849 (Norcross Road)/ Piedmont Triad Farmers Market Division 7 Guilford County PLAN DATE: January 2025 REVIEWED BY: AM Encarnacion

PREPARED BY: JT Stiff REVIEWED BY: PL Alexander REVISIONS INIT. DATE

DOCUMENT NOT CONSIDERED

FINAL UNLESS ALL SIGNATURES COMPLETED

SIG. INVENTORY NO. 07-1118

**G** AtkinsRéalis 1616 EAST MILLBROOK ROAD, SUITE 160 RALEIGH, NORTH CAROLINA 27609