

ECONOLITE ASC/3-2070 LOGIC PROCESSOR PROGRAMMING DETAIL
FOR LEADING PED INTERVAL (DELAYED GREEN)

(program controller as shown)

The following logic processor configuration holds the FYA on signal head 11 red for the duration of the delayed green time (leading ped interval) when serving a ped call on the opposing through phase and serve phase 4 instead of phase 3 if there is a phase 4 ped call.

1. From Main Menu select 1. CONFIGURATION
2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR
3. From the LOGIC PROCESSOR Submenu select 2. LOGIC STATEMENTS

ENTER A "1" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP#: 1 COPY FROM: 1 ACTIVE: M (T/F)

IF PED ON PH WALK 2 IS ON

AND VEH GREEN ON PH 2 IS OFF

THEN SIG SET OLP RED 1 ON

SIG SET OLP YELLOW 1 OFF

SIG SET OVLP GREEN 1 OFF

ELSE

HOLD SIGNAL HEAD 11 FYA RED DURING THE PHASE 2 DELAYED GREEN TIME (LEADING PED INTERVAL)

ENTER A "2" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP#: 2 COPY FROM: 2 ACTIVE: M (T/F)

IF CTR ON PH PED CHK 4 IS ON

THEN CTR OMIT PHASE 3 ON

ELSE

LOGIC TO SERVE PHASE 4 INSTEAD OF PHASE 3 IF THERE IS A PHASE 4 PED CALL.

1. From Main Menu select 1. CONFIGURATION
2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR
3. From the LOGIC PROCESSOR Submenu select 1. LOGIC STATEMENT CONTROL

ENABLE LOGIC PROCESSOR STATEMENTS 1-4 BY POSITIONING THE CURSOR OVER THE FIELDS SHOWN BELOW AND USING THE TOGGLE KEY TO ENABLE THEM .

LOGIC STATEMENT CONTROL

1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6

LP 1-15 E E

LP 16-30

LP 31-45

LP 46-60

LP 61-75

LP 76-90

END PROGRAMMING

ECONOLITE ASC/3-2070 STARTUP AND
SOFTWARE FLASH PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER
2. From CONTROLLER Submenu select 5. START/FLASH

START/FLASH DATA

-----START UP-----

1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6

PHASE G G

A B C D E F G H I J K L M N O P

OVERLAP X X X X X X X X X X X X X X X X

FLASH>MON. NO FL TIME.. 0 ALL RED... 6

PWR START SEQ.. 1 MUTCD> YES Y- G: NO

Scroll down on this screen and set "Exit Fl" to Green "G"

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1116
DESIGNED: January 2025
SEALED: 03-14-2025
REVISED: N/A

ECONOLITE ASC/3-2070 BACKUP
PROTECTION ENABLE PROGRAMMING

(program controller as shown)

1. From Main Menu select 1. CONFIGURATION
2. From CONFIGURATION Submenu select 1. CONTROLLER SEQ
3. From CONTROLLER SEQUENCE Submenu select 3. BACKUP PREVENT PHASES

Follow programming as shown below. On the 'ENABLE BACKUP PREVENT' screen move cursor to the appropriate field and press 'YES/NO' on the controller keypad to toggle field value between 'X' and 'OFF'.

ENABLE BACKUP PREVENT

TMG/BKUP 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6

1

2

3 . X

4

5

6

7

8

9

10

11

12

13

14

15

16

END PROGRAMMING

NOTES

1. 'X' inhibits the controller from servicing the 'BACKUP' (column) phase when the 'TIMING' (row) phase is active or next.

ASC/3 FLASH SENSE INPUT CONTROL
FOR RED-RED FLASH

*The NCDOT default database is programmed to addresss Yellow-Red flash. Logic Statement 100 must be modified as shown when running Red-Red flash.

1. From Main Menu select 1. CONFIGURATION
2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR
3. From LOGIC PROCESSOR Submenu select 2. LOGIC STATEMENTS

Change the "LP" to 100 and move the cursor down. Delete the two "CTR-SET" statements by moving the cursor over them and hitting the "C" key. then hit "ENTER", select "LP SET CIB ON", hit "ENT", and then set the number to 427.

LP#:100 COPY FROM:100 ACTIVE: M FALSE

IF LP CIB CODE ON 331 F

THEN LP DELAY FOR 1.0 SECONDS

LP SET CIB ON 427

ELSE

THIS STATEMENT IS USED TO CONTROL THE FLASH SENSE INPUT WHEN RUNNING RED-RED FLASH OPERATION.

Hit "ESC", then 1 for "LOGIC STATEMENT CONTROL", next verify that LP#100 is ENABLED.

END PROGRAMMING

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Electrical Detail - Sheet 4 of 4

Electrical AND PROGRAMMING DETAILS FOR:

Prepared for the Offices of:

ALL Transportation Mobility and Safety Projects
PROJECT OR TRANSPORTATION & Signal Management System

SR 1850 (Sandy Ridge Road)
at
SR 1844 (National Serivce Road)

Division 7 Guilford County High Point

PLAN DATE: January 2025 REVIEWED BY: AM Encarnacion

PREPARED BY: JT Stiff REVIEWED BY: PL Alexander

REVISIONS INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

SEAL 044476

ENGINEER

Anthony Encarnacion

3/14/2025

SIG. INVENTORY NO. 07-1116

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