ECONOLITE ASC/3-2070 LOGIC PROCESSOR PROGRAMMING DETAIL FOR LEADING PED INTERVAL (DELAYED GREEN)

(program controller as shown)

The following logic processor configuration holds the FYA's on signal heads 11, 31, 51, and 71 red for the duration of the delayed green time (leading ped interval) when serving a ped call on the opposing through phase.

1. From Main Menu select | 1. CONFIGURATION

2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR

3. From the LOGIC PROCESSOR Submenu select 2. LOGIC STATEMENTS

ENTER A "1" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP# :	1	COPY FROM:	1	ACTIVE:	М	(T/F)
I F AND		ON PH WALK Green on Ph		2 2	IS IS	
THEN		SET OLP RED		1		ON
		SET OLP YELLO SET OVLP GREE		1 1		OF F OF F
ELSE						

HOLD SIGNAL HEAD 11 FYA RED DURING THE PHASE 2 DELAYED GREEN TIME (LEADING PED INTERVAL)

ENTER A "2" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP#:	2	COPY FROM:	2	ACTIVE:	М	(T/F)
I F AND		ON PH WALK GREEN ON PH		4 4	IS IS	
THEN	SIG	SET OLP RED SET OLP YELL SET OVLP GRE		2 2 2		ON OFF OFF
ELSE						

HOLD SIGNAL HEAD 31 FYA RED DURING THE PHASE 4 DELAYED GREEN TIME (LEADING PED INTERVAL)

ENTER A "3" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP#:	3	COPY FROM:	3	ACTIVE:	М	(T/F)
I F AND		ON PH WALK GREEN ON PH		6 6	IS IS	ON OF F
THEN	SIG	SET OLP RED SET OLP YELL SET OVLP GRE		3 3 3		ON OFF OFF
ELSE						

HOLD SIGNAL HEAD 51 FYA RED DURING THE PHASE 6 DELAYED GREEN TIME (LEADING PED INTERVAL)

ENTER A "4" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP#:	4	COPY FROM:	4	ACTIVE:	М	(T/F)	
I F AND		ON PH WALK GREEN ON PH		8 8	IS IS	ON OF F	
THEN	SIG	SET OLP RED SET OLP YELLO SET OVLP GREE		4 4 4		ON OF F OF F	
ELSE							

HOLD SIGNAL HEAD 71 FYA RED DURING THE PHASE 8 DELAYED GREEN TIME (LEADING PED INTERVAL)

1. From Main Menu select | 1. CONFIGURATION

2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR

3. From the LOGIC PROCESSOR Submenu select | 1. LOGIC STATEMENT CONTROL

ENABLE LOGIC PROCESSOR STATEMENTS 1-4 BY POSITIONING THE CURSOR OVER THE FIELDS SHOWN BELOW AND USING THE TOGGLE KEY TO ENABLE THEM .

LOGIC STATEMENT CONTROL																
	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	6
LP 1-15	Ε	Ε	Ε	Ε	•			•	•			•	•			•
LP 16-3	0.	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
LP 31-4	5.	•	•	•	•	•			•	•		•	•	•	•	•
LP 46-6	0.	•		•	•	•	•	•	•	•	•	•			•	•
LP 61-7	5.	•	•	•	•	•	•	•	•	•	•	•	•		•	•
LP 76-9	0.	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•

END PROGRAMMING

ASC/3 FLASH SENSE INPUT CONTROL FOR RED-RED FLASH

*The NCDOT default database is programmed to addresss Yellow-Red flash. Logic Statement 100 must be modified as shown when running Red-Red flash.

1. From Main Menu select | 1. CONFIGURATION |

2. From CONFIGURATION Submenu select | 8. LOGIC PROCESSOR |

3. From LOGIC PROCESSOR Submenu select 2. LOGIC STATEMENTS

Change the "LP" to 100 and move the cursor down. Delete the two "CTR-SET" statements by moving the cursor over them and hitting the "C" key. then hit "ENTER", select "LP SET CIB ON", hit "ENT", and then set the number to 427.

LP#:100 COPY FROM:100 ACTIVE: M FALSE IF LP CIB CODE ON 331 F THEN LP DELAY FOR 1.0 SECONDS LP SET CIB ON 427 ELSE

Hit "ESC", then 1 for "LOGIC STATEMENT CONTROL", next verify that LP#100 is ENABLED.

END PROGRAMMING

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THIS STATEMENT IS USED

SENSE INPUT WHEN RUNNING

RED-RED FLASH OPERATION.

TO CONTROL THE FLASH

ECONOLITE ASC/3-2070 STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select | 2. CONTROLLER |

2. From CONTROLLER Submenu select | 5. START/FLASH

START/FLASH DATA ----START UP-----1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 PHASE A B C D E F G H I J K L M N O P | FLASH>MON. NO FL TIME.. O ALL RED... 6 PWR START SEQ.. 1 MUTCD→ YES Y- G: NO

Scroll down on this screen and set "Exit FI" to Green "G"

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-0935 DESIGNED: January 2025 SEALED: 03-14-2025 REVISED: N/A

Electrical Detail - Sheet 5 of 5 Final Design

ELECTRICAL AND PROGRAMMING Prepared for the Offices of:

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Division 7 Guilford County PLAN DATE: January 2025 REVIEWED BY: AM Encarnacion PREPARED BY: JT Stiff REVIEWED BY: PL Alexander REVISIONS INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

PROJECT REFERENCE NO. SHEET NO.

| Sig. 7.5

U-4758

SIG. INVENTORY NO. 07-0935