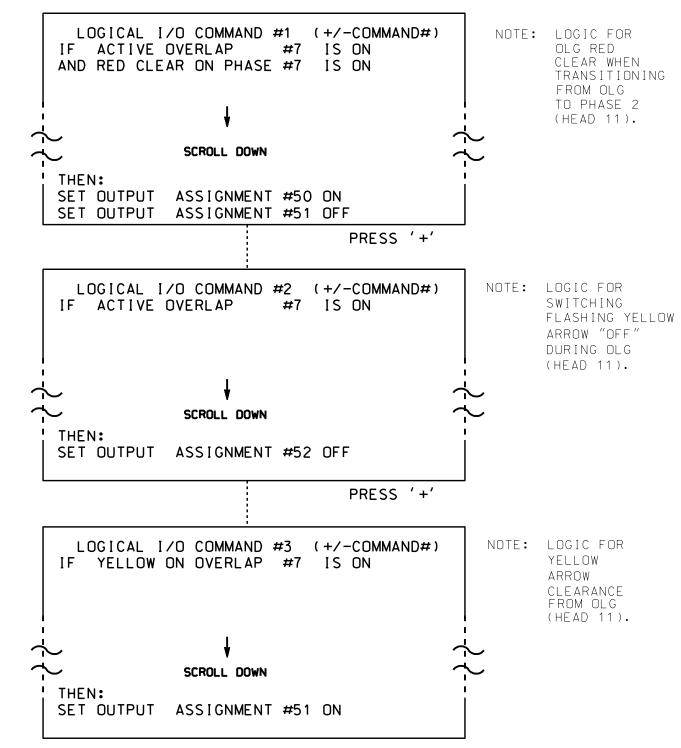
(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green

PROJECT REFERENCE NO. Sig. 15 2 I-5880

OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS ¦12345678910111213141516 VEH OVL PARENTS: X X VEH OVL NOT VEH: VEH OVL NOT PED: VEH OVL GRN EXT: | STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW X GREEN NOTICE GREEN FLASH SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...N GREEN EXTENSION (0-255 SEC).....0 YELLOW CLEAR (O=PARENT,3-25.5 SEC)..0.0 RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)...0

PRESS '+'

PAGE 1: VEHICLE OVERLAP 'B' SETTINGS 12345678910111213141516 VEH OVL PARENTS: X X VEH OVL NOT VEH: | VEH OVL NOT PED: VEH OVL GRN EXT: : STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW _ GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...N GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (O=PARENT,3-25.5 SEC)..0.0 RED CLEAR (0=PARENT, 0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (O=NONE, 1-16)....0

PRESS '+' 5 TIMES

PAGE 1: VEHICLE OVERLAP 'G' SETTINGS PHASE: 12345678910111213141516 VEH OVL PARENTS: | VEH OVL NOT VEH: VEH OVL NOT PED: VEH OVL GRN EXT: STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW _ GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...N GREEN EXTENSION (0-255 SEC)...... YELLOW CLEAR (O=PARENT,3-25.5 SEC)..0.0 RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0600T2 DESIGNED: September 2024 SEALED: 01-02-25 REVISED: N/A

Electrical Design - Sheet 2 of 3 - Temporary Design 2 ELECTRICAL AND PROGRAMMING DETAILS FOR: NC 109/SR 4325 (Thomasville Road)

I-40 EB Ramp

Forsyth County

Winston-Salem oivision 9 PLAN DATE: December 2024 REVIEWED BY: PREPARED BY: James Peterson Reviewed BY: REVISIONS INIT. DATE

SIG. INVENTORY NO. 09-0600T2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL

SIGNATURES COMPLETED

036833

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