

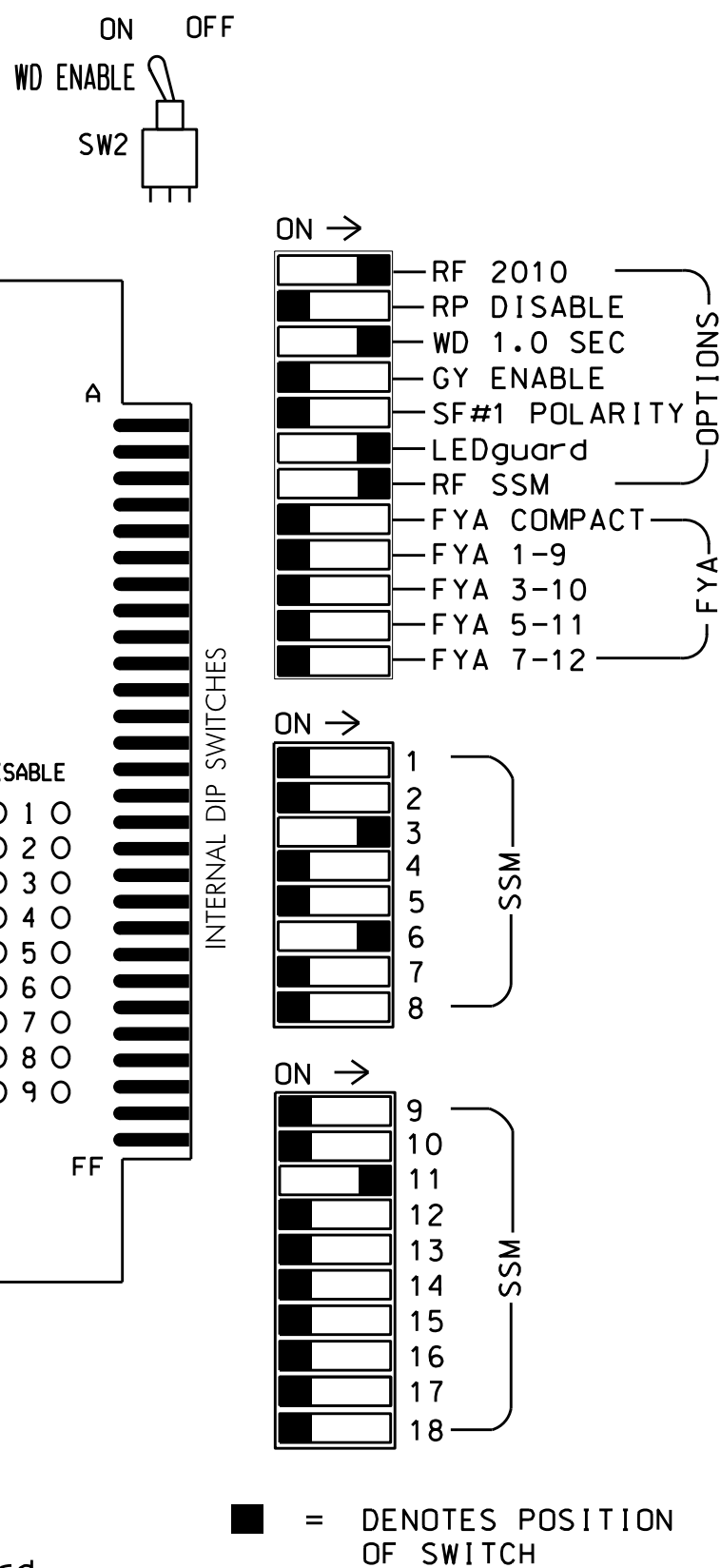
(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 3-II and 6-II.



NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.



1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all Phases.
3. Disable all phases for Startup In Green.
4. Program 6 as First Phases.
5. Disable all phases for Yellow Flash.
6. The cabinet and controller are part of the Winston-Salem Signal System.

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CONTROLLER.....2070
CABINET.....332 /W/ AUX
SOFTWARE.....ECONOLITE OASIS
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS..18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S4,S8,AUX S4
PHASES USED.....3,6
OVERLAPS.....NONE

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[illegible]

NU = Not Used

* Denotes install load resistor. See load resistor installation detail below.

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN
'1' (VEHICLE OVERLAP SETTINGS).

PRESS '+' TWICE

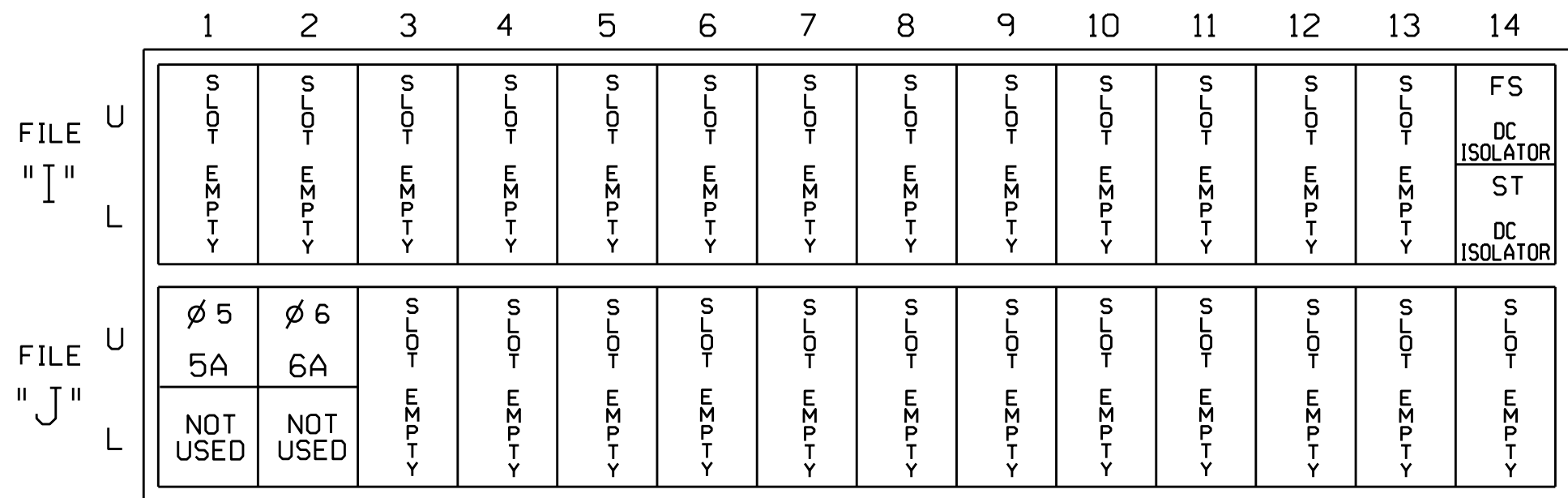
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PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE:      12345678910111213141516
VEH OVL PARENTS:      X  X
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR:  _ RED  _ YELLOW  _ GREEN
FLASH COLORS:   _ RED  _ YELLOW  _ GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?..N
GREEN EXTENSION (0-255 SEC).....0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)..0.0
OUTPUT AS PHASE # (0=NONE,1-16)....0

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OVERLAP PROGRAMMING COMPLETE

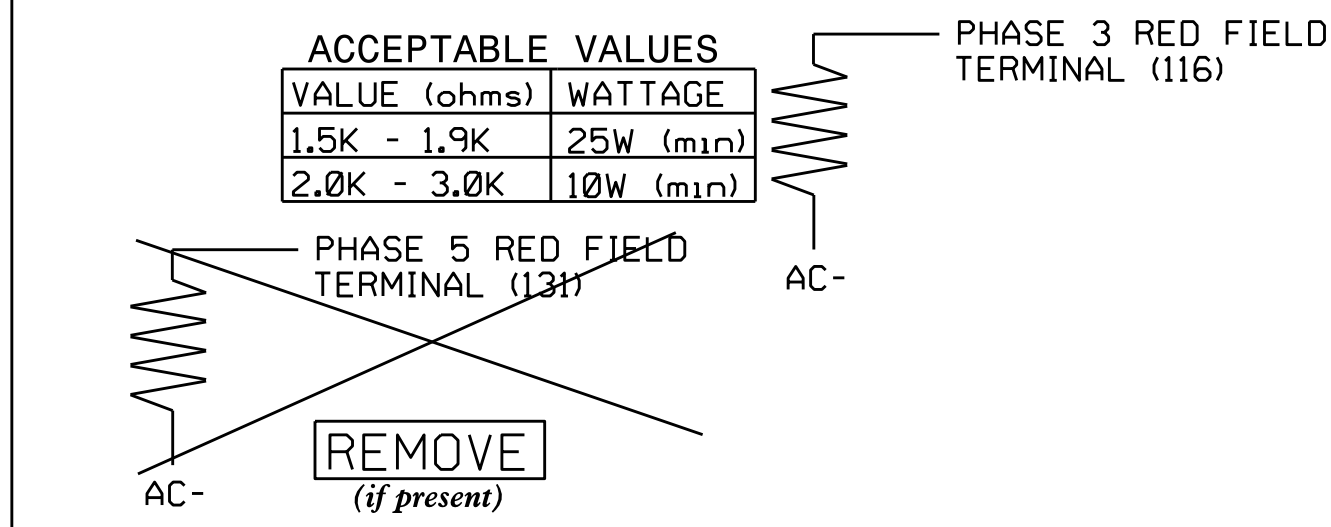
(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

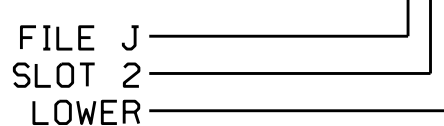
FS = FLASH SENSE
ST = STOP TIME

(install resistors as shown below)



LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
5A	TB3-1,2	J1U	55	17	5	5	Y	Y			15
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			

INPUT FILE POSITION LEGEND: J2L



electrical Detail - Temporary Design 2 (TMP Phase II)

at
I-40 WB Ramp

Division 09 Forsyth County Winston-Salem

PLAN DATE: December 2024	REVIEWED BY:
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PREPARED BY: James Peterson	REVIEWED BY:
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REVISIONS	INIT.	DATE
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**DOCUMENT NOT CONSIDERED
FINAL UNLESS ALL
SIGNATURES COMPLETED**

SEAI



Signed by: W. H. H.

Ryan W. Hough 01/13/2025
430320EAA28E4C3

SIGNATURE _____ DATE _____

SIG. INVENTORY NO. 09-0328T2