MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel

Main Menu > Controller > Overlap > Overlap Parameters / Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	ap 1		3	4	5	6
Туре	FYA 4 - Section					
Included Phases	2	4	6	8	6	2
Modifier Phases	1		5	-	-	-
Modifier Overlaps	1	ı		-	ı	1
Trail Green	0	0	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0	0.0	0.0

MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Coordination >Patterns

Web Interface

Home >Controller >Coordination >Patterns

Pattern Parameters

	i attorri i ara	11101010		
Pattern		Veh Det Plan	Overlap Plan	
	*	2	2	

*The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel
Main Menu>Controller>Unit

Web Interface Home>Controller>Unit

Start Up Parameters Startup Clearance Hold **Unit Flash Parameters** All Red Flash Exit Time

MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 1A & 5A

Front Panel

Main Menu >Controller >Detector >Veh Det Plans

Web Interface

Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

5A

	Detector	Call Phase	Delay
1A	1	1	0.0
	29	0	3.0

Call Phase Delay Detector 5 0.0 31 0 3.0

MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases

for heads 11 and 51 to run protected turns only.

VEH DET PLAN 2: Disables phase 6 call on loop 1A

and reduces delay time for phase 1 call on loop 1A to 0 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel

Main Menu > Controller > Overlap > Overlap Parameters / Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	1	2	3	4	5	6	
Туре	FYA 4 - Section						
Included Phases	-	4	-	8	6	2	NOTICE
Modifier Phases	1	-	5	-	-	-	INCLUDED
Modifier Overlaps	-	-	-	-	-	-	PHASES
Trail Green	0	0	0	0	0	0	
Trail Yellow	0.0	0.0	0.0	0.0	0.0	0.0	
Trail Red	0.0	0.0	0.0	0.0	0.0	0.0	

OUTPUT CHANNEL CONFIGURATION

Front Panel

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channels Configuration

Channel Configuration

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Phase Vehicle	1		Χ	Х	1
2	Phase Vehicle	2		Χ		2
3	Phase Vehicle	3		Х	X	3
4	Phase Vehicle	4		Х		4
5	Phase Vehicle	5		Х		5
6	Phase Vehicle	6		Х	Х	6
7	Phase Vehicle	7		Х		7
8	Phase Vehicle	8		Х	Х	8
9	Overlap	1		Х	Х	9
10	Overlap	2		Х	Х	10
11	Overlap	3		Х		11
12	Overlap	4		Х		12
13	Phase Ped	2				13
14	Phase Ped	4				14
15	Phase Ped	6				15
16	Phase Ped	8				16
17	Overlap	5		Χ	Х	17
18	Overlap	6		Х		18

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

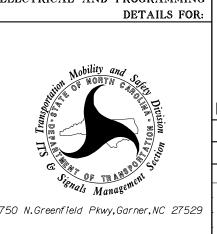
- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 02-0923 DESIGNED: Apr 2025

SEALED: 4/24/2025 REVISED: N/A

Electrical Detail Sheet 2 of 2



Plans Prepared By

BDRMP

NC 43 Savoy Drive/Shadow Brook Lane

Division 2 Craven County New Bern REVIEWED BY: BN Groome PLAN DATE: April 2025 PREPARED BY: DS Griffith DRMP PROJ. NO.: 17359 (040) REVISIONS INIT. DATE

SIG. INVENTORY NO. 02-0923

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Sig-7.2

R-4463A

TH CARO 052936

Brittary N Groome 4/24/2021