PROJECT REFERENCE NO. Sig-12.5

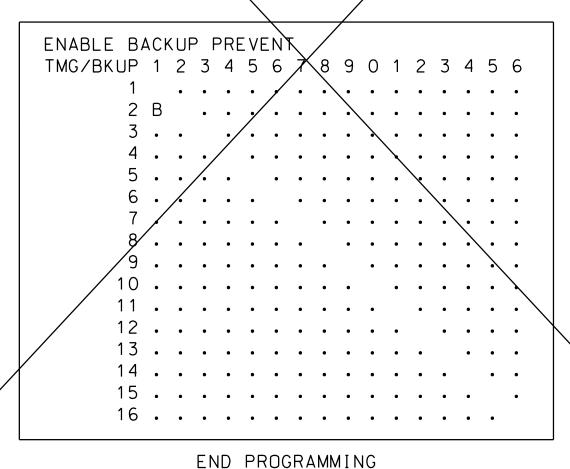
REMOVE!

ECONOLITE ASC/3-2070 BACKUP PROTECTION ENABLE PROGRAMMING

(program controller as shown)

- 1. From Main Menu select | 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select | 1. CONTROLLER SEQ |
- 3. From CONTROLLER SEQUENCE Submenu select | 3. BACKUP PREVENT PHASES

Follow programming as shown below. On the 'ENABLE BACKUP PREVENT' screen move/cursor to the appropriate field and presø 'YES/NO' on the controller kexpad to toggle/field value between 'X', 'B', 'G' and 'OFF'.



NOTE

1. 'B' without a 'C' programmed for the 'TIMING' (row) phase inhibits the controller from servicing the 'BACKUP' (column) phase when the 'TIMING' (row) phase is active, or next, until the controller goes through Red Revert and Red Clear. Make sure the proper Red Revert and Red Clear times shown on the Signal Design plan are programmed in the controller phase timing.

ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT. PHASING DURING FREE RUN - PROGRAM CHANGES (SHOWN BELOW) IN A TIME BASED ACTION PLAN. SCHEDULE A DAY PLAN THAT INCLUDES THE ACTION PLAN PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 and 5.

TO RUN ALT. PHASING DURING COORDINATION - SELECT THE TIME BASED ACTION PLAN THAT IS PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 and 5.

PHASING	VEH DET PLAN	SF BITS ENABLED
ACTIONS REQUIRED TO RUN DEFAULT PHASING	1	NONE
ACTIONS REQUIRED TO RUN <u>ALTERNATE PHASING</u>	2	1, 5

IMPORTANT: IF ALT. PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERATE TIME OF DAY EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN SF BITS 1, AND 5 AND VEH DET PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

SF BITS 1,5:

Modifies overlap parent phases for heads 11 and 51 to run protected

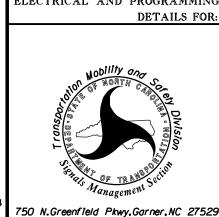
VEH DET PLAN 2: Disables phase 6 call on loop 1A

and reduces delay time for phase 1 call on loop 1A to 3 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-2100 DESIGNED: Mar 2025 SEALED: 3/17/2025 REVISED: N/A

Electrical Detail - Sheet 5 of 5 Final Design



NC 62 (Liberty Road) SR 1162 (Weant Road) / SR 4066 (Modlin Grove Road)

Guilford County Division 7 REVIEWED BY: ZM Esposito PLAN DATE: March 2025 PREPARED BY: AW Poole REVIEWED BY: 17380 (040) REVISIONS

Jachary M. Esposito 3/17/2025 SIG. INVENTORY NO. 07-2100

DOCUMENT NOT CONSIDERED

FINAL UNLESS ALL SIGNATURES COMPLETED

