

ECONOLITE ASC/3-2070 OVERLAP
PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER
2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP...[A] TYPE: PPLT FYA
PROTECTED LEFT TURN... PHASE 1
OPPOSING THROUGH..... PHASE 2
FLASHING ARROW OUTPUT.....CH9 ISOLATE
DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 1

Toggle Once

OVERLAP B

Select TMG VEH OVLP [B] and 'OTHER/ECONOLITE'

TMG VEH OVLP...[B] TYPE: OTHER/ECONOLITE
PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . . X
PROTECT
PED PRTC
NOT OVLP
FLSH GRN . . . 1
LAG X PH
LAG 2 PH
LAG GRN 0.0 YEL 0.0 RED 0.0 ADV GRN 0.0

Toggle Twice

OVERLAP D

Select TMG VEH OVLP [D] and 'OTHER/ECONOLITE'

TMG VEH OVLP...[D] TYPE: OTHER/ECONOLITE
PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED X
PROTECT
PED PRTC
NOT OVLP
FLSH GRN 1
LAG X PH
LAG 2 PH
LAG GRN 0.0 YEL 0.0 RED 0.0 ADV GRN 0.0

END PROGRAMMING

ECONOLITE ASC/3-2070 VEHICLE DETECTOR
SETUP PROGRAMMING DETAIL

FOR ALTERNATE PHASING LOOP 1A

(program controller as shown)

IMPORTANT!

Program detectors per the input file connection and programming chart shown on sheet 1 before proceeding.

1. From Main Menu select 8. UTILITIES
2. From UTILITIES Submenu select 1. COPY/CLEAR
3. Copy from DETECTOR PLAN "1" to DETECTOR PLAN "2".

COPY / CLEAR UTILITY	FROM	TO
PHASE TIMING....	>	PHASE TIMING....
TIMING PLAN.....	>	TIMING PLAN.....
PH DET OPT PLAN..	>	PH DET OPT PLAN..
DETECTOR PLAN... 1	>	DETECTOR PLAN... 2
TOGGLE TO SELECT A "FROM" AND A "TO" THEN PRESS ENTER		

4. From Main Menu select 6. DETECTORS
5. From DETECTOR Submenu select 2. VEHICLE DETECTOR SETUP
6. Place cursor in VEH DET PLAN [] position and enter "2".

- Place cursor in VEH DETECTOR [] position and enter "1".
- Set delay time to "3".

VEH DETECTOR [1]	VEH DET PLAN [2]
TYPE: N-NTCIP	
TS2 DETECTOR..... ECPI LOG..... NO	
DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6	
1 1	
CALL OPTION.... YES DELAY TIME... 3.0	
EXT OPTION. PASSAGE EXTENSION TIME. 0.0	
USE ADDED INITIAL . CROSS SWITCH PH.. 0	
LOCK IN..... NONE NTCIP VOL . OR OCC .	
PMT QUEUE DELAY. NO	

NOTICE VEH
DET PLAN 2

ENSURE DELAY
IS SET TO '3'

- Place cursor in VEH DETECTOR [] position and enter "26".
- Set assigned phase to "0".

VEH DETECTOR [26]	VEH DET PLAN [2]
TYPE: G-GREEN EXTENSION/DELAY	
TS2 DETECTOR..... ECPI LOG..... NO	
DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6	
26 0	
EXTEND TIME... 0.0 DELAY TIME... 3.0	
USE ADDED INITIAL . CROSS SWITCH PH.. 0	
LOCK IN..... NONE NTCIP VOL . OR OCC .	
PMT QUEUE DELAY. NO	

NOTICE VEH
DET PLAN 2

ENSURE DELAY
IS SET TO '3'

END PROGRAMMING

ECONOLITE ASC/3-2070 BACKUP
PROTECTION ENABLE PROGRAMMING

(program controller as shown)

1. From Main Menu select 1. CONFIGURATION
2. From CONFIGURATION Submenu select 1. CONTROLLER SEQ
3. From CONTROLLER SEQUENCE Submenu select 3. BACKUP PREVENT PHASES

Follow programming as shown below. On the 'ENABLE BACKUP PREVENT' screen move cursor to the appropriate field and press 'YES/NO' on the controller keypad to toggle field value between 'X', 'B', 'C' and 'OFF'.

ENABLE BACKUP PREVENT	TMG/BKUP	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	6
1	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
2	B	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
3	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
4	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
5	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
6	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
7	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
8	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
9	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
10	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
11	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
12	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
13	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
14	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
15	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
16	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.

END PROGRAMMING

NOTE

1. 'B' without a 'C' programmed for the 'TIMING' (row) phase inhibits the controller from servicing the 'BACKUP' (column) phase when the 'TIMING' (row) phase is active, or next, until the controller goes through Red Revert and Red Clear. Make sure the proper Red Revert and Red Clear times shown on the Signal Design plan are programmed in the controller phase timing.

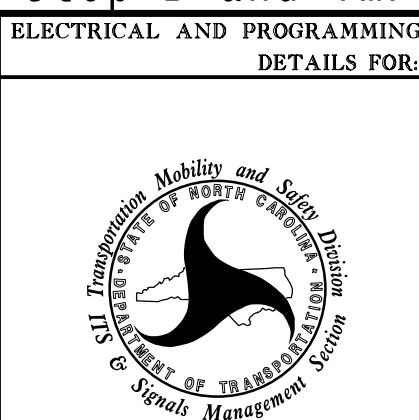
THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 07-2100T2
DESIGNED: Mar 2025
SEALED: 3/17/2025
REVISED: N/A

Electrical Detail - Sheet 2 of 4
Temporary Design 2 (TMP Phase 2,
Step B and TMP Phase III)



DRMP, INC.
8210 UNIVERSITY EXECUTIVE PARK DR.
SUITE 200
CHARLOTTE, NC 28262
PHONE: 704-543-4260

750 N. Greenfield Pkwy, Garner, NC 27529



ELECTRICAL AND PROGRAMMING DETAILS FOR:		NC 62 (Liberty Road) at SR 1162 (Weant Road) / SR 4066 (Modlin Grove Road) Division 7 Guilford County Archdale	
PLAN DATE: March 2025	REVIEWED BY: ZM Esposito	SEALED: 3/17/2025	
PREPARED BY: AW Poole	RKA PROJ. NO.: 17380 (040)	SIGNED BY: Zachary M. Esposito	
REVISIONS	INIT.	DATE	

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED	
SEAL NORTH CAROLINA PROFESSIONAL ENGINEER 054155 ZACHARY M. ESPOSITO	
SIGNED BY: Zachary M. Esposito	DATE: 3/17/2025
SIGNATURE	DATE
SIG. INVENTORY NO. 07-2100T2	