<u>L0</u>

All chan controll Notice '

1. From

2. From

LD	SWITCH	ASSI							
	PHASE		DI	I MN	1 I N	١G	———F	FLASH	
	/OVLP	ΤΥΡΕ	R	Y	G	D	PWR	AUT	TGR
1	1	V	•	•	•	+	А	R	Х
2	2	V	•	•	•	+	Α	R	•
3	3	V	•	•	•	+	А	R	Х
4	4	V	•	•	•	+	Α	R	•
5	5	V	•	•	•	_	Α	R	•
6	6	V	•	•	•	-	А	R	Х
7	7	V	•	•	•	—	Α	R	•
8	8	V	•	•	•	-	Α	R	Х
9	1	0	•	•	•	+	Α	R	Х
10	2	0	•	•	•	+	А	R	Х
11	3	0	•	•	•	-	А	R	•
12	4	0	•	•	•	—	А	R	•
13	2	Ρ	•	•	•	+	Α	•	•
14	4	Р	•	•	•	—	А	•	•
15	6	Р	•	•	•	+	А	•	•
16	8	Ρ	•	•	•	-	А	•	•

<u>EC</u> SOF

1. F

2. F

<u>ECONOLITE ASC/3-2070</u> <u>D SWITCH ASSIGNMENT DETAIL</u>	
(program controller as shown)	ALTERNATE PHASI
els must be programmed to flash red in - for red-red flash operation as shown below. JT" for all channels is programmed for RED.	TO RUN ALT. PHASING DURING FREE RUN - PROGRA
Main Menu select 1. CONFIGURATION	TO SEL
CONFIGURATION Submenu select 3. LOAD SW ASSIGN	TO RUN ALT. PHASING DURING COORDINATION - SE TO
LD SWITCH ASSIGN PHASE DIMMINGFLASH /OVLP TYPE R Y G D PWR AUT TGR 1 1 V + A R X 2 2 V + A R X 3 3 V + A R X 4 4 V + A R X 5 5 V A R X 6 6 V A R X 7 7 V A R X 9 1 0 + A R X 10 2 0 + A R X 11 3 0 A R X 9 1 0 + A R X 10 2 0 + A R X 11 3 0 A R 12 4 0 A 13 2 P + A 14 4 P	PHASING ACTIONS REQUIRED TO RUN <u>DEFAULT PHASI</u> ACTIONS REQUIRED TO RUN <u>ALTERNATE PHA</u> IMPORTANT: IF ALT. PHASING IS USED DURING FR EVENTS CONCURRENTLY WITH COORDINA RUN EVENT SHOULD END BEFORE COORD
NOLITE ASC/3-2070 STARTUP AND TWARE FLASH PROGRAMMING DETAIL	
(program controller as shown)	ALTERNATE PHA
m Main Menu select 2. CONTROLLER	THE FOLLOWING IS A SUMMA SF BIT 5 AND VEH DET PLA
m CONTROLLER Submenu select 5. START/FLASH	"ALTERNATE PHASING":
START/FLASH DATA $START UP$	SF BIT 5: Modif for he turns VEH DET PLAN 2: Disabl and re call c
FLASH SENSE INPUT CONTROL FOR RED-RED FLASH default database is programmed to addresss Yellow-Red flash. ement 100 must be modified as shown when running Red-Red flash.	
rom Main Menu select 1. CONFIGURATION rom CONFIGURATION Submenu select 8. LOGIC PROCESSOR rom LOGIC PROCESSOR Submenu select 2. LOGIC STATEMENTS	
ye the "LP" to 100 and move the cursor down, Delete wo "CTR-SET" statements by moving the cursor over and hitting the "C" key, then hit "ENTER", select WET CIB ON", hit "ENT", and then set the number to 427.	
P#:100 COPY FROM:100 ACTIVE: M FALSE LP CIB CODE ON 331 F THIS STATEMENT IS USED TO CONTROL THE FLASH SENSE INPUT WHEN RUNNING	Ele Ten
IEN LP DELAY FOR 1.0 SECONDS LP SET CIB ON 427	ELECT
.SE	
"ESC", then 1 for "LOGIC STATEMENT CONTROL", verify that LP#100 is ENABLED.	
END PROGRAMMING	DRMP, INC. 8210 UNIVERSITY EXECUTIVE PARK DR. NC LICENSE NO. F-1524 SUITE 220 www.drmp.com CHARLOTTE, NC 28262 PHONE: 704-549-4260

<u>ASC/</u>

*The NCDO Logic Sta

1

Sector We note have been in the intervention of the intervention		
ALTERNATE PHASI ALTERNATE PHASI ALTERNATE PHASI ALTERNATE PHASI ALTERNATE PHASI ALTERNATE PHASI ALTERNATE PHASI ALTERNATE PHASI ALTERNATE PHASI TO dx AL - #ASING DLAINS <u>PHAL dx - endow</u> TO SO TO SO TO SO TO U. AL - #ASING DLAINS <u>PHAL dx - endow</u> TO SO TO U. AL - #ASING DLAINS <u>CONTAINED</u> TO U. AL - #ASING DLAINS <u>CONTAINED</u> ACTIONS REQUIRED TO RIN <u>ALTERNATE PH</u> ACTIONS REQUIRED TO RIN <u>ALTERNATE PH</u> HEOREMAN TO U. AL - #ASING DLAINS <u>CONTAINED</u> ACTIONS REQUIRED TO RIN <u>ALTERNATE PH</u> HEOREMAN TO U. ALL - #ASING DLAINS <u>CONTAINED</u> ACTIONS REQUIRED TO RIN ALTERNATE PH HEOREMAN TO U. ALL - #ASING DLAINS <u>CONTAINED</u> ACTIONS REQUIRED TO RIN ALTERNATE PH HEOREMAN TO U. ALL - #ASING DLAINS <u>CONTAINED</u> TO U. ALL - #ASING DLAINS <u>CONTAINED</u>		
mails could be programmed to Tree in a transmission of the first could be programmed to Tree in a transmission of the first could be appropriate defection of the first could be approprimedefection of the first c	(program controller as shown)	ALTERNATE PHAST
TO SER TO SER In SOUTONITON Submer Leving LOD SM \$45516 In Souton Leving Lon Souton Leving In Souton Leving	ler for red-red flash operation as shown below.	TO RUN ALT. PHASING DURING FREE RUN - PROGRA
¹ + 2 + 2 + 2 + 2 + 2 + 2 + 2 + 2 + 2 +	om Main Menu select 1. CONFIGURATION	TO SEL
Image:		TO RUN ALT. PHASING DURING COORDINATION - SE TO
FTWARE FLASH PROGRAMMING DETAIL (program: cuttrellar as shown) From Math. Waru, astlest [2, CONTROLLER] From Math. Waru, astlest [2, CONTROLLER] Statt Trits Controller as shown) Statt Trits Controller as the shown as shown) Statt Trits Controller as the shown as	PHASE DIMMING FLASH /OVLP TYPE R Y G D PWR AUT TGR 1 1 V • • + A R X 2 2 V • • + A R X 3 3 V • • + A R X 4 4 V • • + A R X 4 4 V • • + A R X 5 5 V • - - A R X 6 6 V • - - A R X 9 1 0 • • + A R X 10 2 0 • • + A R . 11 3 0 • • - A R . 12 4 <	ACTIONS REQUIRED TO RUN <u>DEFAULT PHAS</u> ACTIONS REQUIRED TO RUN <u>ALTERNATE PH</u>
Image: controller as those; Prior McIn Menu Select [2: CONTROLLER] Prior CONTROLLER Submenu select [5: START/FLASH] Image: controller as those; Image: controller as those; <td< td=""><td></td><td></td></td<>		
From Koln Monu select [2: CONTROLLER From CONTROLLER Submenu select [3: START/FLASH START/FLASH DATA 		ALTERNATE PHA
From CONTROLLER Submenu select <u>S. START/FLASH</u> START/FLASH DATA START/FLASH DATA S	From Main Menu select 2. CONTROLLER	THE FOLLOWING IS A SUMMA SF BIT 5 AND VEH DET PLA "ALTERNATE PHASING".
Start/TLSH DATA START 771 ASH DATA START 55 6 7 8 9 01 2 3 4 5 6 PHASE 5 7 S 0 0 0 F R 0 H 1 / K L M N 0 P DVERLAR X X X X X X X X X X X X X X X X X X X		
FOR RED-RED FLASH Of default dotobase is programmed to oddress Yellow-Red flash. i-drement 100 must be modified as shown when running Red-Red flash. i-from CONFIGURATION From CONFIGURATION Submenu select (a. LOGIC PROCESSOR) From LOGIC PROCESSOR Submenu select (a. LOGIC STATEMENTS) prome har "LP" to 100 and nove the ourser down. Deletement to output over years on an ohitHing the "C" key, then hit "ENTER", select (b. Statement to verify atoment set) moving the output over years over years on an ohitHing the "C" key, then hit "ENTER", select (b. Statement to verify the turber of 1.0 SECONDS (b. Statement to verify the turber of 1.0 SECONDS (b. Statement control * 200 (b. Statement con	START UP	for h turns
From CONFIGURATION Submenu select B. LOGIC PROCESSOR From LOGIC PROCESSOR Submenu select C. LOGIC STATEMENTS honge the "LP" to 100 and move the cursor down. Delete two "CTR-SET" statements by moving th	FOR RED-RED FLASH	
THEN LP DELAY FOR 1.0 SECONDS LP SET CIB ON 427 ELSE It "ESC", then 1 for "LOGIC STATEMENT CONTROL", EXT OF DECOMMENDE ENDERDED. ENDERD. ENDERD. ENDERD. ENDERD. ENDERD. ENDERD.	. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR	
THEN LP DELAY FOR 1.0 SECONDS LP SET CIB ON 427 ELSE it "ESC", then 1 for "LOGIC STATEMENT CONTROL", ext verify that LP#100 is ENABLED. END DEDOCMANUNC	ne two "CTR-SET" statements by moving the cursor over nem and hitting the "C" key, then hit "ENTER", select	
it "ESC", then 1 for "LOGIC STATEMENT CONTROL", ext verify that LP#100 is ENABLED.	LP#:100 COPY FROM:100 ACTIVE: M FALSE IF LP CIB CODE ON 331 F THEN LP DELAY FOR 1.0 SECONDS TO CONTROL THE FLASH	El Te ELEC
	ELSE	
DRMP, INC. 8210 UNIVERSITY EXECUTIVE PARK DR. NC LICENSE NO. F-1524 SUITE 220 CHARLOTTE, NC 28262 PHONE: 704-549-4260 750		
	END PROGRAMMING	DRMP, INC. 8210 UNIVERSITY EXECUTIVE PARK DR. NC LICENSE NO. F-1524 SUITE 220 www.drmp.com CHARLOTTE, NC 28262 PHONE: 704-549-4260

<u>ECONOLITE ASC/3-2070</u> DAD SWITCH ASSIGNMENT DETAIL	
(program controller as shown)	ALTERNATE PHASI
nnels must be programmed to flash red in ler for red-red flash operation as shown below. 'AUT" for all channels is programmed for RED.	TO RUN ALT. PHASING DURING FREE RUN - PROGRA SCHEDU
m Main Menu select 1. CONFIGURATION	TO SEL
m CONFIGURATION Submenu select 3. LOAD SW ASSIGN LD SWITCH ASSIGN PHASE DIMMINGFLASH	TO RUN ALT. PHASING DURING COORDINATION - SE TO
/0VLP TYPE R Y G D PWR AUT TGR 1 1 V . . + A R X 2 2 V . . + A R X 3 3 V . . + A R X 4 4 V . . + A R X 4 4 V . . + A R X 5 5 V . . - A R X 6 6 V . . - A R X 7 7 V . . - A R X 9 1 0 . . + A R X 10 2 0 . . - A R . 13 2 P . . - A </td <td>PHASING ACTIONS REQUIRED TO RUN <u>DEFAULT PHASI</u> ACTIONS REQUIRED TO RUN <u>ALTERNATE PHA</u> IMPORTANT: IF ALT. PHASING IS USED DURING FF</td>	PHASING ACTIONS REQUIRED TO RUN <u>DEFAULT PHASI</u> ACTIONS REQUIRED TO RUN <u>ALTERNATE PHA</u> IMPORTANT: IF ALT. PHASING IS USED DURING FF
	EVENTS CONCURRENTLY WITH COORDINA RUN EVENT SHOULD END BEFORE COORD
CONOLITE ASC/3-2070 STARTUP AND	
FTWARE FLASH PROGRAMMING DETAIL (program controller as shown)	ALTERNATE PHA
rom Main Menu select 2. CONTROLLER	THE FOLLOWING IS A SUMMA SF BIT 5 AND VEH DET PLA
rom CONTROLLER Submenu select 5. START/FLASH	"ALTERNATE PHASING":
START/FLASH DATASTART UP	SF BIT 5: Modif for h turns VEH DET PLAN 2: Disab and re call o
3 FLASH SENSE INPUT CONTROL FOR RED-RED FLASH DT default database is programmed to addresss Yellow-Red flash. tatement 100 must be modified as shown when running Red-Red flash.	
From Main Menu select 1. CONFIGURATION From CONFIGURATION Submenu select 8. LOGIC PROCESSOR From LOGIC PROCESSOR Submenu select 2. LOGIC STATEMENTS	
ange the "LP" to 100 and move the cursor down. Delete he two "CTR-SET" statements by moving the cursor over hem and hitting the "C" key. then hit "ENTER", select P SET CIB ON", hit "ENT", and then set the number to 427.	
LP#:100COPY FROM:100ACTIVE: MFALSEIFLPCIBCODEON331FTHENLPDELAYFOR1.0SECONDSRED-REDFLASHLPSETCIBON427FF	Elect
ELSE	
it "ESC", then 1 for "LOGIC STATEMENT CONTROL", ext verify that LP#100 is ENABLED.	B D R M P INC.
END PROGRAMMING	DRMP, INC. 8210 UNIVERSITY EXECUTIVE PARK DR. NC LICENSE NO. F-1524 SUITE 220 www.drmp.com CHARLOTTE, NC 28262 PHONE: 704-549-4260

	PROJECT REFERENCE NO. SHEET U-6018 Sig-8	
ING ACTIVATION DETAIL		
RAM CHANGES (SHOWN BELOW) IN A TIME E DULE A DAY PLAN THAT INCLUDES THE ACT ELECT VEH DET PLAN 2 AND ENABLE SF BI	TION PLAN PROGRAMMED	
SELECT THE TIME BASED ACTION PLAN THA		
TO SELECT VEH DET PLAN 2 AND ENABLE S		
VEH DET PLAN SF BIT	TS ENABLED	
	NONE	
HASING 2	5	
FREE RUN AND COORDINATION, DO NOT OPE NATION PLAN EVENTS IN THE EVENT SCHEE		
RDINATION PLAN EVENT STARTS AND VICE-		
HASING CHANGE SUMMARY		
MARY OF WHAT TAKES PLACE WHEN LAN 2 ACTIVATE TO CALL THE		
fies overlap parent phases head 51 to run protected hs only.		
ibles phase 2 call on loop 5A		
reduces delay time for phase 5 on loop 5A to 0 seconds.		
THIS	ELECTRICAL DETAIL IS FOR	
THE S	SIGNAL DESIGN: 07-2091T3 GNED: March 2025	
	ED: 3/17/2025 SED: N/A	
]	
lectrical Detail - Sheet 3 of 3 emporary Design 3 - (TMP Phase III)	DOCUMENT NOT CONSIDER FINAL UNLESS ALL	
ECTRICAL AND PROGRAMMING DETAILS FOR: NC 62 (Liberty)	Road)	ر
At I-85 Northbound	TH CAROL	
Division 7 Guilford County PLAN DATE: March 2025 REVIEWED BY:	Archdale ZM Esposito	
PREPARED BY: AW Poole RKA PROJ. NO. REVISIONS	INIT. DATE Signed by:	<u>י</u> סחיר
0 N.Greenfield Pkwy,Garner,NC 27529	SIG. INVENTORY NO. 07 - 2091	