ECONOLITE ASC/3-2070 LOAD SWITCH ASSIGNMENT DETAIL

(program controller as shown)

All channels must be programmed to flash red in controller for red-red flash operation as shown below. Notice "AUT" for all channels is programmed for RED.

- 1. From Main Menu select | 1. CONFIGURATION |
- 2. From CONFIGURATION Submenu select | 3. LOAD SW ASSIGN

LD SWITCH ASSIGN PHASE DIMMING ---FLASH---/OVLP TYPE R Y G D PWR AUT TGR 16 8 P ... - A . .

ECONOLITE ASC/3-2070 STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select | 2. CONTROLLER
- 2. From CONTROLLER Submenu select | 5. START/FLASH

START/FLASH DATA -----START UP-----1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 PHASE ABCDEFGHIJKLMNOP | FLASH>MON. NO FL TIME.. O ALL RED... 6 PWR START SEQ.. 1 MUTCD→ YES Y- G: NO

Scroll down on this screen and set "Exit FI" to Green "G"

ASC/3 FLASH SENSE INPUT CONTROL FOR RED-RED FLASH

*The NCDOT default database is programmed to addresss Yellow-Red flash. Logic Statement 100 must be modified as shown when running Red-Red flash.

- 1. From Main Menu select | 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select | 8. LOGIC PROCESSOR
- 3. From LOGIC PROCESSOR Submenu select | 2. LOGIC STATEMENTS

Change the "LP" to 100 and move the cursor down. Delete the two "CTR-SET" statements by moving the cursor over them and hitting the "C" key. then hit "ENTER". select "LP SET CIB ON", hit "ENT", and then set the number to 427.

END PROGRAMMING

next verify that LP#100 is ENABLED.

THIS STATEMENT IS USED LP#:100 COPY FROM:100 ACTIVE: M FALSE TO CONTROL THE FLASH IF LP CIB CODE ON 331 F SENSE INPUT WHEN RUNNING RED-RED FLASH OPERATION. THEN LP DELAY FOR 1.0 SECONDS LP SET CIB ON ELSE Hit "ESC", then 1 for "LOGIC STATEMENT CONTROL",

ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT, PHASING DURING FREE RUN - PROGRAM CHANGES (SHOWN BELOW) IN A TIME BASED ACTION PLAN, SCHEDULE A DAY PLAN THAT INCLUDES THE ACTION PLAN PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BIT 5.

TO RUN ALT. PHASING DURING COORDINATION - SELECT THE TIME BASED ACTION PLAN THAT IS PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BIT 5.

PHASING	VEH DET PLAN	SF BITS ENABLED
ACTIONS REQUIRED TO RUN <u>DEFAULT PHASING</u>	1	NONE
ACTIONS REQUIRED TO RUN <u>ALTERNATE PHASING</u>	2	5

IMPORTANT: IF ALT. PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERATE TIME OF DAY EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN SF BIT 5 AND VEH DET PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

SF BIT 5:

Modifies overlap parent phases for head 51 to run protected

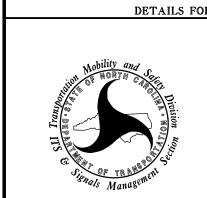
turns only.

VEH DET PLAN 2:

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-2091T1 DESIGNED: Mar 2025 SEALED: 3/17/2025 REVISED: N/A

Electrical Detail - Sheet 3 of 3 Temporary Design 1 - (TMP Phase I)



NC 62 (Liberty Road) I-85 Northbound Ramps

Division 7 Guilford County PLAN DATE: March 2025 REVIEWED BY: ZM Esposito RKA PROJ. NO.: 17380 (040) PREPARED BY: AW Poole

Bachary W" Esposito

PROJECT REFERENCE NO.

Sig-6.3

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REVISIONS

IG. INVENTORY NO. 07-2091T1

DOCUMENT NOT CONSIDERED

FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL 054155