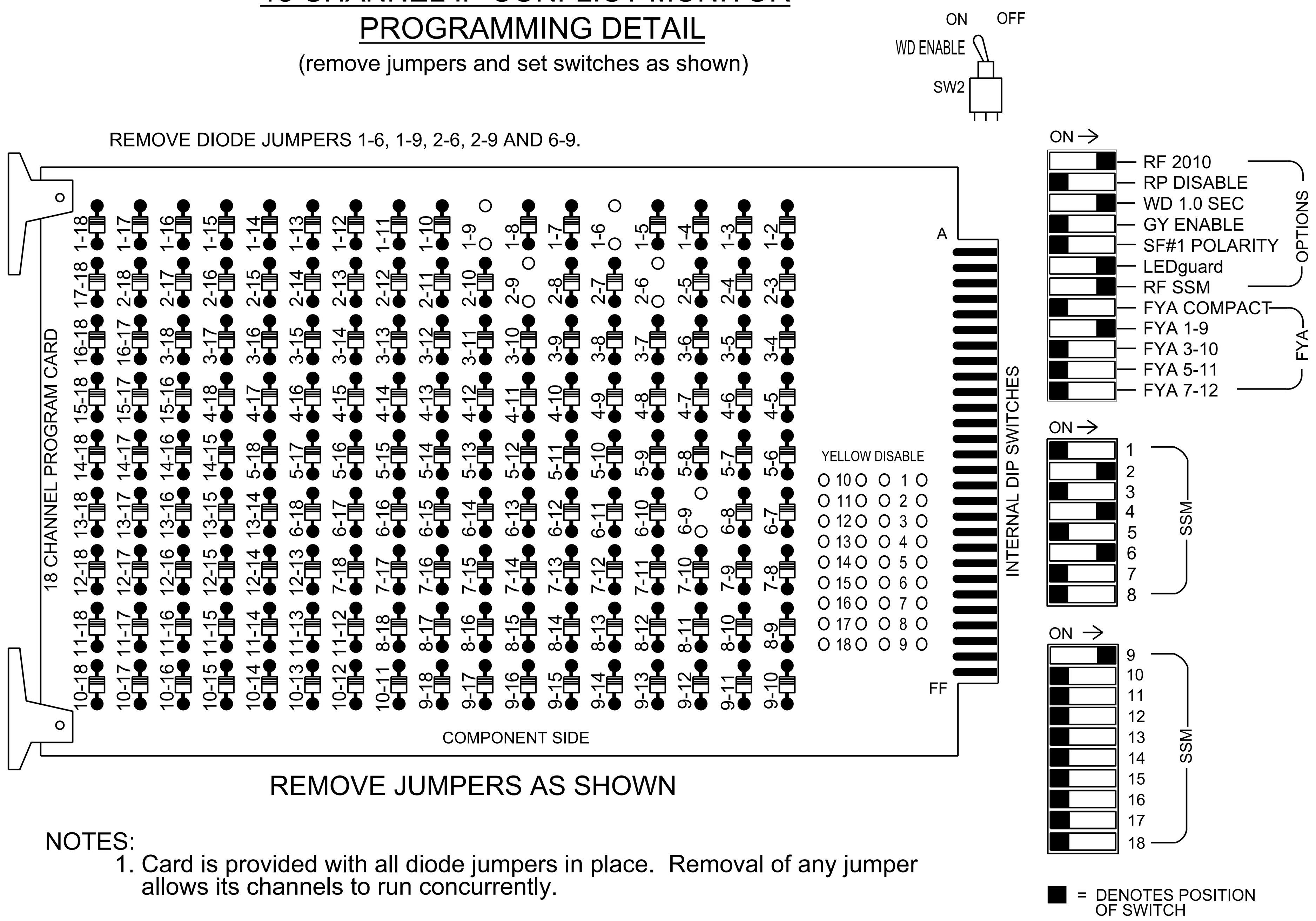
18 CHANNEL IP CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-6, 1-9, 2-6, 2-9 AND 6-9.



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that the Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program controller to start up in phase 2 Green and 6 Green.
3. The cabinet and controller are part of the High Point Signal System.

EQUIPMENT INFORMATION

CONTROLLER..............2070LX
CABINET.................332 W/ AUX
SOFTWARE................ECONOLITE ASC/3-2070
CABINET MOUNT...........BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED......S1,S2,S5,S8,AUX S1
PHASES USED.............1,2,4,6
OVERLAP "A".............*
OVERLAP "B".............NOT USED
OVERLAP "C".............NOT USED
OVERLAP "D".............NOT USED

* See overlap programming on this sheet.

PROJECT REFERENCE NO.	SHEET NO.
U-6018	Sig-4.1

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2		S3	S4	S5	S6	S7	S8		S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2		13	3	4	14	5	6		15	7	8	16	9	10	17	11	12	18
PHASE	1	2		2 PED	3	4	4 PED	5	6		6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11★	21	22	NU	NU	41,42,43	NU	NU	61	62	NU	NU	NU	NU	11★	NU	NU	NU	NU	NU
RED		128	128			101			134	134										
YELLOW	✳	129	129			102			135	135										
GREEN			130			103			136											
RED ARROW															A121					
YELLOW ARROW															A122					
FLASHING YELLOW ARROW															A123					
GREEN ARROW	127	130								136										

NU = Not Used
★ See pictorial of head wiring in detail this sheet.
✳ Denotes install load resistor. See load resistor installation detail this sheet.

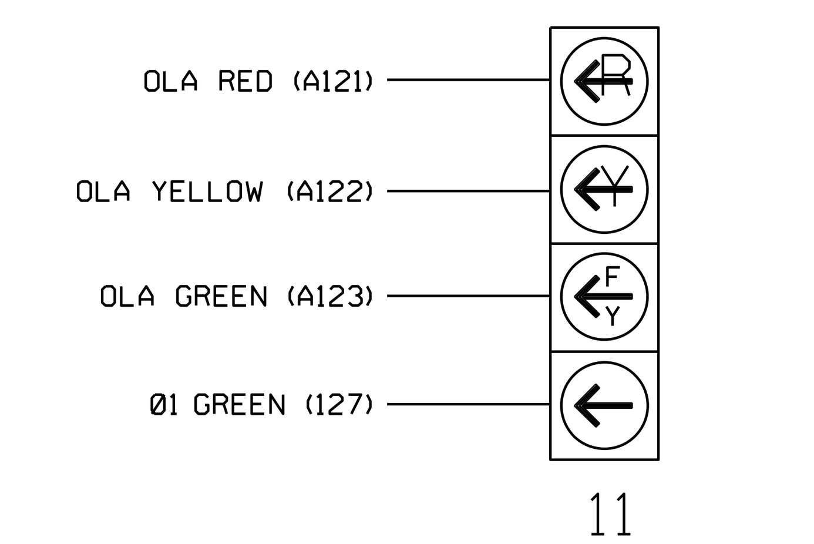
SPECIAL DETECTOR NOTE

For all loops, install a multizone microwave detection system for vehicle detection. Perform installation according to the manufacturer's directions and NCDOT engineer approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

For Detection Zone 1A, the equipment and slot reserved for wired input is typical for an NCDOT installation.

FYA SIGNAL WIRING DETAIL

(wire signal head as shown)



INPUT FILE POSITION LAYOUT

(front view)

		1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I"	U	Ø 1 1A	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	FS DC ISOLATOR
	L	NOT USED													ST DC ISOLATOR
FILE "J"	U	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	WIRED INPUT ⊗	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY
	L														

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	ADDED INITIAL	DETECTOR TYPE
1A [1]	TB2-1,2	I1U	56	1 ★	1	YES		15		N
	-	J4U	48	26★	6	YES		3		G

[1] Add jumper from I1-W to J4-W, on rear of input file.

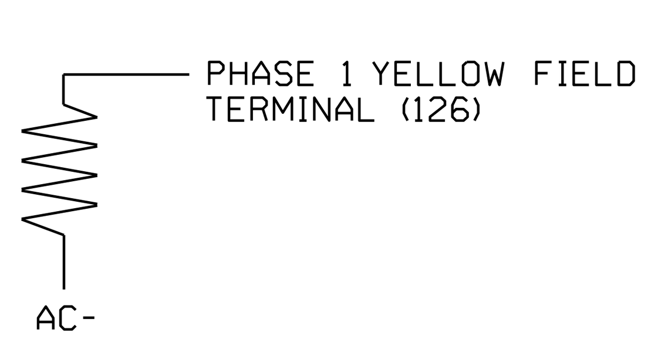
★ See vehicle detector setup programming detail for alternate phasing on sheet 2.

LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown)

ACCEPTABLE VALUES

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select [2. CONTROLLER]
2. From CONTROLLER Submenu select [2. VEHICLE OVERLAPS]

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

```
TMG VEH OVLP...[A] TYPE: .....PPLT FYA

PROTECTED LEFT TURN....   PHASE   1
OPPOSING THROUGH.......   PHASE   2

FLASHING ARROW OUTPUT.....CH9 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 1
```

← NOTICE ACTION PLAN SF BIT 1

END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 07-2090T3
DESIGNED: Mar 2025
SEALED: 3/17/2025
REVISED: N/A

Electrical Detail - Sheet 1 of 3
Temporary Design 3 - (TMP Phase III)

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

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ELECTRICAL AND PROGRAMMING DETAILS FOR:

Transportation Mobility and Safety Division – ITS & Signals Management Section
750 N.Greenfield Pkwy,Garner,NC 27529

NC 62 (Liberty Road)
at
I-85 Southbound Ramps

Division 7 Guilford County Archdale

PLAN DATE: March 2025	REVIEWED BY: ZM Esposito
PREPARED BY: AW Poole	RKA PROJ. NO.: 17380 (040)

REVISIONS	INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 054155 ENGINEER ZACHARY M. ESPOSITO

Signed by: Zachary M. Esposito
5647DD76D4884E8...
SIGNATURE 3/17/2025 DATE

SIG. INVENTORY NO. 07-2090T3