ECONOLITE ASC/3-2070 LOAD SWITCH ASSIGNMENT DETAIL

(program controller as shown)

All channels must be programmed to flash red in controller for red-red flash operation as shown below. Notice "AUT" for all channels is programmed for RED.

- 1. From Main Menu select 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select 3. LOAD SW ASSIGN

ECONOLITE ASC/3-2070 STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select | 2. CONTROLLER
- 2. From CONTROLLER Submenu select 5. START/FLASH

Scroll down on this screen and set "Exit FI" to Green "G"

ASC/3 FLASH SENSE INPUT CONTROL FOR RED-RED FLASH

*The NCDOT default database is programmed to addresss Yellow-Red flash. Logic Statement 100 must be modified as shown when running Red-Red flash.

- 1. From Main Menu select 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR
- 3. From LOGIC PROCESSOR Submenu select 2. LOGIC STATEMENTS

THIS STATEMENT IS USED

SENSE INPUT WHEN RUNNING RED-RED FLASH OPERATION.

TO CONTROL THE FLASH

Change the "LP" to 100 and move the cursor down. Delete the two "CTR-SET" statements by moving the cursor over them and hitting the "C" key. then hit "ENTER", select "LP SET CIB ON", hit "ENT", and then set the number to 427.

LP#:100 COPY FROM:100 ACTIVE: M FALSE
IF LP CIB CODE ON 331 F

THEN LP DELAY FOR 1.0 SECONDS
LP SET CIB ON 427

ELSE

Hit "ESC", then 1 for "LOGIC STATEMENT CONTROL", next verify that LP#100 is ENABLED.

END PROGRAMMING

U-6018 Sig-3.3

ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT, PHASING DURING FREE RUN - PROGRAM CHANGES (SHOWN BELOW) IN A TIME BASED ACTION PLAN,
SCHEDULE A DAY PLAN THAT INCLUDES THE ACTION PLAN PROGRAMMED
TO SELECT VEH DET PLAN 2 AND ENABLE SF BIT 1.

TO RUN ALT. PHASING DURING COORDINATION - SELECT THE TIME BASED ACTION PLAN THAT IS PROGRAMMED

TO SELECT VEH DET PLAN 2 AND ENABLE SF BIT 1.

VEH DET PLAN	SF BITS ENABLED
1	NONE
2	1
_	1 2

IMPORTANT: IF ALT. PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERATE TIME OF DAY EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN SF BIT 1 AND VEH DET PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

SF BIT 1:

Modifies overlap parent phases for head 11 to run protected

turns only.

VEH DET PLAN 2:

Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 0 seconds.

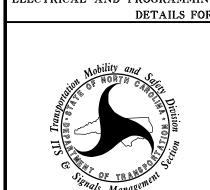
THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-2090T2

DESIGNED: Mar 2025

SEALED: 3/17/2025

REVISED: N/A

Electrical Detail - Sheet 3 of 3 Temporary Design 2 - (TMP Phase II)



NC 62 (Liberty Road)
at

I-85 Southbound Ramps
Division 7 Guilford County Arc

PLAN DATE: March 2025 REVIEWED BY: ZM Esposito
PREPARED BY: AW Poole RKA PROJ. NO.: 17380 (040)
REVISIONS INIT. DATE

SEAL
054 155

Signed by:

Jachary M. Esposito
3/17/2025

SIG. INVENTORY NO. 07-2090T2

DOCUMENT NOT CONSIDERED

FINAL UNLESS ALL SIGNATURES COMPLETED

