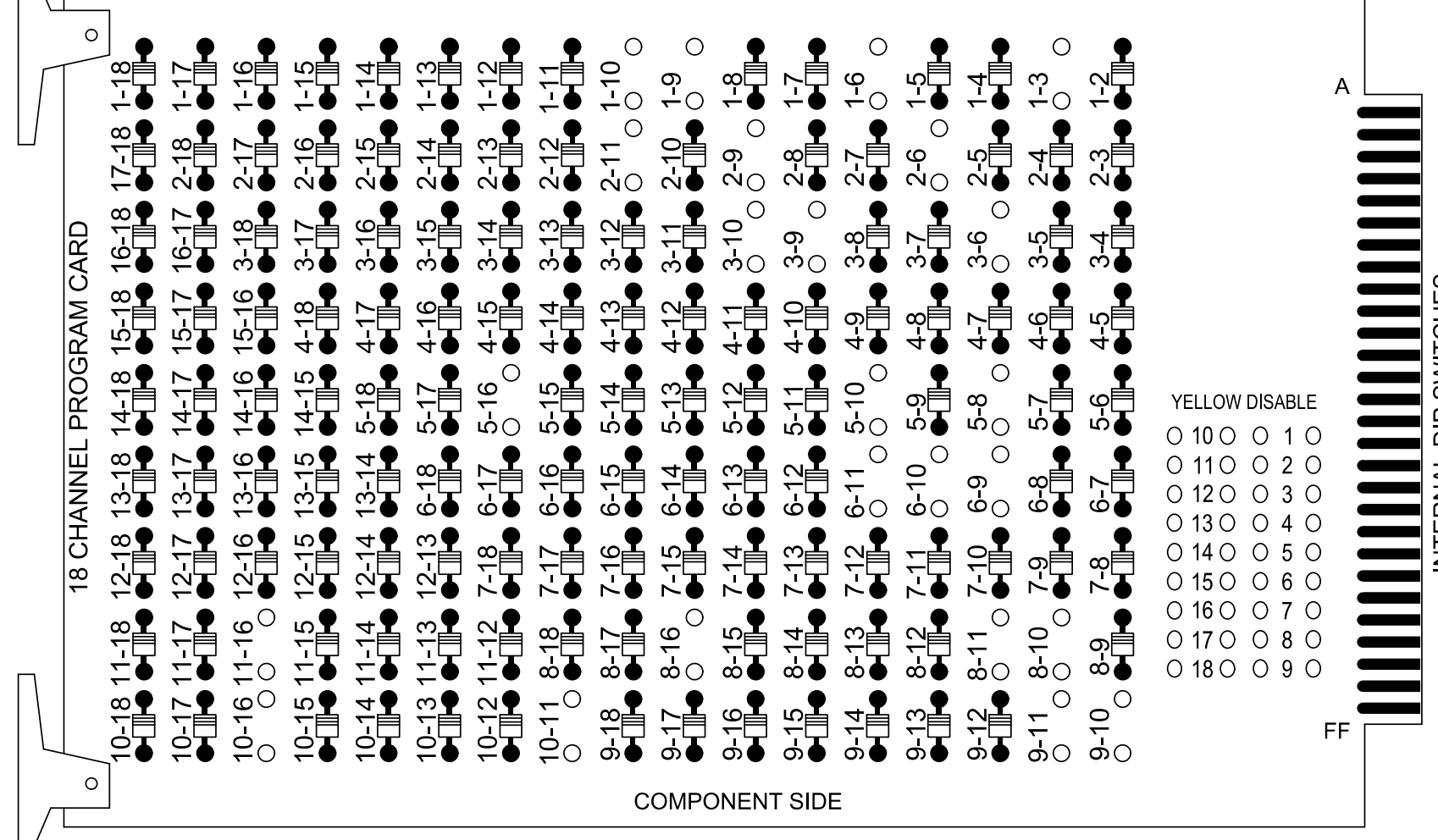


### 18 CHANNEL CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

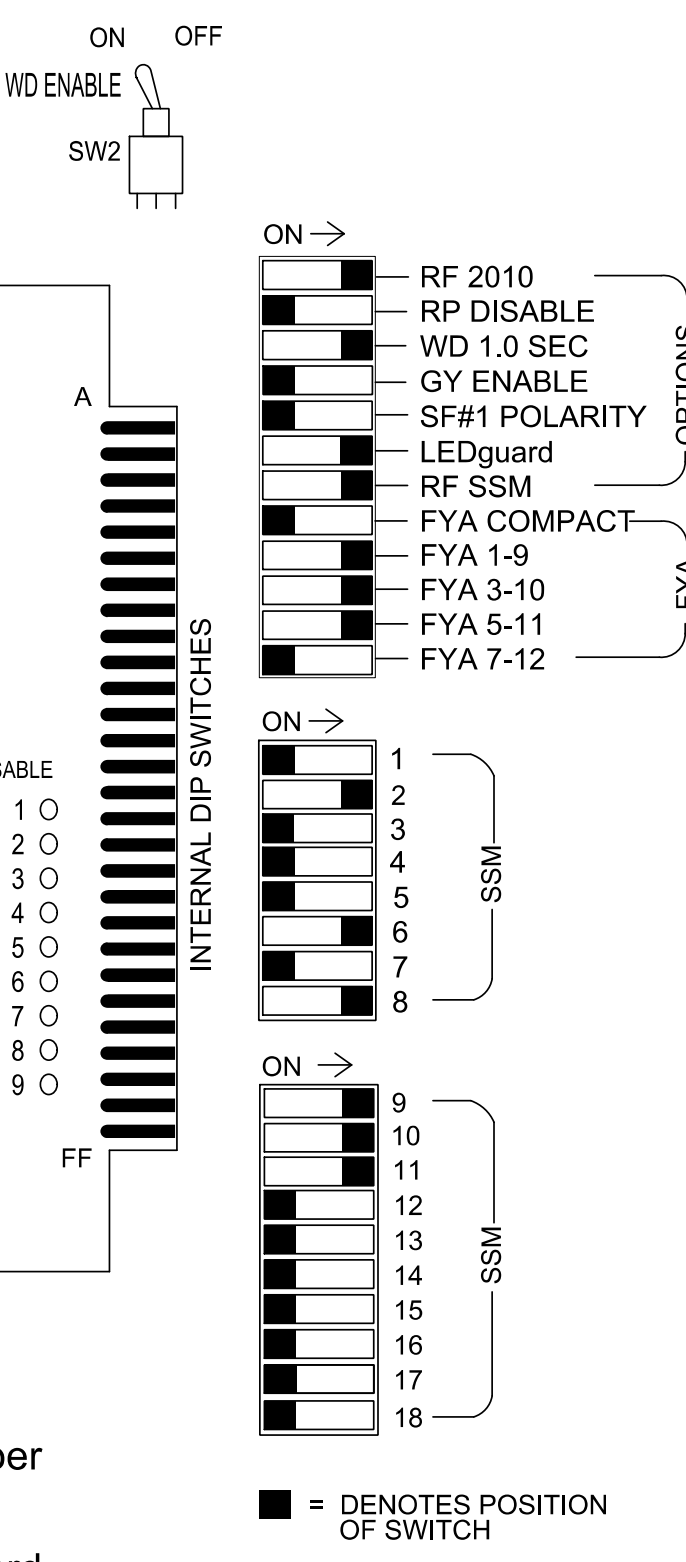
REMOVE DIODE JUMPERS 1-3, 1-6, 1-9, 1-10, 2-6, 2-9, 2-11, 3-6, 3-9, 3-10, 5-8, 5-10, 5-16, 6-9, 6-10, 6-11, 8-10, 8-11, 8-16, 9-10, 9-11, 10-11, 10-16 and 11-16.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that the Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



### NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
- The cabinet and controller are part of the (SR 2700 (Chatham Park Way) Closed Loop System) Signal System #: D08-35\_Pittsboro.

### EQUIPMENT INFORMATION

Controller.....2070LX  
 Cabinet.....332 w/ Aux  
 Software.....Q-Free MAXTIME  
 Cabinet Mount.....Base  
 Output File Positions.....18 With Aux. Output File  
 Load Switches Used.....S1, S2, S4, S7, S8, S11, S12, AUX S1, AUX S2, AUX S4  
 Phases Used.....1,2,6,8,8PED  
 Overlap "1".....\*  
 Overlap "2".....\*  
 Overlap "3".....\*  
 Overlap "4".....NOT USED  
 Overlap "5".....NOT USED  
 Overlap "6".....NOT USED  
 Overlap "7".....\*  
 Overlap "8".....\*

\*See overlap programming detail on sheet 2

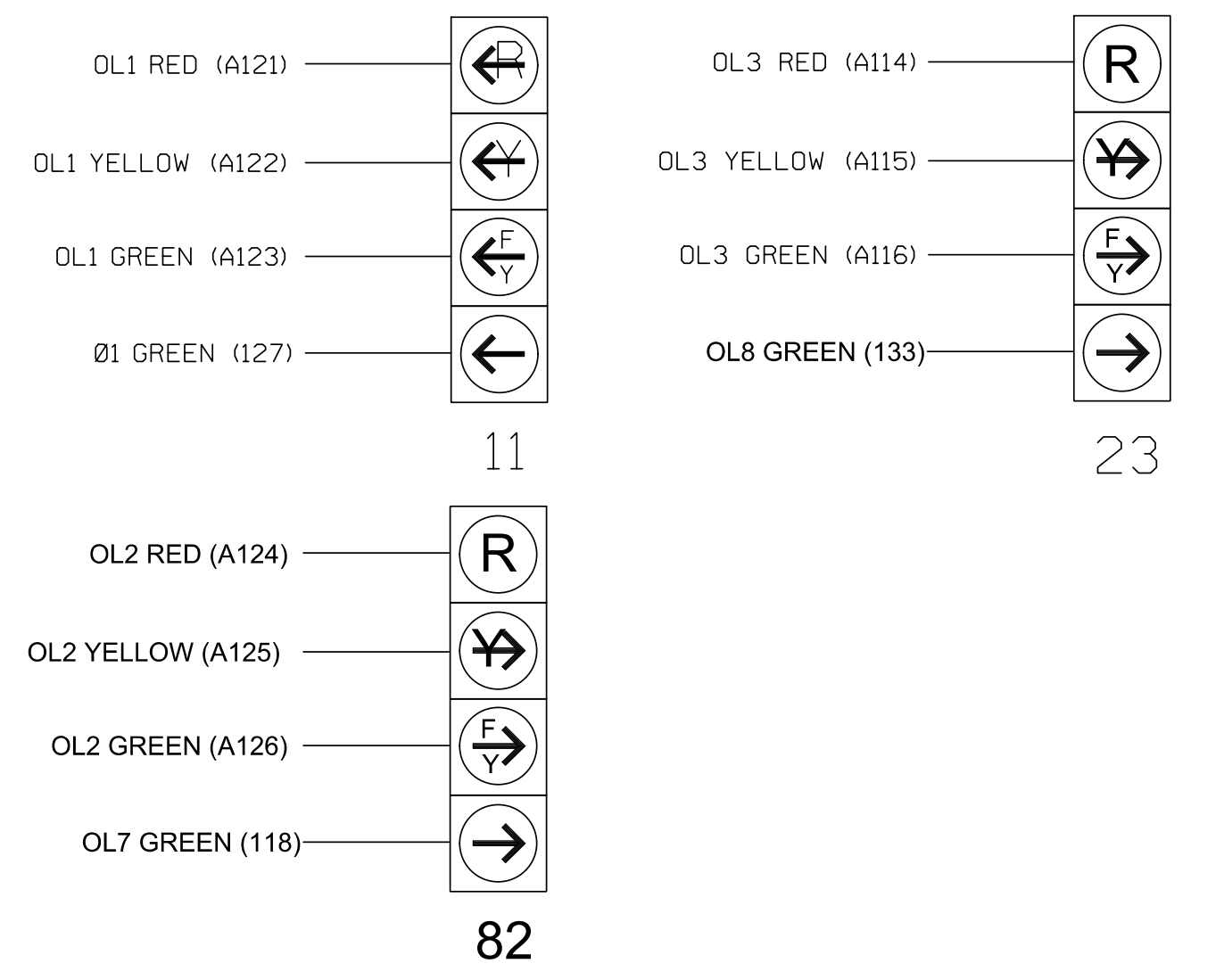
### SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	OL7	4	4 PED	OL8	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	11	21	22	NU	82	NU	23	61,62	NU	NU	81	P81, P82	11	82	NU	23	NU	NU
RED	128	128						134			107			A124		A114		
YELLOW	*	129	129	*			*	135										
GREEN		130						136										
RED ARROW														A121				
YELLOW ARROW											108			A122	A125	A115		
FLASHING YELLOW ARROW														A123	A126	A116		
GREEN ARROW	127	130		118				133			109							
Hand icon																		110
Walking person icon																		112

NU = Not Used  
 \* Denotes install load resistor. See load resistor installation detail this sheet.  
 ★ See pictorial of head wiring in detail this sheet.

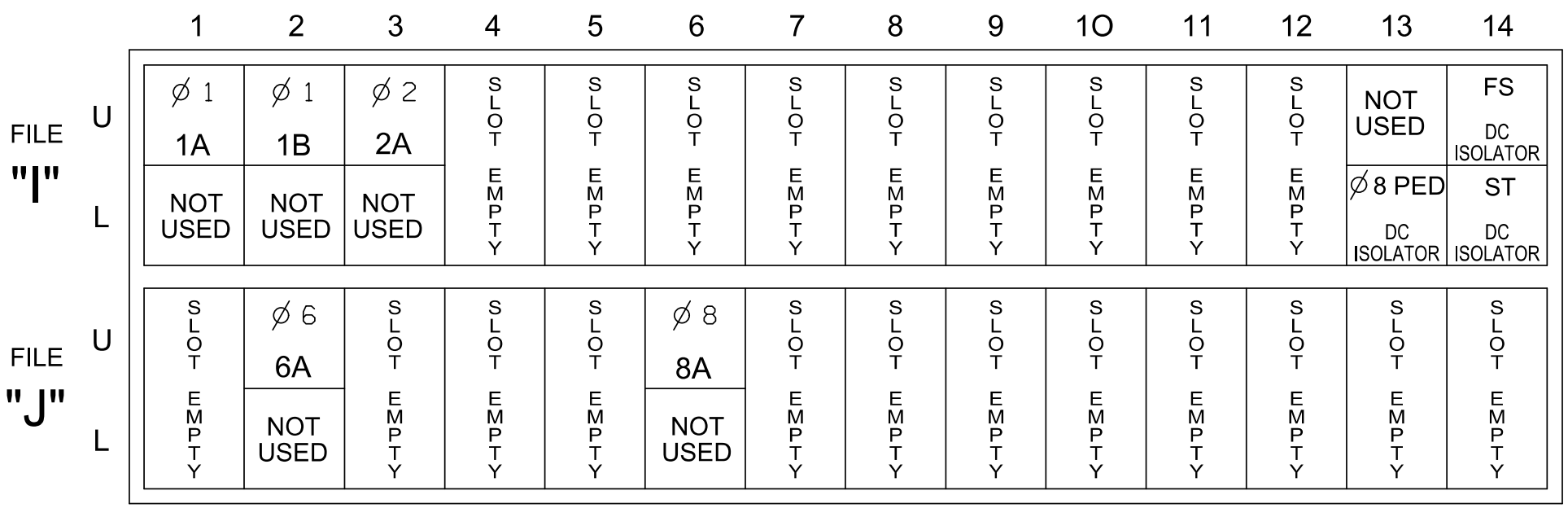
### FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



### INPUT FILE POSITION LAYOUT

(front view)



EX. : 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

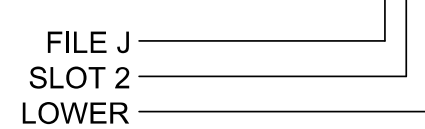
### INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
1A	TB2-1,2	I1U	56	18	1 ★	1	15.0		X		X	
					29 ★	6	3.0		X		X	X
1B	TB2-5,6	I2U	39	1	2	1	15.0		X		X	
2A	TB2-9,10	I3U	63	29	4	2			X	X	X	
6A	TB3-5,6	J2U	40	2	16	6			X	X	X	
8A	TB5-9,10	J6U	42	4	22	8			X		X	
PED PUSH BUTTONS												
P81,P82	TB8-8,9	I13L	70	36	8	8 PED						

NOTE: INSTALL DC ISOLATOR IN INPUT FILE SLOT I13.

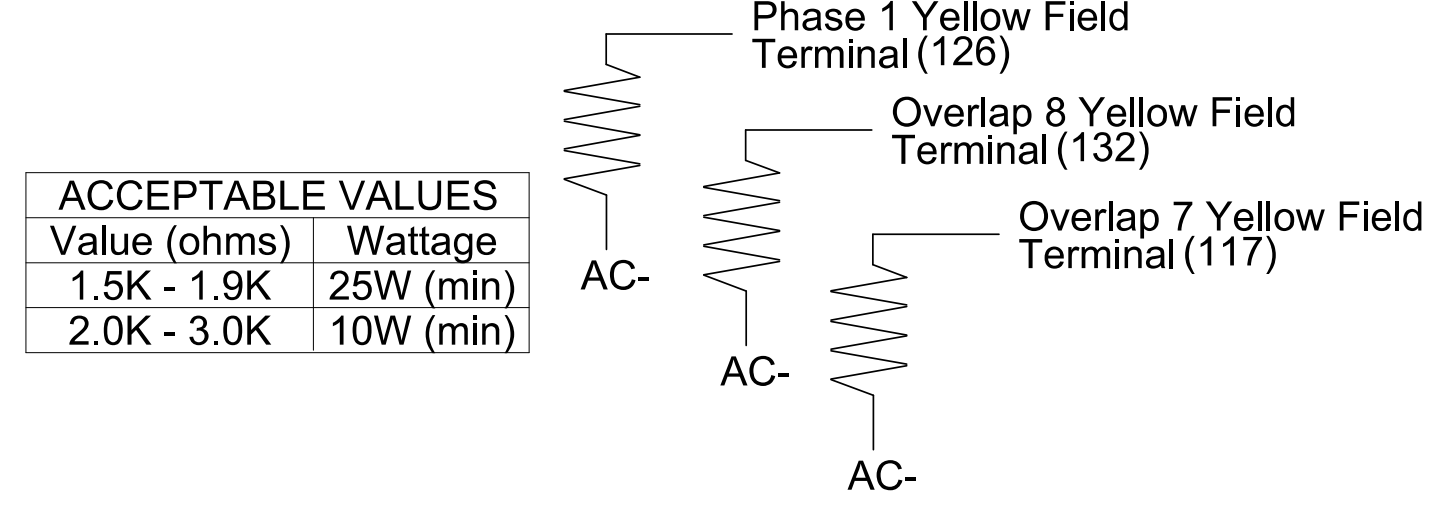
★ For the detectors to work as shown on the signal design plan, see the Vehicle Detector Setup Programming Detail for Alternate Phasing on sheet 2.

### INPUT FILE POSITION LEGEND: J2L



### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)



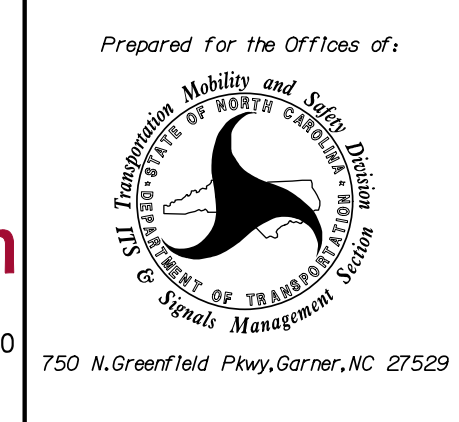
Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)

### COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

Electrical Detail Sheet 1 of 2

ELECTRICAL AND PROGRAMMING DETAILS FOR:



SR 2700 (Chatham Park Way) at US 64 WB Ramps

Division 8	Chatham County	Pittsboro
PLAN DATE: April 2024	REVIEWED BY: KP Baumann	
PREPARED BY: SP Pennington	REVIEWED BY:	
REVISIONS	INIT.	DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

12/12/2024

SIG. INVENTORY NO. 08-0520