(remove jumpers and set switches as shown)

2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.

3. Ensure that the Red Enable is active at all times during normal operation.

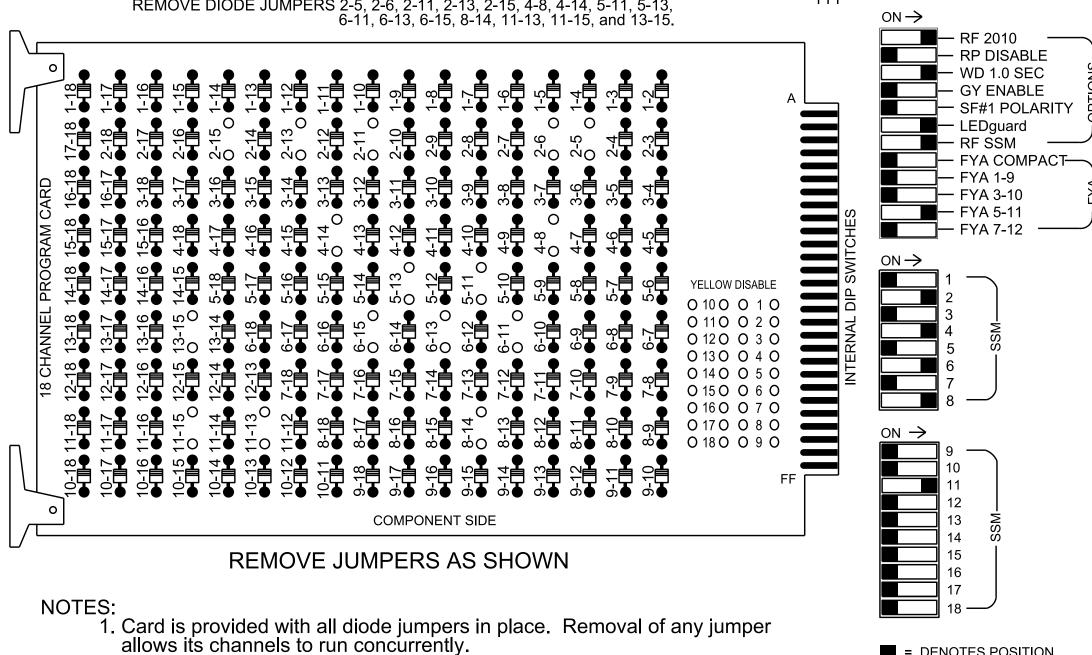
NOT USED

INPUT FILE POSITION LAYOUT

(front view)

4. Integrate monitor with Ethernet network in cabinet.

REMOVE DIODE JUMPERS 2-5, 2-6, 2-11, 2-13, 2-15, 4-8, 4-14, 5-11, 5-13, 6-11, 6-13, 6-15, 8-14, 11-13, 11-15, and 13-15.



- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- 3. Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- 4. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
- 5. The cabinet and controller are part of the D13-22\_Asheville System.

### **EQUIPMENT INFORMATION**

Controller	2070LX
Cabinet	332 w/ Aux
Software	Q-Free MAXTIME
Cabinet Mount	Base
Output File Positions	18 With Aux. Output File
Load Switches Used	S2, S3, S5, S7, S8, S9,
	S11, S12, AUX S4
Phases Used	2, 2PED, 4, 5, 6, 6PED,8, 8PED
Overlap "1"	NOT USED
Overlap "2"	NOT USED
Overlap "3"	*
Overlap "4"	

### NOTES

- 2. Program phases 4 and 8 for Dual Entry and Simultaneous Start.

\*See overlap programming detail on Sheet 2.

## COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

SIGNAL HEAD HOOK-UP CHART

134

136

**\*** | 135 |

133

NU 41,42 NU

101

102

103

\*Denotes install load resistor. See load resistor installation detail this sheet.

S1 S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 AUX AUX AUX AUX S5 S6 S6

8 OL1 OL2 SPARE OL3 OL4 SPARE

NU = Not Used

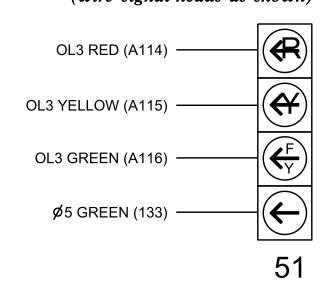
51 61,62 P61, P62 NU 81,82 P81, P82 NU NU NU 51 NU NU NU

108

109

### FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)

Phase 5 Yellow Field Terminal (132)

THIS PLAN SUPERSEDES THE PLAN SIGNED AND SEALED ON 12/7/2024.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 13-0218 DESIGNED: December 2023 SEALED: 2/9/2024 **REVISED:** 

#### Electrical Detail

CMU CHANNEL NO.

PHASE

SIGNAL HEAD NO

RED

YELLOW

**GREEN** 

RED **ARROW** 

YELLOW ARROW FLASHING

YELLOW

ARROW GREEN

ARROW

128

129

130

113

★See pictorial of head wiring in detail this sheet.

# Electrical and Programming Details For: Prepared in the Offices of:

## US 19-23 Business / SR 3548 (Haywood Road)

I-240 EB Ramp / Hanover Street PLAN DATE: January 2024 REVIEWED BY:

D.T.J. PREPARED BY: D.J. Craddock REVIEWED BY: REVISIONS

D. Todd Joya 02/09/2024 SIG. INVENTORY NO. 13-0218

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

031001

### INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN	
4A	TB4-9,10	I6U	41	3	8	4	10		Х		Х		
5A	TB3-1,2	J1U	55	17	15	5	15		Х		Х		
DA DA	103-1,2	310	00	-	31	2			Х		Х		
6A	TB3-5,6	J2U	40	2	16	6			Х		Χ		
6B	TB3-7,8	J2L	44	6	17	6			Х		Χ		
8A	TB5-9,10	J6U	42	4	22	8	10		Х		Χ		
PED PUSH BUTTONS							NOTE:	1 00 1001		NPUT FILE	POSITIC	N LEGEN	D: J2L
P61,P62	TB8-7,9	I13U	68	34	6	PED 6	INSTALL DC ISOLATOR IN INPUT FILE SLOT I13. FILE J						
P81,P82	TB8-8,9	I13L	70	36	8	PED 8	]			SL	OT 2 —		
•							=			LO	WER		

### SPECIAL DETECTOR NOTE

For Zone 2A install a multizone microwave detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineerapproved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

### **OVERLAP PROGRAMMING**

Front Panel

FILE

5A

NOT USED

EX.: 1A, 2A, ETC. = LOOP NO.'S

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

NOTE: REMOVE EXISTING JUMPERS ASSOCIATED W/ DETECTOR SLOTS I5 AND I8 FROM REAR OF INPUT FILE.

Web Interface

Home >Controller >Overlap Configuration >Overlaps

#### Overlap Plan 1

Overlap	1	2	3	4
Type	Off	Off	FYA 4 - Section	Off
Included Phases	ı		6	ı
Modifier Phases	1	•	5	•
Modifier Overlaps	•	-	•	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

### **BACKUP PREVENTION PROGRAMMING**

Front Panel

FS = FLASH SENSE

ST = STOP TIME

Ø6PED FS

Ø8 PED ST

DC DC ISOLATOR ISOLATOR

DC DC SOLATOR ISOLATOR

ON

SW2

WD ENABLE  $\Omega$ 

Main Menu >Controller >Sequence & Phs Config >Backup Prevention >Backup Protection Plan

Web Interface

Home >Controller> Backup Prevention >Backup Protection Plan

#### Sequence 1

No Backup Phase	1	2	3	4	5	6	7	8
Serve Phase 1	1	ı	ı	ı	ı	ı	ı	ı
Serve Phase 2	ı	ı	ı	ı	ı	ı	ı	-
Serve Phase 3	-	-	1	-	1	-	-	-
Serve Phase 4	-	-	1	-	1	-	-	-
Serve Phase 5	-	-	1	-	1	-	-	-
Serve Phase 6	-	-	1	-	X	-	-	-
Serve Phase 7								-
Serve Phase 8	-	-	1	-	1	-	-	-

### ALL RED BACKUP **PROGRAMMING**

Front Panel

Main Menu > Controller > Sequence & Phs Config > Backup Prevention >Backup Through Red

Web Interface

Home >Controller >Backup Prevention >Backup Calls Phase Plans >(scroll down) to Backup Through Red

Backup Through All Red

Sequence	Backup Through All Red
1	YES