

OVERLAP PROGRAMMING DETAIL FOR OVERLAPS A, B, C, And D*

(program controller as shown below)

*NOTE FOR ALL OVERLAPS: Use Default values for Overlap 'PLUS' programming details

FROM MAIN MENU PRESS "1" CONTROLLER
AND THEN "5" OVERLAPS

Overlaps
1.General Parm
2.Program
3.Status

General Overlap Parameters
Lock Inhibit OFF
Conf Lock Enable OFF
Parent P Clnocs ON
Extra Included Phases OFF

PRESS "ESC"

Overlaps
1.General Parm
2.Program
3.Status

Enter Overlap # 1
then press Enter

Overlap A-1
1.Program Parm
2.Confl Prog+
3.Program Parm+

Notice
Type
FYA-4

OvrIp A-1 Ps.....
Included Ps 1 0 0 0 0 0 0 0
Modifier Ps 2 0 0 0 0 0 0 0
Type:FYA-4 Grn: 0 Yel: 3.5 Red: 1.5

PRESS "ESC" TWICE

Enter Overlap # 2
then press Enter

Overlap B-2
1.Program Parm
2.Confl Prog+
3.Program Parm+

OvrIp B-2 Ps.....
Included Ps 2 0 0 0 0 0 0 0
Modifier Ps 0 0 0 0 0 0 0 0
Type:NORMAL Grn: 0 Yel: 3.5 Red: 1.5

PRESS "ESC" TWICE

Enter Overlap # 3
then press Enter

Overlap C-3
1.Program Parm
2.Confl Prog+
3.Program Parm+

OvrIp C-3 Ps.....
Included Ps 6 0 0 0 0 0 0 0
Modifier Ps 0 0 0 0 0 0 0 0
Type:NORMAL Grn: 0 Yel: 3.5 Red: 1.5

PRESS "ESC" TWICE

Enter Overlap # 4
then press Enter

Overlap D-4
1.Program Parm
2.Confl Prog+
3.Program Parm+

OvrIp D-4 Ps.....
Included Ps 8 0 0 0 0 0 0 0
Modifier Ps 0 0 0 0 0 0 0 0
Type:NORMAL Grn: 0 Yel: 3.5 Red: 1.5

END OF OVERLAP PROGRAMMING DETAIL

NOTE

IF PRESENT, REMOVE PROGRAMMING FOR OVERLAP E.

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO ENSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1689
DESIGNED: August 2024
SEALED: August 22, 2024
REVISED:

Signal Upgrade - Final Design
Electrical Detail - Sheet 2 of 4

DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING
DETAILS FOR:

SR 1556 (Gallimore Dairy Rd.)

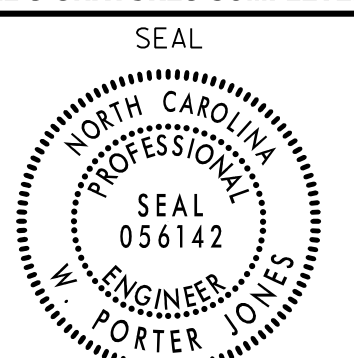
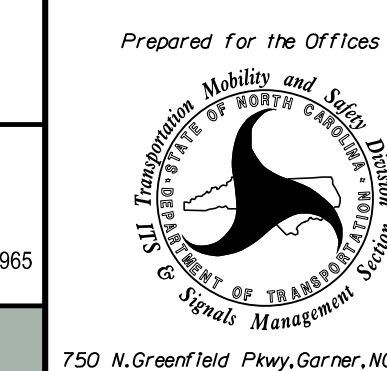
at
SR 1554 (Chimney Rock Rd.)/
Simply Southern

Division 7 Guilford County Greensboro

PLAN DATE: August 2024 REVIEWED BY: DT Sears

PREPARED BY: WP Erickson-Jones REVIEWED BY:

REVISIONS INIT. DATE



Porter Jones 8/22/2024

SIGNATURE DATE

SIG. INVENTORY NO. 07-1689