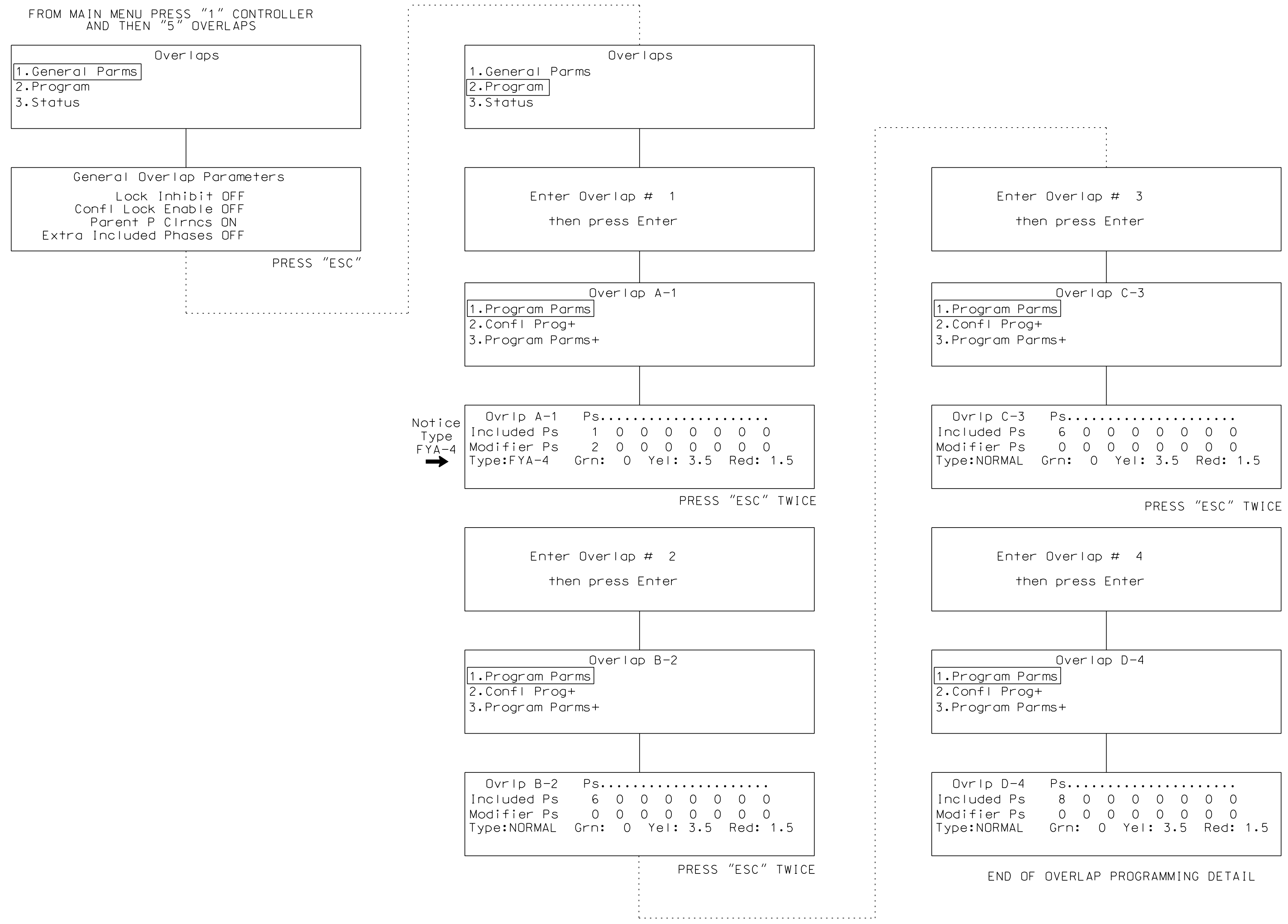


OVERLAP PROGRAMMING DETAIL FOR OVERLAPS A, B, C, and D*

(program controller as shown below)

*NOTE FOR ALL OVERLAPS: Use Default values for Overlap 'PLUS' programming details



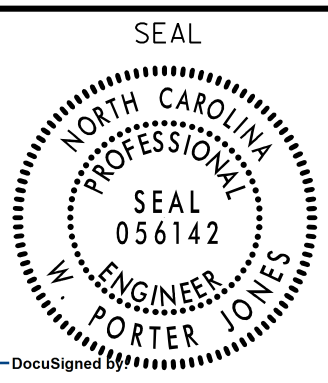


Notice
Type
FYA-4

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 07-1689T1
DESIGNED: August 2024
SEALED: August 22, 2024
REVISED:

Signal Upgrade - Temporary Design 1
(TMP Phase I) Electrical Detail - Sheet 2 of 3

DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED

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		<p>Division 7 Guilford County Greensboro</p> <p>PLAN DATE: August 2024 REVIEWED BY: DT Sears</p> <p>PREPARED BY: WP Erickson-Jones REVIEWED BY:</p>	<table border="1"> <thead> <tr> <th>REVISIONS</th> <th>INIT.</th> <th>DATE</th> </tr> </thead> <tbody> <tr> <td> </td> <td> </td> <td> </td> </tr> <tr> <td> </td> <td> </td> <td> </td> </tr> <tr> <td> </td> <td> </td> <td> </td> </tr> </tbody> </table>		REVISIONS	INIT.	DATE							
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8/22/2024
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