### ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT. PHASING DURING <u>Free run</u> - Program Changes (Shown Below) in a time based action plan. SCHEDULE A DAY PLAN THAT INCLUDES THE ACTION PLAN PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 3 AND 7

TO RUN ALT. PHASING DURING COORDINATION - SELECT THE TIME BASED ACTION PLAN THAT IS PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 3 AND 7.

PHAS I NG	VEH DET PLAN	SF BITS ENABLED
ACTIONS REQUIRED TO RUN DEFAULT PHASING	1	NONE
ACTIONS REQUIRED TO RUN <u>ALTERNATE PHASING</u>	2	3, 7

IMPORTANT: IF ALT, PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERATE TIME OF DAY EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).

#### ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN SF BITS 3 AND 7 AND VEH DET PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

SF BITS 3,7:

Modifies overlap parent phases for heads 31 and 71 to run protected turns only.

VEH DET PLAN 2: Disables phase 8 call on loop 3A and reduces delay time for phase 3 call on loop 3A to 3 seconds.

> Disables phase 4 call on loop 7A and reduces delay time for phase 7 call on loop 7A to 3 seconds.

### ASC/3 FLASH SENSE INPUT CONTROL FOR RED-RED FLASH

\*The NCDOT default database is programmed to addresss Yellow-Red flash. Logic Statement 100 must be modified as shown when running Red-Red flash.

- 1. From Main Menu select 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select | 8. LOGIC PROCESSOR
- 3. From LOGIC PROCESSOR Submenu select | 2. LOGIC STATEMENTS

Change the "LP" to 100 and move the cursor down. Delete the two "CTR-SET" statements by moving the cursor over them and hitting the "C" key. then hit "ENTER", select "LP SET CIB ON", hit "ENT", and then set the number to 427.

LP#:100 COPY FROM:100 ACTIVE: M FALSE 331 F IF LP CIB CODE ON

1.0 SECONDS THEN LP DELAY FOR LP SET CIB ON 427

ELSE

THIS STATEMENT IS USED TO CONTROL THE FLASH SENSE INPUT WHEN RUNNING RED-RED FLASH OPERATION.

Hit "ESC", then 1 for "LOGIC STATEMENT CONTROL", next verify that LP#100 is ENABLED.

END PROGRAMMING

## ECONOLITE ASC/3-2070 STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select | 2. CONTROLLER
- 2. From CONTROLLER Submenu select | 5. START/FLASH

START/FLASH DATA ----START UP-----1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 ABCDEFGHIJKLMNOP FLASH>MON. NO FL TIME.. O ALL RED... 6 PWR START SEQ.. 1 MUTCD→ YES Y- G: NO

Scroll down on this screen and set "Exit FI" to Green "G"

# ECONOLITE ASC/3-2070 ACTION PLAN

PROJECT REFERENCE NO. | Sig. 2.3 U-4015A

### PROGRAMMING DETAIL

- 1. From Main Menu select | 5. TIME BASE
- 2. From TIME BASE Submenu select 2. ACTION PLAN

PATTERN	SYS OVERRIDE NO																
TIMING PLAN 0							SEQUENCE 0										
VEH DETECTOR PLAN. 2 FLASH						DET LOGNONE RED RESTNO											
																	VEH DET DIAG PLN O
DIMMING ENABLE NO						PRIORITY RETURN. NO											
PED PR RE	ΓUR	Ν		NO		QUE	UE	DEL	ΑΥ.		. \	10					
PMT COND [	DEL	ДΥ		NO													
PHASE	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	6	
PED RCL	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
WALK 2	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
VEX 2	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
VEH RCL	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
MAX RCL	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
MAX 2	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
PHASE	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	6	
MAX 3	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
CS INH	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
OMIT	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
SPC FCT	•	•	Χ	•	•	•	Χ	•	( 1	-8)							
AUX FCT	•	•	•	( 1	-3	)											
	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5		
LP 1-15	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•		
LP 16-30	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•		
LP 31-45	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•		
LP 46-60	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•		
LP 61-75	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•		
LP 76-90	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•		

\*The Action Plan number(s) are to be determined by the Division and/or City Traffic Engineer.

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1438T1 DESIGNED: August 2024 SEALED: August 22, 2024 REVISED:

Signal Upgrade - Temporary Design 1 (TMP Phase I-II) - Electrical Detail - Sheet 3 of 3

ELECTRICAL AND PROGRAMMING Prepared for the Offices of:

NC 68 (Eastchester Dr.) SR 1556 (Gallimore Dairy Rd.)

Guilford County PLAN DATE: August 2024 REVIEWED BY: DT Sears PREPARED BY: WP Erickson-Jones REVIEWED BY: REVISIONS SIG. INVENTORY NO. 07-1438T1

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