

750 N. Greenfield Pkwy, Garner, NC 27529

D. Todd Joyce, P.E. - Signal Equipment Design Review Engineer

Gregory A. Green - Signal Communications Project Engineer

Standard Metal Pole Drawings

Signal Communication Plans

Sig. M1–M8 SCP1–SCP11 DEFAULT

PHASING DIAGRAM

04+8

Ø2+6

Ø2+5

01+6

Ø1+5

PHASING DIAGRAM DETECTION LEGEND

DETECTED MOVEMENT

UNSIGNALIZED MOVEMENT PEDESTRIAN MOVEMENT

Sig-2.0 U-3422A LEGEND

**EXISTING** 

 $\longrightarrow$ 

10/3/2024

MAXTIME DETECTOR INSTALLATION CHART **DETECTOR PROGRAMMING** DISTANCE CALL DELAY EXTEND ON LINI O ZONE 1 | 15.0\* | - | X | - | X | 6X40 6# 3.0 5 | 15.0\* - | X | - | X | - | @ 2# 3.0 - X - X X @ @ | @ | 8 | 3.0 | - | X | - | X | - | @ @ | @ | 8 | 15.0 | - | X | - | X | - | @ \* Reduce Delay To 3 Seconds During Alternate Phasing Operation. # Disable Phase Call For Loop During Alternate Phasing Operation.

#### @ Multi-zone Microwave Detection.

Wood Pole #2 -L- STA. 63+53 +/-

#### SIGNAL FACE I.D.

21,22 41,42 61,62 81,82

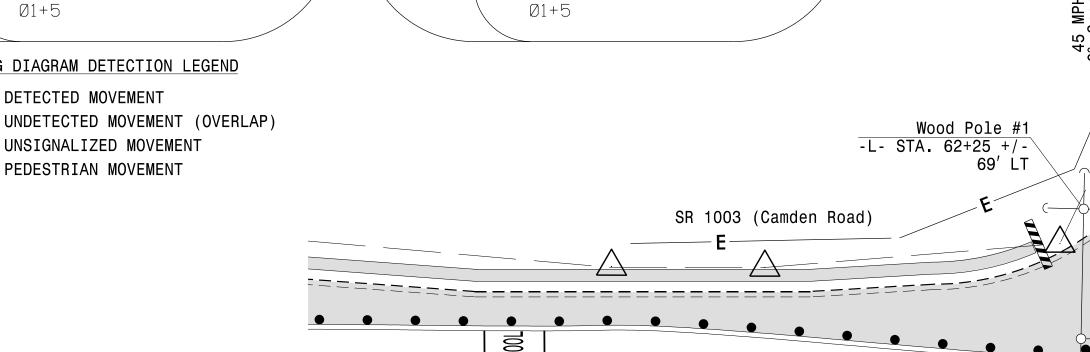
0% Grade

SR 1003 (Camden Road)

**Plans Prepared By:** 

8210 University Executive Park Drive, Suite 220 Charlotte, NC 28262 NC License No. F-1524 (704) 332-2289 www.DRMP.com

All Heads L.E.D.



Ø4+8

ALTERNATE

PHASING DIAGRAM

02+6

02+5

01+6

DEFAULT PHASING TABLE OF OPERATION

SIGNAL

FACE

21,22

41,42

51

61,62

63

81,82

PHASE

- F F F

ALTERNATE PHASING TABLE OF OPERATION

SIGNAL

FACE

21,22

41,42

51

61,62

63

81,82

PHASE

\_\_\_\_\_\_\_ 45 MPH 0% Grade

PUE —

Intercept existing conduits/risers and reuse

PUE

	MAX	TIME T	IMING	CHART		
FEATURE			PH	ASE		
FEATURE	1	2	4	5	6	8
Walk *	_	_	_	_	_	_
Ped Clear	_	_	_	_	_	_
Min Green*	7	12	7	7	12	7
Passage *	2.0	2.0	2.0	2.0	2.0	2.0
Max 1 *	15	90	20	15	90	20
Yellow Change	3.0	4.5	4.5	3.0	4.5	4.5
Red Clear	2.4	1.6	1.3	2.6	1.6	1.3
Added Initial *	_	_	_	_	_	_
Maximum Initial *	_	_	_	_	_	_
Time Before Reduction *	_	_	_	_	_	_
Time To Reduce *	_	_	_	_	_	_
Minimum Gap	_	_	_	_	_	_
Advance Walk	-	_	_	_	_	_
Non Lock Detector	Х	_	Х	Х	_	Х
Vehicle Recall	_	MIN RECALL	_	_	MIN RECALL	_
Dual Entry	_	_	Х	_	_	Х

PUE -

\* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

Microwave Detection System Sensor 1 (2A) | Sensor 2 (6A) **FUNCTION** Channel 6 Direction of Travel EΒ WB Detection Zone (ft) 100-500 100-500 **Enable Speed** 35-100 35-100 Speed Range (mph) Enable Estimated Time of Arrival Υ Estimated Time of Arrival (sec) 2.5-6.5 2.5-6.5

#### Signal Upgrade Temporary Design 1 - (TMP Phase 1)



SR 1003 (Camden Road) SR 1113 (Waldos Beach Road)/

**PROPOSED** 

Waldos Beach Road Division 6 Cumberland County PLAN DATE: August 2024 REVIEWED BY: LM Moon

N. Greenfield Pkwy. Garner. NC 27529 PREPARED BY: MR Stanley/DJW DRMP PROJ. NO.: 2400555 REVISIONS INIT. DATE 1"=40'

5 Phase Fully Actuated (D06-28\_Hope Mills)

#### NOTES

- 1. Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January
- 2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- 3. Phase 1 and/or phase 5 may be lagged.
- 4. Set all detector units to presence mode.
- 5. Locate new cabinet so as not to obstruct sight distance of vehicles turning right on red.
- 6. The Division Traffic Engineer will determine the hours of use for each phasing plan.
- 7. This intersection uses multi-zone microwave detection. Install detectors according to the manufacturer's instructions to achieve desired detection.
- 8. Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.

Traffic Signal Head  $\bigcirc$ **-**Modified Signal Head N/A Sign Pedestrian Signal Head With Push Button & Sign Signal Pole with Guy Signal Pole with Sidewalk Guy Metal Strain Pole Inductive Loop Detector Controller & Cabinet Junction Box Oversized Junction Box

2-in Underground Conduit Right of Way Directional Arrow

Microwave Detection Zone N/A Construction Zone N/A Construction Zone Drums N/A Type III Barricade N/A

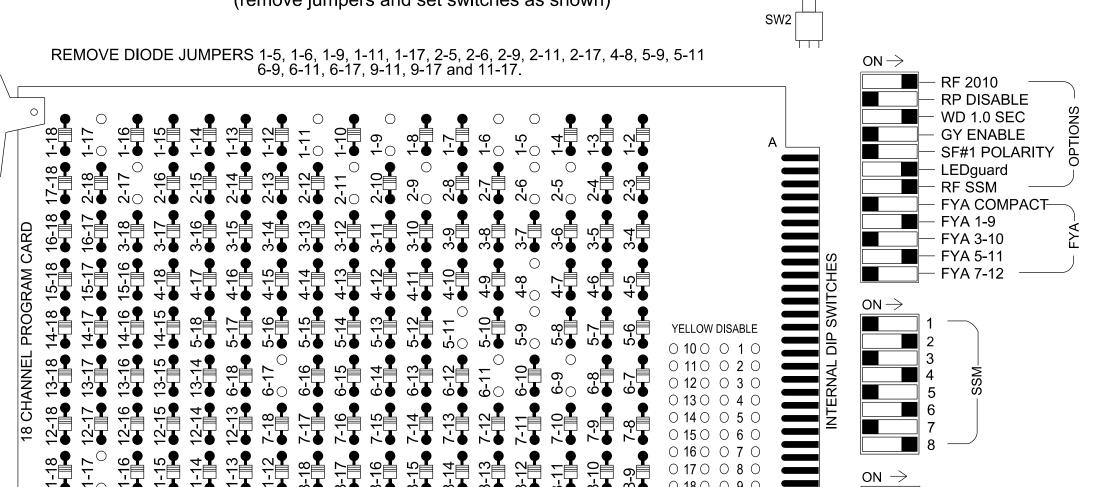
DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

**SEAL** 022516 Hope Mills Lisa Moon

SIG. INVENTORY NO. 06-1348T

#### 18 CHANNEL IP CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



ON OFF

= DENOTES POSITION OF SWITCH

FS = FLASH SENSE ST = STOP TIME

WD ENABLE (

#### REMOVE JUMPERS AS SHOWN

FILE

FILE

EX.: 1A, 2A, ETC. = LOOP NO.'S

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.

COMPONENT SIDE

- 2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- 3. Ensure that the Red Enable is active at all times during normal operation.

INPUT FILE POSITION LAYOUT

(front view)

Note: For Detection Zones 1A and 5A the equipment and slots reserved are typical for a NCDOT installation.

4. Integrate monitor with Ethernet network in cabinet.

#### NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- 2. Program phases 4 and 8 for Dual Entry.
- 3. Program controller to start up in phase 2 Green No Walk and Phase 6 Green No Walk.
- 4. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
- 5. The cabinet and controller are part of the D06-28\_Hope Mills Closed Loop Signal System.

#### **EQUIPMENT INFORMATION**

Controller Cabinet Software Cabinet Mount	
Output File Positions	
Phases Used Overlap "1"	<i></i>
Overlap "2" Overlap "3"	NOT USED
Overlap "4" Overlap "5"	NOT USED

\*See overlap programming detail on sheet 2.

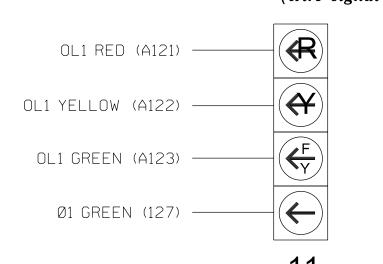
#### Sig-2 1 U-3422A

				SI	SNA	L H	ΙEΑ	DΗ	00	K-U	IP C	HA	RT					
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	OL5	OL3		SPARE
SIGNAL HEAD NO.	11	21,22	NU	NU	41,42	NU	<b>★</b> 51	61,62	NU	NU	81,82	NU	11	NU	<b>★</b> 63	<b>★</b> 51	NU	NU
RED		128			101			134			107				A111			
YELLOW	*	129			102		*	135			108							
GREEN		130			103			136			109							
RED ARROW													A121			A114		
YELLOW ARROW													A122		A112	A115		
FLASHING YELLOW ARROW													A123		A113	A116		
GREEN ARROW	127						133											

- \* Denotes install load resistor. See load resistor installation detail this sheet.
- ★ See pictorial of head wiring in detail this sheet.

#### FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



OL3 RED (A114) OL3 YELLOW (A115) — OL3 GREEN (A116) Ø5 GREEN (133) -

OL5 RED (A111) OL5 YELLOW (A112) -OL5 GREEN (A113)

51

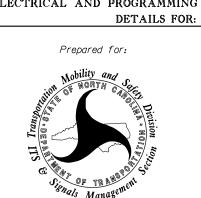
63

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1348T1

DESIGNED: Aug 2024 SEALED:

REVISED: N/A

Electrical Detail - Sheet 1 of 2



**Plans Prepared By:** 

SR 1003 (Camden Road)

Waldos Beach Road

August 2024 REVIEWED BY: LM Moon REVISIONS INIT. DATE

022516

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
4 A	TB2-1,2	I1U	56	18	1	1	15.0		Х		Х	
1A	-	-	-	-	29	6	3.0		Х		Χ	Х
5.0	TB3-1,2	J1U	55	17	15	5	15.0		Х		Х	
5A	-	-	-	-	31	2	3.0		Х		Х	Х

INPUT FILE POSITION LEGEND: J2L FILE J SLOT 2-LOWER-

#### SPECIAL DETECTOR NOTE

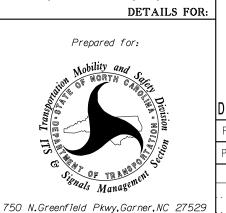
Install a multizone microwave detection zone for approaches for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

#### Phase 1 Yellow Field Terminal (126) Phase 5 Yellow Field Terminal (132) ACCEPTABLE VALUES Value (ohms) Wattage 1.5K - 1.9K | 25W (min) 2.0K - 3.0K | 10W (min)

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

#### Temporary Design 1 - (TMP Phase I) ELECTRICAL AND PROGRAMMING



## SR 1113 (Waldos Beach Road)/ Division 6 Cumberland County

Hope Mills PREPARED BY: MR Stanley/DJW DRMP PROJ. NO: 2400555

10/3/2024 SIG. INVENTORY NO. 06-1348TI

# MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

#### Overlap Plan 1

Overlap	1	3	5
Type	FYA 4 - Section	FYA 4-Section	FYA 4 - Section
Included Phases	2	6	6
Modifier Phases	1	5	-
Modifier Overlap	-	-	<u> </u>
Trail Green	0	0	0
Trail Yellow	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0

# MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

#### Overlap Plan 2

•				
Overlap	1	3	5	
Туре	FYA 4 - Section	FYA 4-Section	FYA 4 - Section	
Included Phases	-	-	6	NOTICE INCLUDED PHASE
Modifier Phases	1	5	-	
Modifier Overlap	-	•	-	
Trail Green	0	0	0	
Trail Yellow	0.0	0.0	0.0	
Trail Red	0.0	0.0	0.0	

# MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Coordination >Patterns

Web Interface

Home >Controller >Coordination >Patterns

Pattern Parameters

. attorri ara	11101010	
Pattern	Veh Det Plan	Overlap Plan
*	2	2

<sup>\*</sup>The Pattern number(s) are to be determined by the Division Traffic Engineer.

#### MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

#### ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases

for heads 11 and 51 to run protected

turns only.

VEH DET PLAN 2: Disables phase 6 call on loop 1A

and reduces delay time for phase 1 call on loop 1A to 3 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 3 seconds.

#### OUTPUT CHANNEL CONFIGURATION

Front Panel

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channel Configuration

#### **Channel Configuration**

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Phase Vehicle	1		Х	Χ	1
2	Phase Vehicle	2		Х		2
3	Phase Vehicle	3		Х	Х	3
4	Phase Vehicle	4		Х		4
5	Phase Vehicle	5		Х		5
6	Phase Vehicle	6		Х	Х	6
7	Phase Vehicle	7		Х		7
8	Phase Vehicle	8		Х	Х	8
9	Overlap	1		Х	Х	9
10	Overlap	2		Х	Х	10
11	Overlap	3		Х		11
12	Overlap	4		Х		12
13	Phase Ped	2				13
14	Phase Ped	4				14
15	Phase Ped	6				15
16	Phase Ped	8				16
17	Overlap	5		Χ	Х	17
18	Overlap	6		Х		18





# PROJECT REFERENCE NO. SHEET NO. U-3422A Sig-2.2

# MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 1A & 5A

Front Panel

Main Menu >Controller >Detector >Veh Det Plans

Web Interface

Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

1A

Detector	Call Phase	Delay
1	1	3.0
29	0	3.0

	Detector	Call Phase	Delay
5A	15	5	3.0
	31	0	3.0

# MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Unit

Web Interface

Home >Controller >Unit

Modify parameters as shown below and save changes.

Start Un Parameters

Start Up Farameters
StartUp Clearance Hold
6

Unit Flash Parameters

All Red Flash Exit Time

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1348T1

DESIGNED: Aug 2024 SEALED:

REVISED: N/A

Electrical Detail - Sheet 2 of 2 Temporary Design 1 - (TMP Phase I)

Prepared for:

Nobility and Signals Management Manageme

# SR 1003 (Camden Road) at

SR 1113 (Waldos Beach Road)/ Waldos Beach Road

Division 6 Cumberland County Hope Mills
PLAN DATE: August 2024 REVIEWED BY: LM Moon
PREPARED BY: MR Stanley/DJW DRMP PROJ. NO: 2400555

August 2024 REVIEWED BY: LM Moon

IR Stanley/DJW DRMP PROJ. NO: 2400555

REVISIONS INIT. DATE

Moon 10/3/2024

DATE

DATE

SIG. INVENTORY NO. 06-1348TI

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Sig-3.0 U-3422A

8 Phase Fully Actuated (D06-28\_Hope Mills)

#### NOTES

- 1. Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January 2024.
- 2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- 3. Phase 1 and/or phase 5 may be lagged.
- 4. Phase 3 and/or phase 7 may be lagged.
- 5. Set all detector units to presence mode.
- 6. The Division Traffic Engineer will determine the hours of use for each phasing plan
- 7. This intersection uses multi-zone microwave detection. Install detectors according to the manufacturer's instructions to achieve desired detection.
- 8. See traffic control plans for stop line locations.

**PROPOSED** 

 $\bigcirc$ 

N/A

9. Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.

LEGEND

Traffic Signal Head

Modified Signal Head

Sign

Pedestrian Signal Head

With Push Button & Sign

Signal Pole with Guy

Signal Pole with Sidewalk Guy

Metal Strain Pole

Inductive Loop Detector

Controller & Cabinet

Junction Box

Oversized Junction Box

2-in Underground Conduit

Right of Way

Directional Arrow

Microwave Detection Zone

Construction Zone

Construction Zone Drums

**EXISTING** 

**●**→

N/A

N/A

N/A

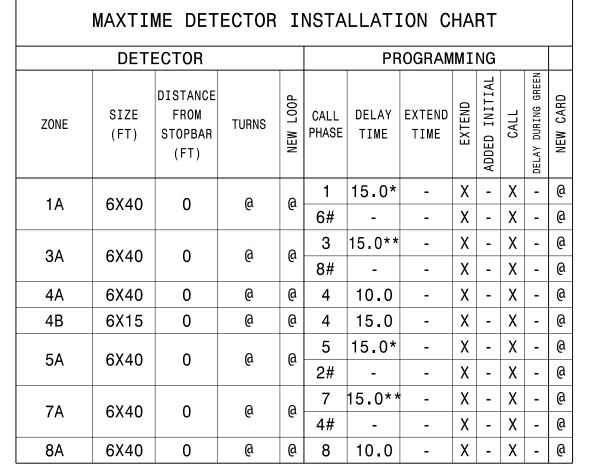
N/A

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SEAL

022516

10/3/2024



- \* Disable Delay During Alternate Phasing Operation. \*\* Reduce Delay to 3 Seconds During Alternate Phasing Operation.
- # Disable Phase Call For Loop During Alternate Phasing Operation.
- @ Multi-zone Microwave Detection.

 $\leftarrow$ 

Metal Pole #2 Case S35L2 -L- STA. 63+53 +/-

74' LT

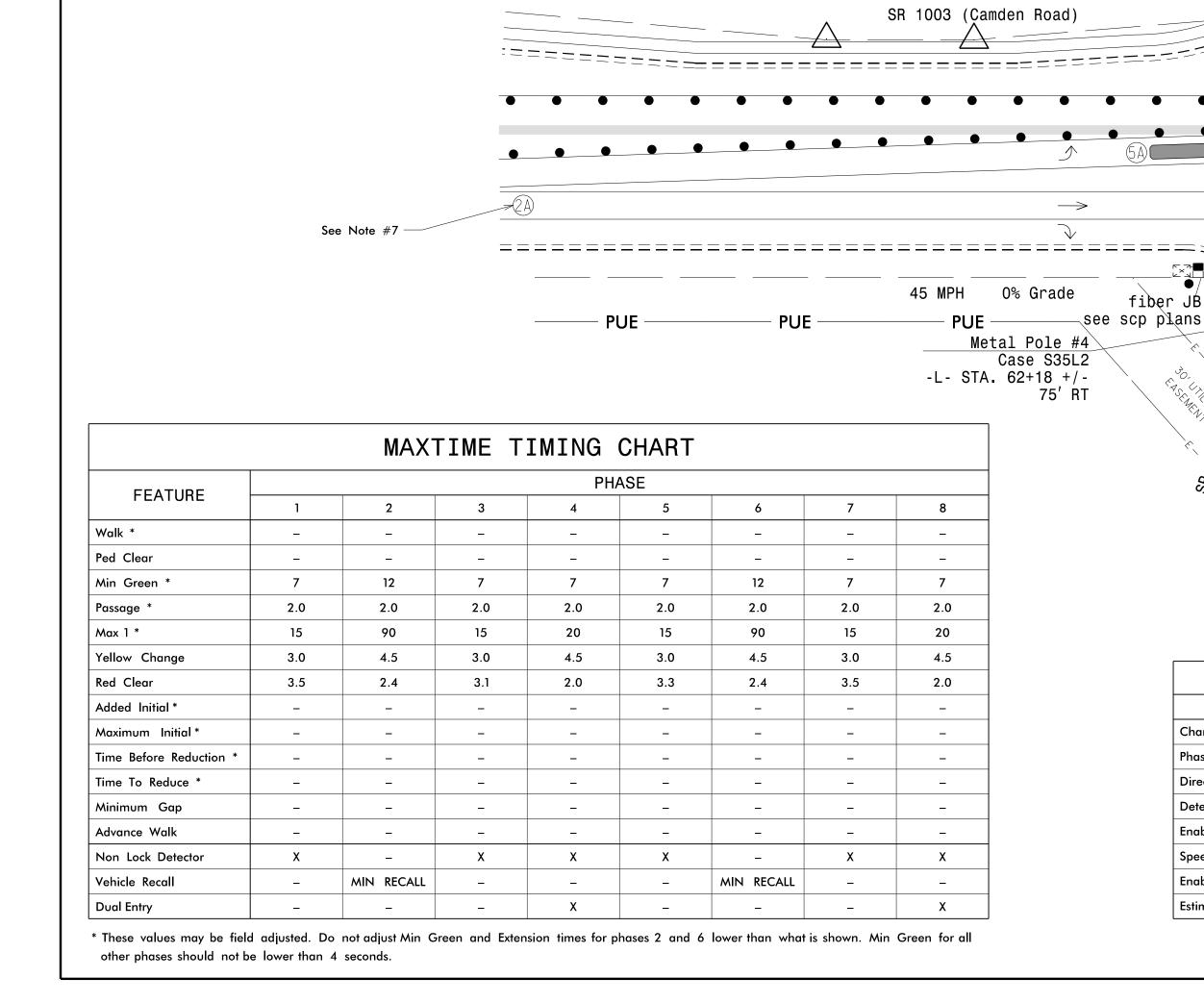
Metal Pole #3

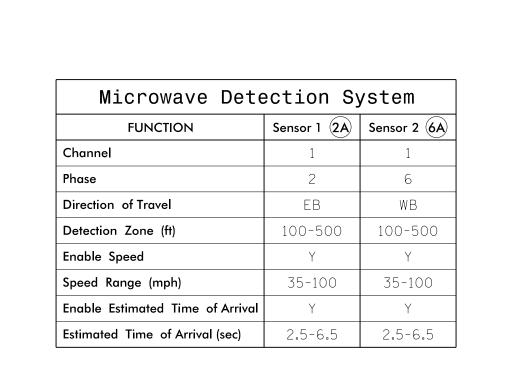
-L- STA. 63+21 +/-

Case S35L2

74' RT

22 **←**♦





SIGNAL FACE I.D.

All Heads L.E.D.

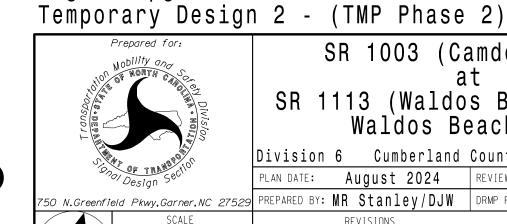
21,22 41,42 61,62 81,82

23 63

Metal Pole #1 Case S35L2

-L- STA. 62+30 +/-

fiber JB



1"=40'

Signal Upgrade - Sheet 1 of 2

0% Grade

SR 1003 (Camden Road)

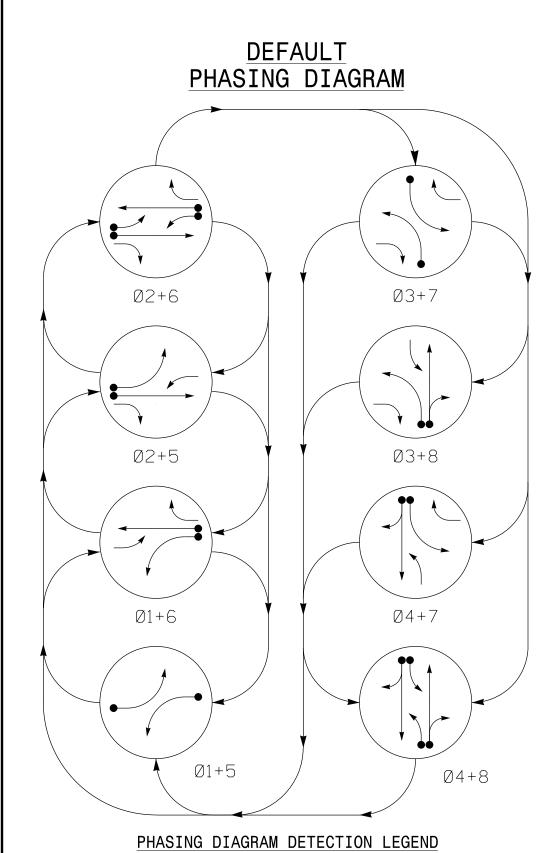
SR 1003 (Camden Road) SR 1113 (Waldos Beach Road)/ Waldos Beach Road

Division 6 Cumberland County Hope Mills PLAN DATE: August 2024 REVIEWED BY: LM Moon D N.Greenfield Pkwy, Garner, NC 27529 PREPARED BY: MR Stanley/DJW DRMP PROJ. NO.: 2400555 REVISIONS INIT. DATE

Lisa Moon SIG. INVENTORY NO. 06-1348T2

**Plans Prepared By:** 

U-3422A Sig-3.1



DETECTED MOVEMENT

 $<\!\!\!<\!\!\!--\!\!\!>$  PEDESTRIAN MOVEMENT

UNSIGNALIZED MOVEMENT

ALTERNATE PHASING DIAGRAM

UNDETECTED MOVEMENT (OVERLAP)

DEFAULT PHASING TABLE OF OPERATION										
				Р	HAS	Ε				
SIGNAL FACE	Ø 1 + 5	Ø 1 + 6	02+5	Ø2+6	Ø 3 + 7	Ø3+8	Ø 4 + 7	Ø 4 + 8	FLASI	
1.1	-	-	<del>-</del> F	<del>F</del> Y	₩	<del></del>	<del></del>	<del></del>	<del></del>	
21,22	R	R	G	G	R	R	R	R	R	
23	R	R	F	F	F	F	R	R	R	
31	<del></del>	<del></del>	<del></del>	<del></del>	-	•	<del>-</del> F	<del>-</del> F <sub>Y</sub>	<del></del>	
41,42	R	R	R	R	R	R	G	G	R	
51	-	<del>-</del> F	-	F	₩	<del></del>	<del></del>	<del></del>	<del></del>	
61,62	R	G	R	G	R	R	R	R	R	
63	R	F	R	F	F	R	F	R	R	
71	₩	<del></del>	<del></del>	<del></del>	-	<del>F</del>	-	<del>-</del> F	<del></del>	
81,82	R	R	R	R	R	G	R	G	R	

ALTERNATE PHASING TABLE OF OPERATION									
				Р	HAS	E			
SIGNAL FACE	Ø 1 + 5	Ø 1 + 6	Ø2+5	Ø2+6	Ø 3 + 7	Ø3+8	Ø 4 + 7	Ø 4 + 8	F L A S H
11	-	•	₩	₩	₩	₩	₩	₩	<b>→</b> R
21,22	R	R	G	G	R	R	R	R	R
23	R	R	F	F	F	F	R	R	R
31	<del></del>	<del></del>	<b>→</b> R	₩	-	-	<b>→</b> R	<b>→</b> R	<b>→</b>
41,42	R	R	R	R	R	R	G	G	R
51	-	<b>-</b> R	-	₩	<b>-</b> ₽	₩	₩	<b>-</b> R	<b>→</b> R
61,62	R	G	R	G	R	R	R	R	R
63	R	F	R	F\$	F	R	F	R	R
71	<del></del>	₩	<del></del>	#	<b>—</b>	#	<u> </u>	<b>→</b> R	<b>→</b> R
81,82	R	R	R	R	R	G	R	G	R

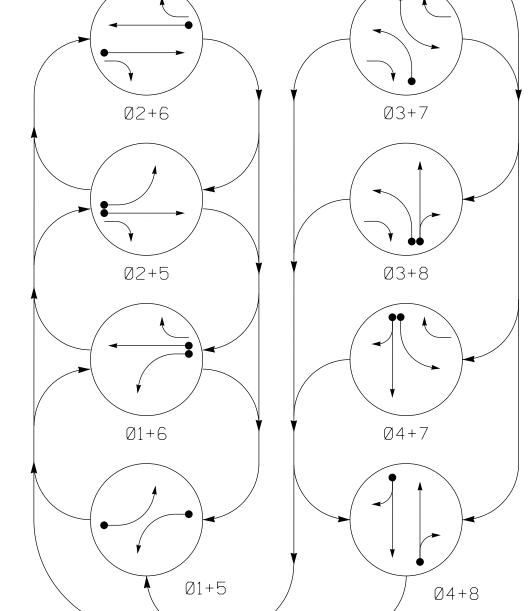
SIGNAL FACE I.D.

All Heads L.E.D.

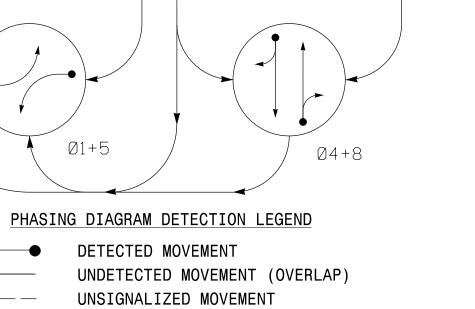
8 Phase Fully Actuated (D06-28\_Hope Mills)

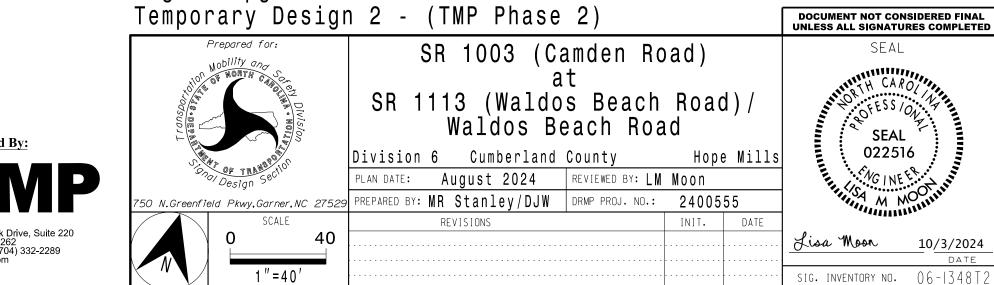
#### NOTES

- Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January 2024.
- 2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- 3. Phase 1 and/or phase 5 may be lagged.
- 4. Phase 3 and/or phase 7 may be lagged.
- 5. Set all detector units to presence mode.
- 6. The Division Traffic Engineer will determine the hours of use for each phasing plan.
- 7. This intersection uses multi-zone microwave detection. Install detectors according to the manufacturer's instructions to achieve desired detection.
- 8. See traffic control plans for stop line locations.
- 9. Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.



 $<\!\!\!<\!\!\!--\!\!\!>$  PEDESTRIAN MOVEMENT





Signal Upgrade - Sheet 2 of 2

Plans Prepared By:

DRMP, Inc.
8210 University Executive Park Drive, Suite 220
Charlotte, NC 28262
NC License No E-1524 (704) 332-2289

## NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- 2. Program phases 4 and 8 for Dual Entry.
- 3. Program controller to start up in phase 2 Green No Walk and Phase 6 Green No Walk.
- 4. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
- 5. The cabinet and controller are part of the D06-28\_Hope Mills Closed Loop Signal System.

#### **EQUIPMENT INFORMATION**

Controller	.2070LX
Cabinet	.332 w/ Aux
Software	.Q-Free MAXTIME
Cabinet Mount	.Base
Output File Positions	.18 With Aux. Output File
Load Switches Used	S1, S2, S4, S5, S7, S8, S10, S11,
	AUX S1, AUX S2, AUX S3, AUX S4,
	AUX S5, AUX S6
Phases Used	1, 2, 3, 4, 5, 6, 7, 8
Overlap "1"	*
Overlap "2"	*
Overlap "3"	*
Overlap "4"	*
Overlap "5"	*
Overlap "6"	* 

Sig-3 2 U-3422A

	SIGNAL HEAD HOOK-UP CHART																	
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	OL5	OL3	OL4	OL6
SIGNAL HEAD NO.	<b>★</b> 11	21,22	NU	<b>★</b> 31	41,42	NU	<b>★</b> 51	61,62	NU	<b>71</b> ★	81,82	NU	11	31 <sup>*</sup>	63 <sup>*</sup>	<b>51</b>	71 <sup>★</sup>	23
RED		128			101			134			107				A111			A104
YELLOW	*	129		*	102		*	135		*	108							
GREEN		130			103			136			109							
RED ARROW													A121	A124		A114	A101	
YELLOW ARROW													A122	A125	A112	A115	A102	A105
FLASHING YELLOW ARROW													A123	A126	A113	A116	A103	A106
GREEN ARROW	127			118			133			124								

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)

OL3 RED (A114)

OL3 YELLOW (A115)

OL3 GREEN (A116)

Ø5 GREEN (133)

OL4 RED (A1Ø1)

OL4 YELLOW (A102)

OL4 GREEN (A1Ø3)

Ø7 GREEN (124)

OL6 RED (A1Ø4)

OL6 YELLOW (A1Ø5)

OL6 GREEN (A1Ø6)

INIT. DATE

\* Denotes install load resistor. See load resistor installation detail this sheet.

OL1 RED (A121) —

OL1 YELLOW (A122) —

OL1 GREEN (A123)

Ø1 GREEN (127) -

OL2 RED (A124)

OL2 YELLOW (A125)

OL2 GREEN (A126)

Ø3 GREEN (118)

OL5 RED (A111)

OL5 YELLOW (A112) —

OL5 GREEN (A113)

Electrical Detail - Sheet 1 of 3

★ See pictorial of head wiring in detail this sheet.

#### INPUT FILE CONNECTION & PROGRAMMING CHART

\*See overlap programming detail on sheet 2.

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
1A	TB2-1,2	I1U	56	18	1	1	15.0		Х		Х	
IA	-	-	-	-	29	6	-		Х		Х	Х
3A	TB4-5,6	I5U	58	20	7	3	15.0		Х		Х	
SA	-	-	-	-	30	8	3.0		Х		Х	Х
5A	TB3-1,2	J1U	55	17	15	5	15.0		Х		Х	
ЭА	-	-	-	-	31	2	-		Х		Х	Х
7.1	TB5-5,6	J5U	57	19	21	7	15.0		Х		Х	
7A	-	-	-	-	32	4	3.0		Х		Х	Х

INPUT	FILE POSITION	LEGEND:	J2L
	FILE J— SLOT 2—		
	LOWER		

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
4.4	TB2-1,2	I1U	56	18	1	1	15.0		Х		Х	
1A	-	-	-	-	29	6	-		Х		Χ	Х
3A	TB4-5,6	I5U	58	20	7	3	15.0		Х		Χ	
JA	-	-	-	-	30	8	3.0		Х		Χ	Х
5A	TB3-1,2	J1U	55	17	15	5	15.0		X		Χ	
JA	-	-	-	-	31	2	-		X		Χ	Х
7A	TB5-5,6	J5U	57	19	21	7	15.0		X		Χ	
//	-	-	-	•	32	4	3.0		X		Χ	Х
	TAIDLIT			ON 1 50	END IOL							

#### LOAD RESISTOR INSTALLATION DETAIL

Note: For Detection Zones 1A, 3A, 5A and 7A the equipment and slots reserved are typical for a NCDOT installation.

(install resistors as shown)

18 CHANNEL IP CONFLICT MONITOR

PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

COMPONENT SIDE

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.

2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.

3. Ensure that the Red Enable is active at all times during normal operation.

INPUT FILE POSITION LAYOUT

(front view)

REMOVE JUMPERS AS SHOWN

4. Integrate monitor with Ethernet network in cabinet.

EX.: 1A, 2A, ETC. = LOOP NO.'S

NOT USED

NOT

FILE

FILE

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 1-17, 2-5, 2-6, 2-9, 2-11, 2-17, 2-18, 3-7, 3-8, 3-10, 3-12, 3-17, 3-18, 4-7, 4-8, 4-10, 4-12, 4-17, 5-9, 5-11, 5-18, 6-9, 6-11, 6-17, 6-18, 7-10, 7-12, 7-17, 7-18, 8-10, 8-12, 8-18, 9-11, 9-17, 9-18, 10-12, 10-17, 10-18, 11-17, 11-18, 12-17, 12-18 and 17-18.

ON OFF

- RF 2010 RP DISABLE — WD 1.0 SEC

 LEDguard - RF SSM

- FYA 3-10 FYA 5-11

FYA 7-12

= DENOTES POSITION OF SWITCH

DC ISOLATOR

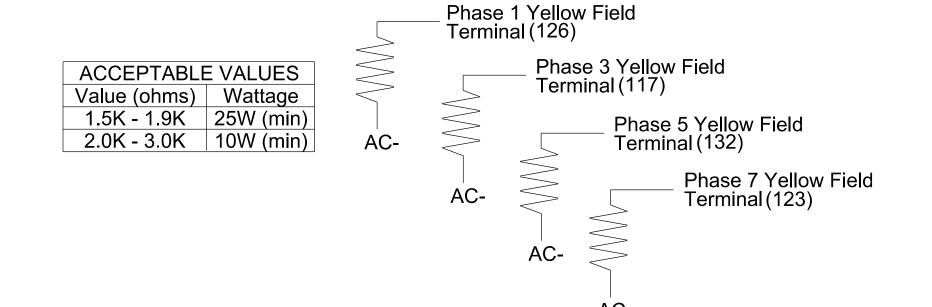
FS = FLASH SENSE ST = STOP TIME

- GY ENABLE

SF#1 POLARITY

FYA COMPACT—

WD ENABLE '



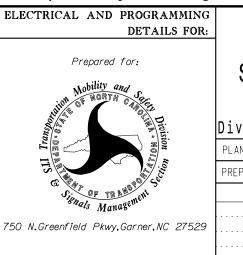
#### SPECIAL DETECTOR NOTE

Install a multizone microwave detection zone for approaches for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1348T2 DESIGNED: Aug 2024 SEALED:

REVISED: N/A





## Temporary Design 2 - (TMP Phase 2) SR 1003 (Camden Road)

SR 1113 (Waldos Beach Road)/ Waldos Beach Road

63

REVISIONS

Division 6 Cumberland County Hope Mills August 2024 REVIEWED BY: LM Moon PREPARED BY: MR Stanley/DJW DRMP PROJ. NO: 2400555

SEAL 022516

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

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(R)

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23

10/3/2024 SIG. INVENTORY NO. 06-1348T2

Sig-3.3

## U-3422A

#### OUTPUT CHANNEL CONFIGURATION

Front Panel

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channel Configuration

Channel Configuration

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Phase Vehicle	1		Х	X	1
2	Phase Vehicle	2		Х		2
3	Phase Vehicle	3		Х	Х	3
4	Phase Vehicle	4		Х		4
5	Phase Vehicle	5		Х		5
6	Phase Vehicle	6		Х	Х	6
7	Phase Vehicle	7		Х		7
8	Phase Vehicle	8		Х	Х	8
9	Overlap	1		Х	Χ	9
10	Overlap	2		Х	Х	10
11	Overlap	3		Х		11
12	Overlap	4		Х		12
13	Phase Ped	2				13
14	Phase Ped	4				14
15	Phase Ped	6				15
16	Phase Ped	8				16
17	Overlap	5		Χ	Χ	17
18	Overlap	6		Х		18



## MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 1A, 3A, 5A & 7A

Front Panel

Main Menu >Controller >Detector >Veh Det Plans

Web Interface

Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

	a =		
	Detector	Call Phase	Delay
1A	1	1	0.0
	29	0	-

	Detector	Call Phase	Delay
3A	7	3	3.0
	30	0	3.0

	Detector	Call Phase	Delay
A	15	5	0.0
	31	0	-

	Detector	Call Phase	Delay
A	21	7	3.0
	32	0	3.0

#### MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

#### Overlap Plan 1

Overlap	1	2	3	4	5	6
Type	FYA 4 - Section	FYA 4-Section	FYA 4 - Section			
Included Phases	2	4	6	8	6,7	2,3
Modifier Phases	1	3	5	7	-	-
Modifier Overlap	-	-	-	-	-	-
Trail Green	0	0	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0	0.0	0.0

#### MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

#### Overlap Plan 2

Overlap	1	2	3	4	5	6
Type	FYA 4 - Section	FYA 4-Section	FYA 4 - Section			
Included Phases	<u></u>	÷	-	<u>-</u>	6,7	2,3
Modifier Phases	1	3	5	7	1	-
Modifier Overlap	ii ii	-	-	=	ı.	-
Trail Green	0	0	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0	0.0	0.0

NOTICE INCLUDED PHASE

**Plans Prepared By:** 

#### MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Unit

Web Interface

Home >Controller >Unit

Modify parameters as shown below and save changes.

Start Up Parameters

StartUp Clearance Hold

**Unit Flash Parameters** 

All Red Flash Exit Time

#### MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel

Main Menu > Controller > Coordination > Patterns

Web Interface

Home >Controller >Coordination >Patterns

Pattern Parameters

i attorri i ara	11101010	
Pattern	Veh Det Plan	Overlap Plan
*	2	2

\*The Pattern number(s) are to be determined by the Division Traffic Engineer.

#### FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2. 2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3. 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1348T2 DESIGNED: Aug 2024 SEALED: REVISED: N/A

Electrical Detail - Sheet 2 of 3 Temporary Design 2 - (TMP Phase 2)

ELECTRICAL AND PROGRAMMING

SR 1003 (Camden Road)

SR 1113 (Waldos Beach Road)/ Waldos Beach Road

Division 6 Cumberland County Hope Mills

REVISIONS INIT. DATE

Lisa Moon 10/3/2024 SIG. INVENTORY NO. 06-1348T2

022516

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

PLAN DATE: August 2024 REVIEWED BY: LM Moon PREPARED BY: MR Stanley/DJW DRMP PROJ. NO: 2400555

Sig-3.4
SHEET NO.

### MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

#### ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases

for heads 11, 31, 51, and 71 to run protected turns only.

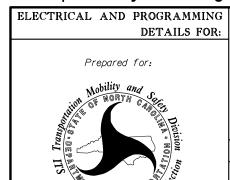
VEH DET PLAN 2: Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 0 seconds.

> Disables phase 8 call on loop 3A and reduces delay time for phase 3 call on loop 3A to 3 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

Disables phase 4 call on loop 7A and reduces delay time for phase 7 call on loop 7A to 3 seconds.

> Electrical Detail - Sheet 3 of 3 Temporary Design 2 - (TMP Phase 2)



SR 1003 (Camden Road) SR 1113 (Waldos Beach Road)/ Waldos Beach Road

Division 6 Cumberland County Hope Mills PLAN DATE: August 2024 REVIEWED BY: LM Moon

PREPARED BY: MR Stanley/DJW DRMP PROJ. NO: 2400555 REVISIONS INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED 022516

SIG. INVENTORY NO. 06-134872

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1348T2 DESIGNED: Aug 2024 SEALED:

REVISED: N/A





Sig-4.0 U-3422A

8 Phase Fully Actuated (D06-28\_Hope Mills)

#### NOTES

- 1. Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January
- 2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- 3. Phase 1 and/or phase 5 may be lagged.
- 4. Phase 3 and/or phase 7 may be lagged.
- 5. Reposition existing signal heads numbered 21, 22, 61 and 62.
- 6. Set all detector units to presence mode.
- 7. The Division Traffic Engineer will determine the hours of use for each phasing plan
- 8. This intersection uses multi-zone microwave detection. Maintain detectors according to the manufacturer's instructions to achieve desired detection.
- 9. See pavement marking plan for stop line locations.
- 10. Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.

LEGEND

Traffic Signal Head

Modified Signal Head

Sign

Pedestrian Signal Head With Push Button & Sign

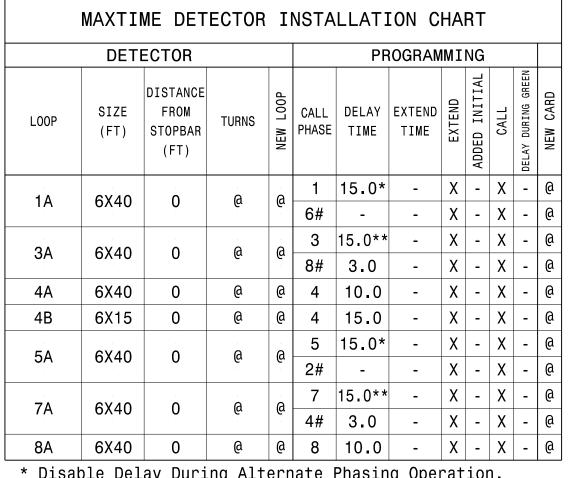
Signal Pole with Guy Signal Pole with Sidewalk Guy

Metal Strain Pole

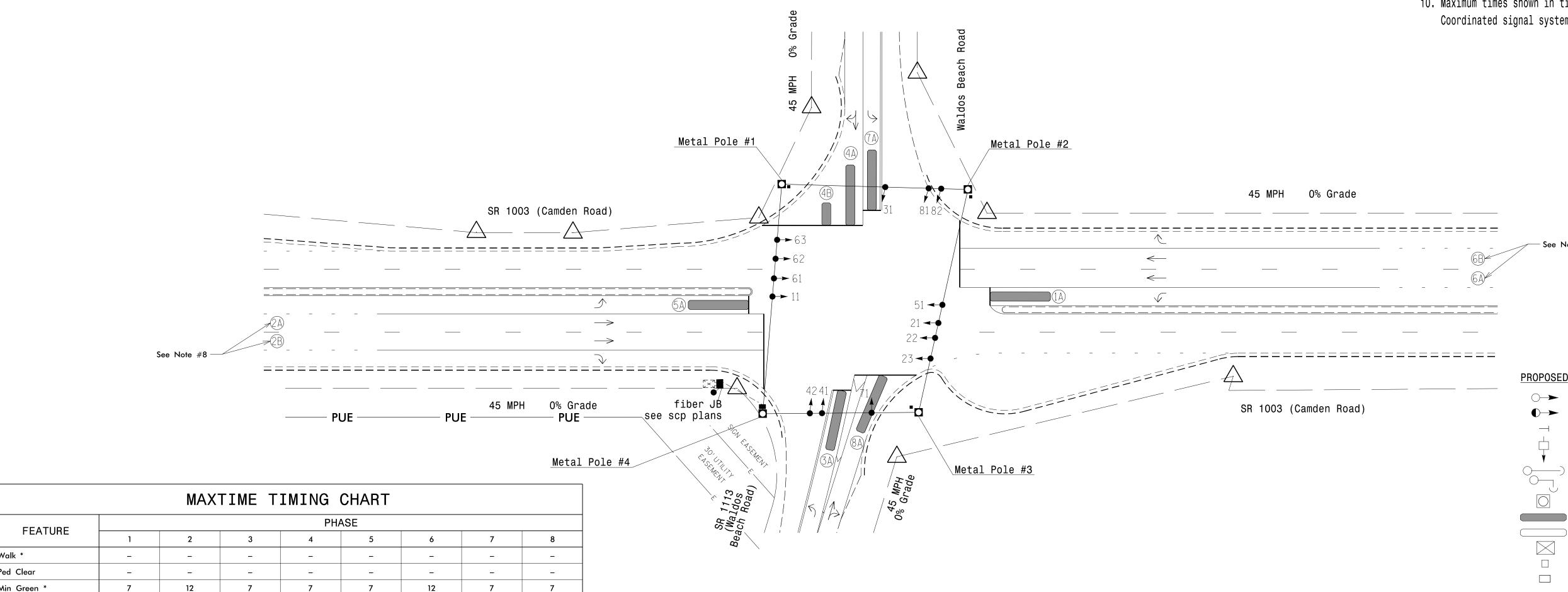
Microwave Detection Zone Inductive Loop Detector Controller & Cabinet Junction Box

Oversized Junction Box 2-in Underground Conduit

> Right of Way Directional Arrow



- \* Disable Delay During Alternate Phasing Operation. \*\* Reduce Delay to 3 Seconds during Alternate Phasing Operation.
- # Disable Phase Call For Loop during Alternate Phasing Operation.
- @ Multi-zone Microwave Detection.



SIGNAL FACE I.D.

All Heads L.E.D.

21,22 41,42 61,62 81,82

R 12"

23 63

		MAX.	TIME T	IMING	CHART						
FEATURE -	PHASE										
FEATURE	1	2	3	4	5	6	7	8			
Walk *	_	-	_	_	_	_	_	_			
Ped Clear	_	-	_	_	_	_	_	_			
Min Green *	7	12	7	7	7	12	7	7			
Passage *	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0			
Max 1 *	15	90	15	20	15	90	15	20			
Yellow Change	3.0	4.5	3.0	4.5	3.0	4.5	3.0	4.5			
Red Clear	3.7	2.6	2.6	2.1	3.3	2.6	3.6	2.1			
Added Initial *	_	-	_	_	_	_	_	_			
Maximum Initial *	_	_	_	_	_	_	_	_			
Time Before Reduction *	_	_	_	_	_	_	_	_			
Time To Reduce *	_	_	_	_	_	_	_	_			
Minimum Gap	_	_	_	_	_	_	_	_			
Advance Walk	_	_	_	_	_	_	_	_			
Non Lock Detector	Х	-	Х	Х	Х	-	Х	Х			
Vehicle Recall	_	MIN RECALL	_	_	_	MIN RECALL	_	_			
Dual Entry	_	_	_	Х	_	_	_	Х			

\* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

Microwave De	etection Sy	/stem		
FUNCTION	Sensor 1 (2A)(2B)	Sensor 2 6A 6B		
Channel	1	1		
Phase	2	6		
Direction of Travel	EB	WB		
Detection Zone (ft)	100-500	100-500		
Enable Speed	Y	Y		
Speed Range (mph)	35-100	35-100		
Enable Estimated Time of Arrival	Y	Y		
Estimated Time of Arrival (sec)	2.5-6.5	2.5-6.5		

Signal Upgrade - Sheet 1 of 2 Final Design



Plans Prepared By:

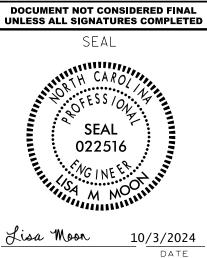
SR 1003 (Camden Road) SR 1113 (Waldos Beach Road)/ Waldos Beach Road

N/A

 $\bigcirc$ 

Division 6 Cumberland County Hope Mills PLAN DATE: August 2024 REVIEWED BY: LM Moon N.Greenfield Pkwy, Garner, NC 27529 PREPARED BY: MR Stanley/DJW DRMP PROJ. NO.: 2400555

REVISIONS INIT. DATE



06-1348

**EXISTING** 

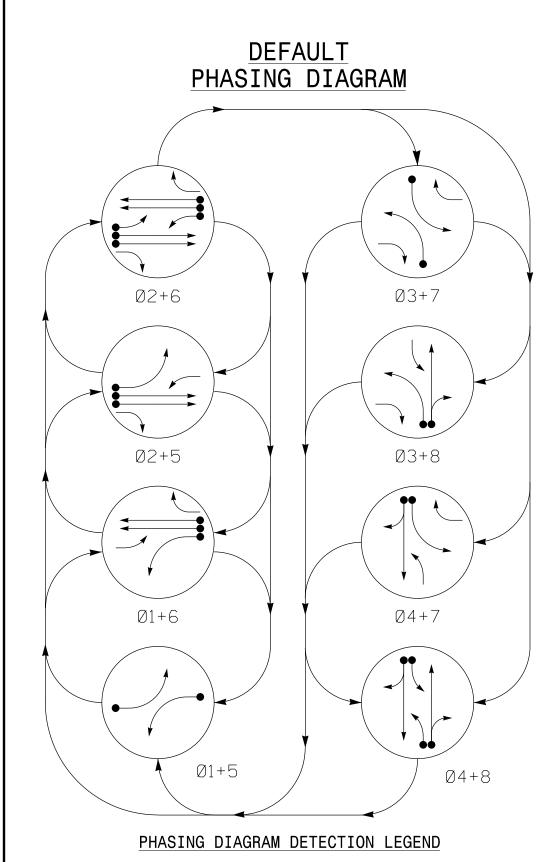
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N/A

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N/A

U-3422A Sig-4.1



DETECTED MOVEMENT

 $<\!\!\!<\!\!\!--\!\!\!>$  PEDESTRIAN MOVEMENT

UNSIGNALIZED MOVEMENT

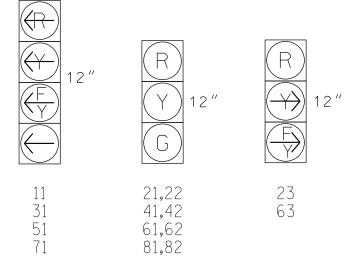
UNDETECTED MOVEMENT (OVERLAP)

DEFAULT PHASING TABLE OF OPERATION										
				Р	HAS	E				
SIGNAL FACE	Ø 1 + 5	Ø 1 + 6	Ø 2 + 5	Ø2+6	Ø 3 + 7	Ø3+8	Ø 4 + 7	Ø 4 + 8	FLASH	
11	¥	•	<del>▼</del> Y	F	₩	₩	₩	₩	<del>∢</del> R	
21,22	R	R	G	G	R	R	R	R	R	
23	R	R	F	F	F	<u>F</u>	R	R	R	
31	*	<del></del>	<del></del>	<del></del>	-	<b>\</b>	<b>-</b> F	<del>▼</del>	<del>-R</del>	
41,42	R	R	R	R	R	R	G	G	R	
51	-	<del>-</del> F	-	<del>-</del> F	<del></del>	<del>▼</del>	<del></del>	<del></del>	<del></del>	
61,62	R	G	R	G	R	R	R	R	R	
63	R	F	R	F	F	R	F	R	R	
71	₩	<del></del>	<del></del>	<del></del>	-	F	-	<del>F</del>	<del></del>	
81,82	R	R	R	R	R	G	R	G	R	

ALTERNATE PHASING TABLE OF OPERATION										
				Р	HAS	E				
SIGNAL FACE	Ø 1 + 5	Ø 1 + 6	Ø 2 + 5	Ø 2 + 6	Ø 3 + 7	Ø 3 + 8	Ø 4 + 7	Ø 4 + 8	FLASH	
11	-	-	<b>→</b> R	<del>√</del> R	<b>→</b> R	<b>→</b> R	<b>-</b> R	<b>→</b> R	<b>→</b> R	
21,22	R	R	G	G	R	R	R	R	R	
23	R	R	F	F	F	F	R	R	R	
31	<b>→</b> R	<del>-R</del>	<b>-</b> R	<b>→</b> R	-	-	<del>-R</del>	<b>→</b> R	<b>→</b> R	
41,42	R	R	R	R	R	R	G	G	R	
51	-	<b>→</b> R	-	<b>-</b> R	<b>→</b> R	<b>-</b> R	<b>-</b> R	<b>-</b> R	<b>-</b> R	
61,62	R	G	R	G	R	R	R	R	R	
63	R	F	R	F	F	R	F	R	R	
71	<b>-</b> R	<b>-</b> R	<b>-</b> R	<b>-</b> ₽	-	<b>-</b> R	-	<b>-</b> R	<b>→</b> R	
81,82	R	R	R	R	R	G	R	G	R	

#### SIGNAL FACE I.D.

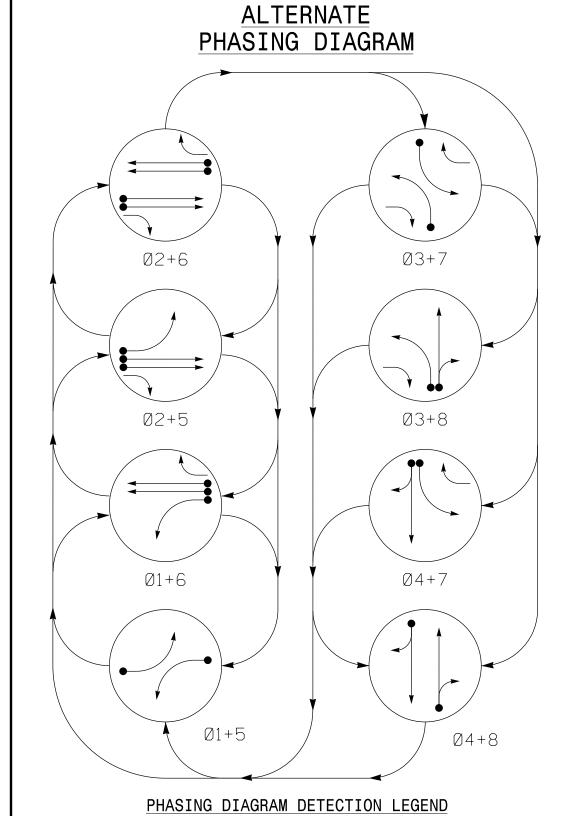
All Heads L.E.D.



8 Phase Fully Actuated (D06-28\_Hope Mills)

#### **NOTES**

- 1. Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January 2024.
- 2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- 3. Phase 1 and/or phase 5 may be lagged.
- 4. Phase 3 and/or phase 7 may be lagged.
- 5. Reposition existing signal heads numbered 21, 22, 61 and 62.
- 6. Set all detector units to presence mode.
- 7. The Division Traffic Engineer will determine the hours of use for each phasing plan
- 8. This intersection uses multi-zone microwave detection. Maintain detectors according to the manufacturer's instructions to achieve desired detection.
- 9. See pavement marking plan for stop line locations.
- 10. Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.

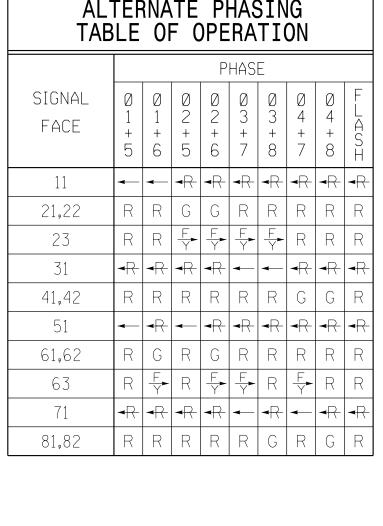


DETECTED MOVEMENT

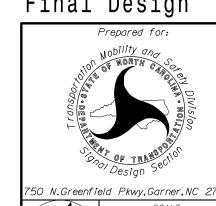
 $<\!\!\!<\!\!\!--\!\!\!>$  PEDESTRIAN MOVEMENT

UNSIGNALIZED MOVEMENT

UNDETECTED MOVEMENT (OVERLAP)



Signal Upgrade - Sheet 2 of 2 Final Design



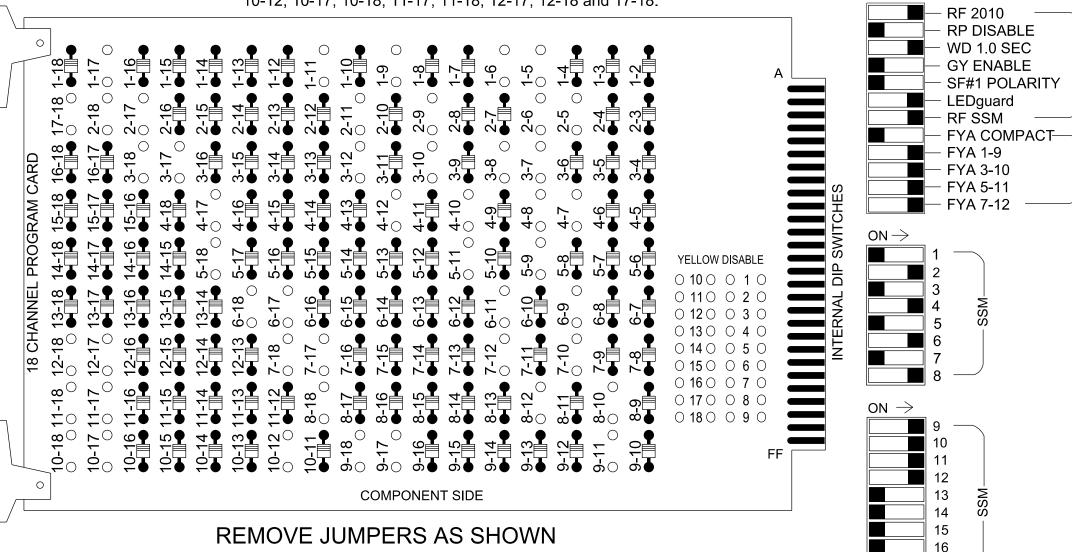
SR 1003 (Camden Road) `at SR 1113 (Waldos Beach Road)/ Waldos Beach Road

Division 6 Cumberland County Hope Mills PLAN DATE: August 2024 REVIEWED BY: LM Moon FOO N.Greenfield Pkwy.Garner.NC 27529 PREPARED BY: MR Stanley/DJW DRMP PROJ. NO.: 2400555 REVISIONS INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

**Plans Prepared By:** 

#### 18 CHANNEL IP CONFLICT MONITOR ON OFF PROGRAMMING DETAIL WD ENABLE ' (remove jumpers and set switches as shown) REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 1-17, 2-5, 2-6, 2-9, 2-11, 2-17, 2-18, 3-7, 3-8, 3-10, 3-12, 3-17, 3-18, 4-7, 4-8, 4-10, 4-12, 4-17, 5-9, 5-11, 5-18, 6-9, 6-11, 6-17, 6-18, 7-10, 7-12, 7-17, 7-18, 8-10, 8-12, 8-18, 9-11, 9-17, 9-18, 10-12, 10-17, 10-18, 11-17, 11-18, 12-17, 12-18 and 17-18.



#### NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- 2. Program phases 4 and 8 for Dual Entry.
- 3. Program controller to start up in phase 2 Green No Walk and Phase 6 Green No Walk.
- 4. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
- 5. The cabinet and controller are part of the D06-28\_Hope Mills Closed Loop Signal System.

#### **EQUIPMENT INFORMATION**

Controller	2070LX
Cabinet	
Software	Q-Free MAXTIME
Cabinet Mount	Base
Output File Positions	18 With Aux. Output File
Load Switches Used	S1, S2, S4, S5, S7, S8, S10, S11,
	AUX S1, AUX S2, AUX S3, AUX S4,
	AUX S5, AUX S6
Phases Used	1, 2, 3, 4, 5, 6, 7, 8
Overlap "1"	*
Overlap "2"	*
Overlap "3"	*
Overlap "4"	*
Overlap "5"	
Overlap "6"	*
*See overlap programming detail	on sheet 2.

Sig-4.2 U-3422A

				SI	<b>GNA</b>	\L F	ΙĒΑ	DΗ	00	K-U	IP C	HA	RT					
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	OL5		OL4	
SIGNAL HEAD NO.	11	21,22	NU	<b>★</b> 31	41,42	NU	<b>★</b> 51	61,62	NU	<b>71</b>	81,82	NU	11	<b>★</b> 31	63 <sup>*</sup>	<b>★</b> 51	<b>71</b> ★	23
RED		128			101			134			107				A111			A104
YELLOW	*	129		*	102		*	135		*	108							
GREEN		130			103			136			109							
RED ARROW													A121	A124		A114	A101	
YELLOW ARROW													A122	A125	A112	A115	A102	A105
FLASHING YELLOW ARROW													A123	A126	A113	A116	A103	A106
GREEN ARROW	127			118			133			124								

\* Denotes install load resistor. See load resistor installation detail this sheet.

OL1 RED (A121) —

OL1 YELLOW (A122) —

OL1 GREEN (A123)

Ø1 GREEN (127) —

OL2 RED (A124)

★ See pictorial of head wiring in detail this sheet.

#### INPUT FILE POSITION LAYOUT

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.

2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.

3. Ensure that the Red Enable is active at all times during normal operation.

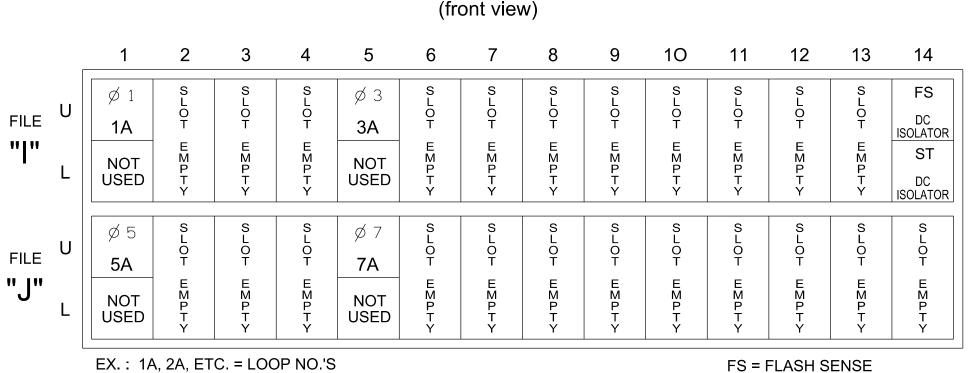
4. Integrate monitor with Ethernet network in cabinet.

ACCEPTABLE VALUES

Value (ohms) Wattage

1.5K - 1.9K 25W (min)

2.0K - 3.0K | 10W (min)



ST = STOP TIME
Note: For Detection Zones 1A, 3A, 5A and 7A the equipment and slots reserved are typical for a NCDOT installation.

LOAD RESISTOR INSTALLATION DETAIL

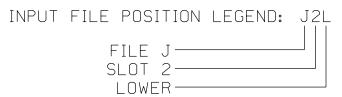
(install resistors as shown)

AC-

Phase 1 Yellow Field Terminal (126)

#### INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
1.0	TB2-1,2	I1U	56	18	1	1	15.0		Х		Х	
1A	-	-	-	-	29	6	-		Х		Х	Х
2.4	TB4-5,6	I5U	58	20	7	3	15.0		Х		Х	
3A	-	-	-	-	30	8	3.0		Х		Х	Х
ΕΛ	TB3-1,2	J1U	55	17	15	5	15.0		Х		Х	
5A	-	-	-	<u>-</u>	31	2	-		Х		Х	Х
7.0	TB5-5,6	J5U	57	19	21	7	15.0		Х		Х	
7A	_	_	-	-	32	4	3.0		Х		Χ	Х



#### SPECIAL DETECTOR NOTE

Install a multizone microwave detection zone for approaches for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

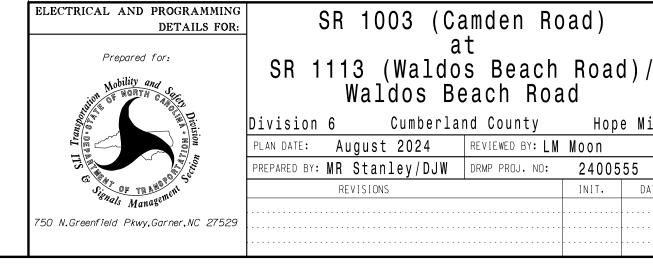
## Phase 3 Yellow Field Terminal (117) Phase 5 Yellow Field Terminal (132) Phase 7 Yellow Field Terminal (123)

= DENOTES POSITION OF SWITCH

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1348 DESIGNED: Aug 2024 SEALED:

REVISED: N/A

**Plans Prepared By:** 



#### OL2 YELLOW (A125) OL4 YELLOW (A1Ø2) (F) OL2 GREEN (A126) OL4 GREEN (A1Ø3) Ø7 GREEN (124) Ø3 GREEN (118) 31 71 OL5 RED (A111) OL6 RED (A1Ø4) OL5 YELLOW (A112) OL6 YELLOW (A1Ø5) F OL5 GREEN (A113) OL6 GREEN (A1Ø6) Electrical Detail - Sheet 1 of 3 Final Design

SR 1003 (Camden Road)

Waldos Beach Road

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)

OL3 RED (A114)

OL3 YELLOW (A115)

OL3 GREEN (A116)

Ø5 GREEN (133)

OL4 RED (A1Ø1)

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SEAL 022516 Hope Mills

51

REVISIONS INIT. DATE 10/3/2024 SIG. INVENTORY NO. 06-1348

#### MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

#### Overlap Plan 1

Overlap	1	2	3	4	5	6
Type	FYA 4 - Section	FYA 4-Section	FYA 4 - Section			
Included Phases	2	4	6	8	6,7	2,3
Modifier Phases	1	3	5	7	÷	<u>-</u>
Modifier Overlap	-	-	-	<u>.</u>	<u>-</u>	-
Trail Green	0	0	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0	0.0	0.0

#### MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

#### Overlap Plan 2

-							
Overlap	1	2	3	4	5	6	
Туре	FYA 4 - Section	FYA 4-Section	FYA 4 - Section	NOTICE			
Included Phases	-	-	-	-	6,7	2,3	INCLUDED
Modifier Phases	1	3	5	7	-		PHASE
Modifier Overlap	=	-	-	=	=	ā	
Trail Green	0	0	0	0	0	0	
Trail Yellow	0.0	0.0	0.0	0.0	0.0	0.0	
Trail Red	0.0	0.0	0.0	0.0	0.0	0.0	

#### MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Unit

Web Interface

Home >Controller >Unit

Modify parameters as shown below and save changes.

Start Up Parameters

StartUp Clearance Hold

**Unit Flash Parameters** All Red Flash Exit Time

#### **OUTPUT CHANNEL CONFIGURATION**

Front Panel

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channel Configuration

#### Channel Configuration

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Phase Vehicle	1		Х	Х	1
2	Phase Vehicle	2		Х		2
3	Phase Vehicle	3		Х	Х	3
4	Phase Vehicle	4		Х		4
5	Phase Vehicle	5		Х		5
6	Phase Vehicle	6		Х	Х	6
7	Phase Vehicle	7		Х		7
8	Phase Vehicle	8		Х	Х	8
9	Overlap	1		Х	Х	9
10	Overlap	2		Х	Х	10
11	Overlap	3		Х		11
12	Overlap	4		Х		12
13	Phase Ped	2				13
14	Phase Ped	4				14
15	Phase Ped	6				15
16	Phase Ped	8				16
17	Overlap	5		Х	Х	17
18	Overlap	6		Х		18



#### MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 1A, 3A, 5A & 7A

Front Panel

Main Menu >Controller >Detector >Veh Det Plans

Web Interface

Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

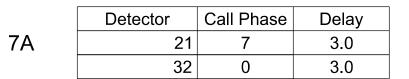
Plan 2

1A

a <u>-</u>		
Detector	Call Phase	Delay
1	1	0.0
29	0	<u>-</u>

	Detector	Call Phase	Delay
3A	7	3	3.0
	30	0	3.0

	Detector	Call Phase	Delay
5A	15	5	0.0
	31	0	=







#### MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Coordination >Patterns

Web Interface

Home >Controller >Coordination >Patterns

Pattern Parameters

Veh Det Plan | Overlap Plan

\*The Pattern number(s) are to be determined by the Division Traffic Engineer.

#### FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1348 DESIGNED: Aug 2024

SEALED: REVISED: N/A

Electrical Detail - Sheet 2 of 3 Final Design

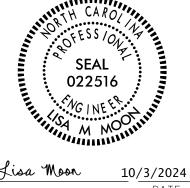
ELECTRICAL AND PROGRAMMING Prepared for:

## SR 1003 (Camden Road)

SR 1113 (Waldos Beach Road)/ Waldos Beach Road

Division 6 Cumberland County Hope Mills

PLAN DATE: August 2024 REVIEWED BY: LM Moon PREPARED BY: MR Stanley/DJW DRMP PROJ. NO: 2400555 REVISIONS INIT. DATE



DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SIG. INVENTORY NO. 06-1348

PROJECT REFERENCE NO.	SHEET NO.
U - 3422A	Sig-4.4

#### MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2. A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

OVERLAP PLAN	VEH DET PLAN
1	1
2	2
	OVERLAP PLAN  1 2

#### ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases

for heads 11, 31, 51, and 71 to run protected turns only.

VEH DET PLAN 2: Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 0 seconds.

> Disables phase 8 call on loop 3A and reduces delay time for phase 3 call on loop 3A to 3 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

Disables phase 4 call on loop 7A and reduces delay time for phase 7 call on loop 7A to 3 seconds.

Electrical Detail - Sheet 3 of 3

Final Design ELECTRICAL AND PROGRAMMING

Prepared for:

SR 1003 (Camden Road)

SR 1113 (Waldos Beach Road)/ Waldos Beach Road

Division 6 Cumberland County Hope Mills PLAN DATE: August 2024 REVIEWED BY: LM Moon PREPARED BY: MR Stanley/DJW DRMP PROJ. NO: 2400555

REVISIONS

022516 INIT. DATE

10/3/2024 SIG. INVENTORY NO. 06-1348

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1348 DESIGNED: Aug 2024 SEALED:

REVISED: N/A

**Plans Prepared By:** 

Sig-5.0 U-3422A

6 Phase Fully Actuated (D06-28\_Hope Mills)

#### NOTES

- 1. Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January 2024.
- 2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- 3. Phase 1 and/or phase 5 may be lagged.
- 4. Phase 7 may be lagged.
- 5. Set all detector units to presence mode.
- 6. In the event of loop replacement, refer to the current ITS and Signals Design Manual and submit a Plan of Record to the Signal Design Section.
- 7. Pavement markings are existing.
- 8. The Division Traffic Engineer will determine the hours of use for each phasing plan.
- 9. Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.
- 10. Install 2070LX controller with MAXTIME software in existing signal cabinet.

MAXTIME DETECTOR INSTALLATION CHART DETECTOR PROGRAMMING CALL DELAY EXTEND HASE TIME TIME CALL DISTANCE SIZE FROM STOPBAR 6 3.0 15.0 0 2-4-2 300 EXIST 2 | - | - | X | X | X | -4 10.0 0 2-4-2 5 | 15.0 | - | X | - | X | -2 3.0 300 EXIST 6 - - XXXX-15.0 - X - X -+5 2-4-2 4 3.0 - | X | - | X | X | +5 | 2-4-2 | - | 8 | 3.0 | - | X | - | X | -0 | 2-4-2 | - | 8 | - | - | X | - | X | - |

04+8 PHASING DIAGRAM DETECTION LEGEND DETECTED MOVEMENT UNDETECTED MOVEMENT (OVERLAP) UNSIGNALIZED MOVEMENT  $<\!\!\!<\!\!\!--\!\!\!>$  PEDESTRIAN MOVEMENT

41,42

61,62

TABLE OF OPERATION

PHASE

┃<del></del>╾┃<del>╸</del>┃<del>╸</del>┡

PHASING DIAGRAM

02+5

Ø1+6

Ø4+7

SR 1112 (Stoney Point Rd)

Posted 35 MPH 0% Grade

(Design Speed 45 MPH)

MAXTIME TIMING CHART PHASE **FEATURE** 1 7 \_ Ped Clear 12 7 Min Green \* 7 7 2.0 2.0 2.0 6.0 2.0 2.0 6.0 Passage 15 20 90 20 20 Yellow Change 3.0 4.5 3.8 3.0 4.5 3.0 3.8 Red Clear 2.4 1.3 2.1 1.3 2.4 1.6 Added Initial \* 2.5 2.5 \_ 34 Maximum Initial \* Time Before Reduction 15 15 Time To Reduce \* 45 45 Minimum Gap 3.0 3.0 Advance Walk Non Lock Detector Χ Χ \_ MIN RECALL Vehicle Recall MIN RECALL

\* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown Min Green for all other phases should not be lower than 4 seconds

Dual Entry

SR 1112 (Rockfish Rd)

SIGNAL FACE I.D.

ALL HEADS L.E.D.

21,22 41,42 61,62 82,83

LEGEND **PROPOSED EXISTING** Traffic Signal Head  $\bigcirc$ **●**→ Modified Signal Head N/A Sign  $\dashv$ Pedestrian Signal Head With Push Button & Sign Signal Pole with Guy Signal Pole with Sidewalk Guy Inductive Loop Detector Controller & Cabinet Junction Box 2-in Underground Conduit N/A Right of Way Directional Arrow N/A Wheelchair Ramp "YIELD" Sign (R1-2) "DO NOT BLOCK INTERSECTION"

Sign (R10-7)

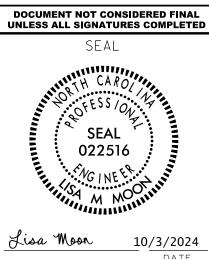
Signal Upgrade

1"=40'

SR 1112 (Stoney Point Rd/ Rockfish Rd)

SR 1108 (Lakewood Dr/King Rd)

Division 6 Cumberland County PLAN DATE: July 2024 PREPARED BY: MR Stanley/DJW DRMP PROJ. NO.: 2400555 INIT. DATE



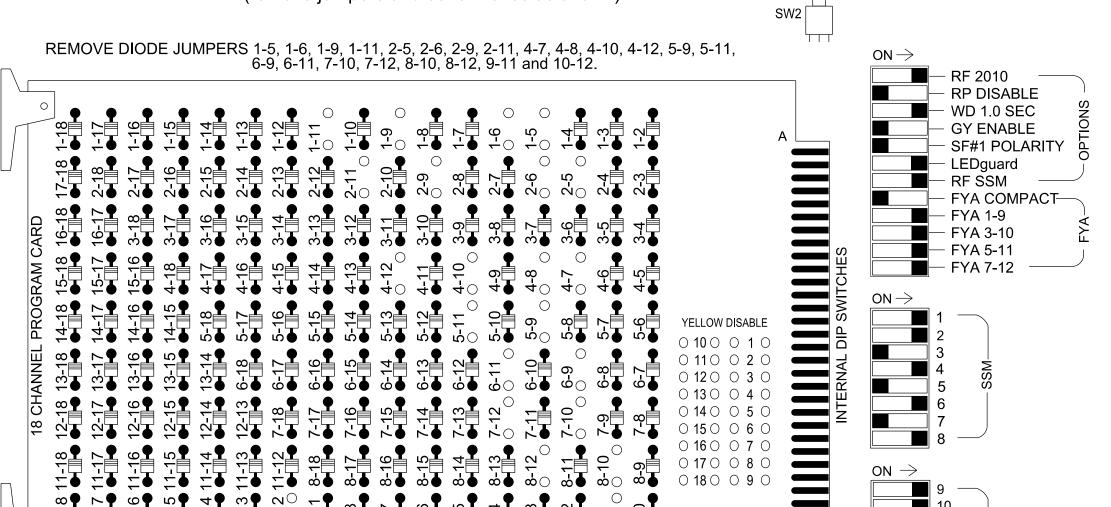
SIG. INVENTORY NO.

06-0707

Plans Prepared By: DRMP, Inc, 8210 University Executive Park Drive, Suite 220 Charlotte, NC 28262 NC License No. F-1524 (704) 332-2289 www.DRMP.com

#### 18 CHANNEL IP CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



ON OFF

16

= DENOTES POSITION OF SWITCH

DC ISOLATOR

WD ENABLE (

#### REMOVE JUMPERS AS SHOWN

FILE

FILE

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.

COMPONENT SIDE

- 2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- 3. Ensure that the Red Enable is active at all times during normal operation.

USED

NOT

USED

INPUT FILE POSITION LAYOUT

(front view)

 $\phi_1$   $\phi_2$   $\phi_1$  s s  $\phi_4$  s s s s s

4. Integrate monitor with Ethernet network in cabinet.

#### NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- 2. Program phases 4 and 8 for Dual Entry.
- 3. Program controller to start up in phase 2 Green No Walk and phase 6 Green No Walk.
- 4. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
- 5. The cabinet and controller are part of the D06-28 Hope Mills Closed Loop Signal System.

#### **EQUIPMENT INFORMATION**

Controller	2070LX
Cabinet	332 w/ Aux
Software	Q-Free MAXTIME
Cabinet Mount	Base
Output File Positions	18 With Aux. Output File
Load Switches Used	S1, S2, S5, S7, S8, S10, S11,
	AUX S1, AUX S2, AUX S4, AUX S5
Phases Used	1, 2, 4, 5, 6, 7, 8
Overlap "1"	*
Overlap "2"	*
Overlap "3"	*
Overlap "4"	
•	

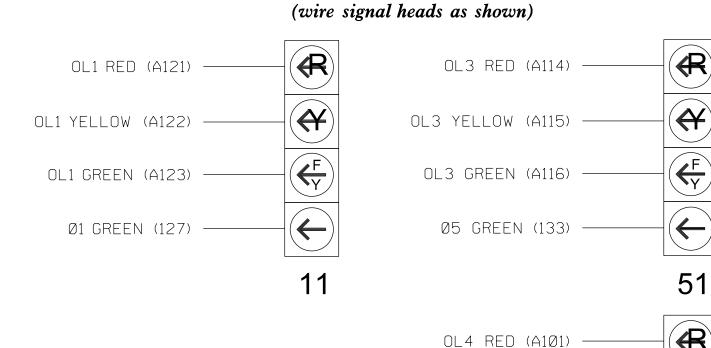
\*See overlap programming detail on sheet 2.

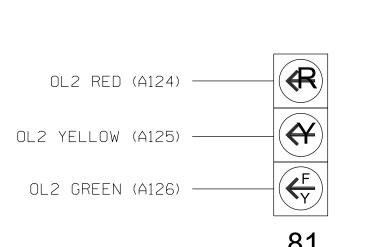
#### Sig-5.1 U-3422A

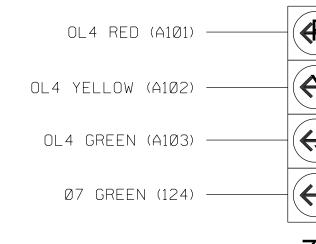
				S	SIGI	<b>NAL</b>	HE	AD	НО	OK-	-UP	СН	AR <sup>-</sup>	Т					
LOAD SWITCH NO.	S	1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	,	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	,	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3		SPARE
SIGNAL HEAD NO.	11	84	21,22	NU	NU	41,42	NU	<b>★</b> 51	61,62	NU	<b>★</b> 71	82,83 84	NU	<b>★</b> 11	<b>★</b> 81	NU	<b>★</b> 51	<b>★</b> 71	NU
RED		*	128			101			134			107							
YELLOW			129			102		*	135		*	108							
GREEN			130			103			136			109							
RED ARROW														A121	A124		A114	A101	
YELLOW ARROW		126												A122	A125		A115	A102	
FLASHING YELLOW ARROW														A123	A126		A116	A103	
GREEN ARROW	127	127						133			124								
NII I — NIa	4 1 1																		

- ★ Denotes install load resistor. See load resistor installation detail this sheet.
- ★ See pictorial of head wiring in detail this sheet.

#### FYA SIGNAL WIRING DETAIL







THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0707

DESIGNED: Jul 2024 SEALED:

REVISED: N/A

#### Electrical Detail - Sheet 1 of 2

ELECTRICAL AND PROGRAMMING SR 1112 (Stoney Point Rd/ DETAILS FOR: Rockfish Rd)

REVISIONS

Prepared for: PLAN DATE: July 2024

50 N.Greenfield Pkwy,Garner,NC 27529

			at			
SR	1108	(Lake	wood	Dr/	King	Rd)
Divisi	on 6	Cumber	land Co	unty	Fayet	tevil
l						

REVIEWED BY: LM Moon PREPARED BY: MR Stanley/DJW DRMP PROJ. NO: 2400555 INIT. DATE

10/3/2024 SIG. INVENTORY NO. 06-0707

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

022516

## INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
1A 1	TB2-1,2	I1U	56	18	1	1	15.0		Х		Х	
IA IA	102-1,2	110	30	-	29	6	3.0		X		Χ	Х
1B	TB2-9,10	I3U	63	29	4	1	15.0		Х		Χ	
2A	TB2-5,6	I2U	39	1	2	2	-		Х	Х	Х	
4A	TB4-9,10	I6U	41	3	8	4	10.0		Х		Х	
5A <sup>2</sup>	TB3-1,2	J1U	55	17	15	5	15.0		Х		Χ	
) DA	100-1,2	310	၂ ၁၁	-	31	2	3.0		Х		Х	Х
6A	TB3-5,6	J2U	40	2	16	6	-		Х	Х	Х	
7A <sup>3</sup>	TDE E G	IELL	57	19	21	7	15.0		Х		Х	
/A	TB5-5,6	J5U	31	-	32	4	3.0		Х		Х	Х
8A	TB5-9,10	J6U	42	4	22	8	3.0		Х		Х	
8B	TB5-11,12	J6L	46	8	23	8	-		Х		Х	

<sup>1</sup>Remove jumper from I1-W to J4-W, on rear of input file.

<sup>2</sup>Remove jumper from J1-W to I4-W, on rear of input file.

<sup>3</sup>Remove jumper from J5-W to I8-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L

FILE J SLOT 2 LOWER

#### LOAD RESISTOR INSTALLATION DETAIL

Note: See notes under the Input File Connection & Programming Chart for removal of jumpers on rear of input file.

(install resistors as shown)

Phase 1 Red Field Terminal (125)

ACCEPTABLE VALUES Value (ohms) Wattage 1.5K - 1.9K 25W (min) 2.0K - 3.0K | 10W (min)

NOT

USED | USED | USED

USED

EX.: 1A, 2A, ETC. = LOOP NO.'S

USED

Phase 5 Yellow Field Terminal (132)

Phase 7 Yellow Field Terminal (123)

10

FS = FLASH SENSE

ST = STOP TIME

**Plans Prepared By:** 

Sig-5.2 U-3422A

#### **OVERLAP PROGRAMMING**

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

#### Overlap Plan 1

Overlap	1	2	3	4
Туре	FYA 4 - Section			
Included Phases	2	4	6	8
Modifier Phases	1	-	5	7
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

#### **OUTPUT CHANNEL CONFIGURATION**

Front Panel

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channel Configuration

#### **Channel Configuration**

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Phase Vehicle	1		Х	X	1
2	Phase Vehicle	2		Х		2
3	Phase Vehicle	3		Х	Х	3
4	Phase Vehicle	4		Х		4
5	Phase Vehicle	5		Х		5
6	Phase Vehicle	6		Х	X	6
7	Phase Vehicle	7		Х		7
8	Phase Vehicle	8		Х	X	8
9	Overlap	1		Х	Χ	9
10	Overlap	2		Х	Χ	10
11	Overlap	3		Х		11
12	Overlap	4		X		12
13	Phase Ped	2				13
14	Phase Ped	4				14
15	Phase Ped	6				15
16	Phase Ped	8				16
17	Overlap	5		X	Х	17
18	Overlap	6		Х		18



#### MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Unit

Web Interface

Home >Controller >Unit

Modify parameters as shown below and save changes.

Start Up Parameters

StartUp Clearance Hold

Unit Flash Parameters

All Red Flash Exit Time

#### FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

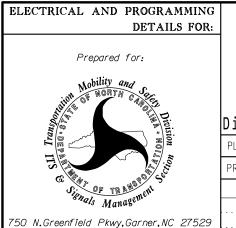
THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0707

DESIGNED: Jul 2024

SEALED:

REVISED: N/A





Electrical Detail - Sheet 2 of 2 SR 1112 (Stoney Point Rd/ Rockfish Rd)

SR 1108 (Lakewood Dr/King Rd) Division 6 Cumberland County Fayetteville PLAN DATE: July 2024 REVIEWED BY: LM Moon

PREPARED BY: MR Stanley/DJW DRMP PROJ. NO: 2400555 REVISIONS INIT. DATE

022516

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SIG. INVENTORY NO. 06-0707

PHASING DIAGRAM

Ø2+6

02+5

01+5

PHASING DIAGRAM DETECTION LEGEND

DETECTED MOVEMENT

UNSIGNALIZED MOVEMENT PEDESTRIAN MOVEMENT

UNDETECTED MOVEMENT (OVERLAP)

Ø3+7

Ø3+8

04+7

04+8

TABLE OF OPERATION

DW DW DW DW DW W W DF

DW W DW W DW DW DW DW DRK
DW DW DW DW DW W DW W

FACE

41,42

61

62

81,82

P41,P42

P81,P82

Sig-6.0 U-3422A

MAXTIME DETECTOR INSTALLATION CHART

21 41,42 61 81,82 P41,P42 P61,P62 P81,P82

SIGNAL FACE I.D.

All Heads L.E.D.

	DETI	ECTOR	PROGRAMMING									
LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN	NEW CARD
1 A	6740		0 4 0		1	15.0	-	Χ	_	Χ	_	_
1A	6X40	0	2-4-2	-	6	3.0	-	Χ	-	Χ	Χ	_
2A	6X6	300	EXIST	_	2	-	-	Χ	Χ	Χ	_	_
2 /	6740		2-4-2		3	15.0	-	Χ	-	Χ	-	_
3A	6X40	0	2-4-2	-	8	3.0	-	Χ	-	Χ	-	-
4A	6X40	0	2-4-2	_	4	10.0	-	Χ	-	Χ	_	_
E A	6740		2-4-2		5	15.0	-	Χ	-	Χ	-	_
5A	6X40	0	2-4-2	-	2	3.0	-	Χ	_	Χ	Χ	-
6A	6X6	300	EXIST	_	6	-	-	Χ	Χ	Χ	_	_
7.0	6740		0 4 0		7	15.0	-	Χ	_	Χ	_	_
7A	6X40	0	2-4-2	-	4	3.0	-	Χ	_	Χ	Χ	-
A8	6X40	0	2-4-2	_	8	_	-	Χ	-	Χ	-	_
8B	6X20	+5	EXIST	_	8	10.0	-	Χ	_	Χ	_	_

8 Phase Fully Actuated (D06-28\_Hope Mills)

#### NOTES

- 1. Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January
- 2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- 3. Phase 1 and/or phase 5 may be lagged.
- 4. Phase 3 and/or phase 7 may be lagged.
- 5. Set all detector units to presence mode.
- 6. Omit "WALK" and flashing "DON'T WALK" with no pedestrian calls.
- 7. Program pedestrian heads to countdown the flashing "Don't Walk" time
- 8. Pavements markings are existing.
- 9. Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.
- 10. Install 2070LX controller with MAXTIME software in existing signal cabinet.

LEGEND

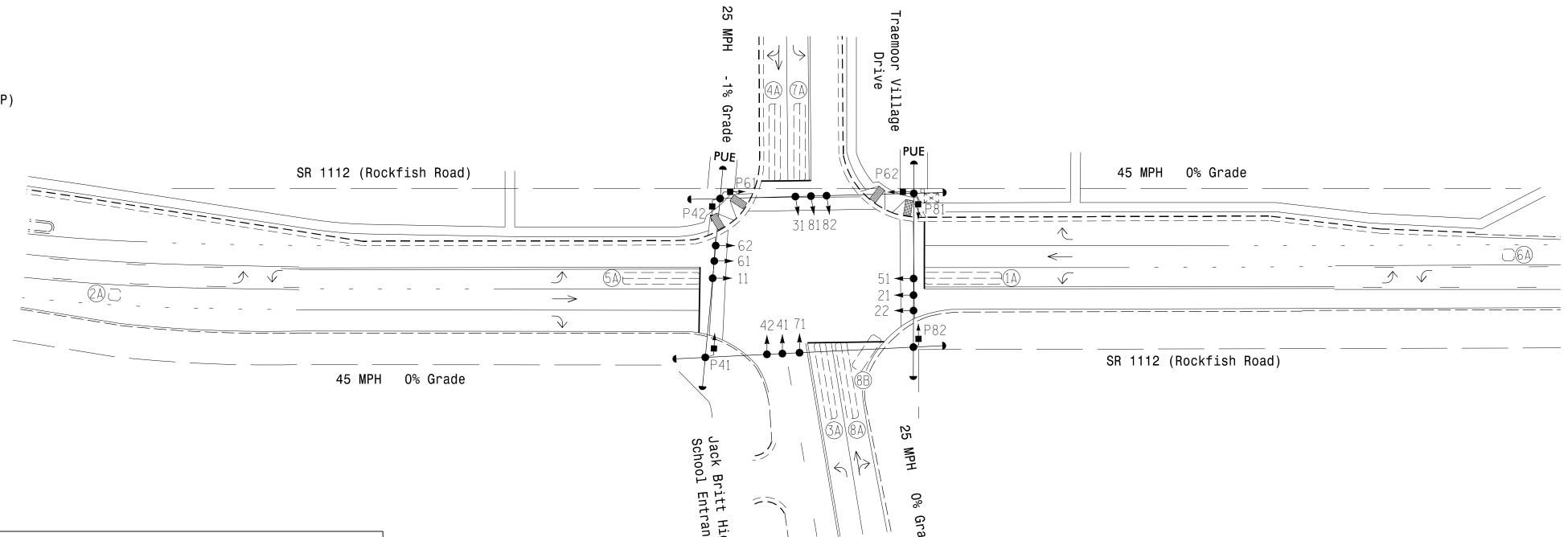
Traffic Signal Head

Modified Signal Head

Sign Pedestrian Signal Head With Push Button & Sign Signal Pole with Guy Signal Pole with Sidewalk Guy Inductive Loop Detector Controller & Cabinet

Junction Box 2-in Underground Conduit

Directional Arrow

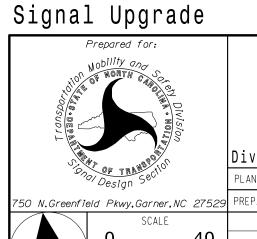


		MAXT	TIME T	IMING	CHART							
FEATURE	PHASE											
FEATURE	1	2	3	4	5	6	7	8				
Walk *	_	_	_	6	_	7	_	6				
Ped Clear	_	_	_	14	_	15	_	14				
Min Green *	7	12	7	7	7	12	7	7				
Passage *	2.0	6.0	2.0	2.0	2.0	6.0	2.0	2.0				
Max 1 *	20	90	20	30	20	90	20	30				
Yellow Change	3.0	4.5	3.0	3.2	3.0	4.5	3.0	3.2				
Red Clear	2.4	1.6	2.4	2.7	2.6	1.6	2.9	2.7				
Added Initial *	_	2.5	_	_	_	2.5	_	_				
Maximum Initial *	_	34	_	_	_	34	_	_				
Time Before Reduction *	_	15	_	_	_	15	_	_				
Time To Reduce *	_	45	_	_	_	45	_	_				
Minimum Gap	_	3.0	_	_	_	3.0	_	_				
Advance Walk	_	_	-	5	_	7	_	6				
Non Lock Detector	Х	_	Х	Х	Х	_	Х	Х				
Vehicle Recall	-	MIN RECALL	-	-	_	MIN RECALL	-	_				
Dual Entry	_	_	_	х	_	_	_	Х				

\* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all

other phases should not be lower than 4 seconds

**Plans Prepared By:** DRMP, Inc, 8210 University Executive Park Drive, Suite 220 Charlotte, NC 28262 NC License No. F-1524 (704) 332-2289 www.DRMP.com



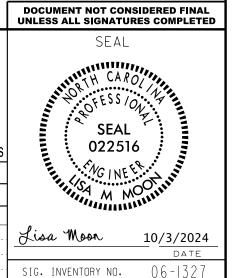
1"=40'

SR 1112 (Rockfish Road) Jack Britt High School/ Traemoor Village Drive

N/A

<u>PROPOSED</u>

Division 6 Cumberland County PLAN DATE: July 2024 O N. Greenfield Pkwy. Garner. NC 27529 PREPARED BY: MR Stanley/DJW DRMP PROJ. NO.: 2400555 REVISIONS INIT. DATE



**EXISTING** 

**-**

N/A

 $\longrightarrow$ 

#### 18 CHANNEL IP CONFLICT MONITOR PROGRAMMING DETAIL ON (remove jumpers and set switches as shown) SW2 REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 1-15, 2-5, 2-6, 2-9, 2-11, 2-15, 3-7, 3-8, 3-10, 3-12, 3-16, 4-7, 4-8, 4-10, 4-12, 4-14, 4-16, 5-9, 5-11, 6-9, 6-11, 6-15, 7-10, 7-12, 7-14, 8-10, 8-12, 8-14, 8-16, 9-11, 9-15, 10-12, 10-14, 10-16, 11-15, 12-14, 12-16 and 14-16. RF 2010 - RP DISABLE WD 1.0 SEC - GY ENABLE SF#1 POLARITY INTERNAL DIP SWITCHES - FYA COMPACT— – FYA 1-9 FYA 3-10 FYA 5-11 — FYA 7-12 COMPONENT SIDE 14 REMOVE JUMPERS AS SHOWN 15 ] 16 NOTES:

#### NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- 2. Program phases 4 and 8 for Dual Entry.
- 3. Program controller to start up in phase 2 Green No Walk and phase 6 Green No Walk.
- 4. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
- 5. The cabinet and controller are part of the D06-28\_Hope Mills Closed Loop Signal System.

#### **EQUIPMENT INFORMATION**

Controller	.2070LX
Cabinet	.332 w/ Aux
Software	.Q-Free MAXTIME
Cabinet Mount	.Base
Output File Positions	.18 With Aux. Output File
Load Switches Used	.S1, S2, S4, S5, S6, S7, S8, S9, S10,
	S11, S12, AUX S1, AUX S2, AUX S4,
	AUX S5
Phases Used	1,2,3,4,4PED,5,6,6PED,7,8,8PED
Overlap "1"	*
Overlap "2"	*
Overlap "3"	*
Overlap "4"	*

\*See overlap programming detail on sheet 2.

TB2-1,2

TB2-5,6

TB4-5,6

TB4-9,10

2A

4A

P41,P42

P61,P62

= DENOTES POSITION OF SWITCH

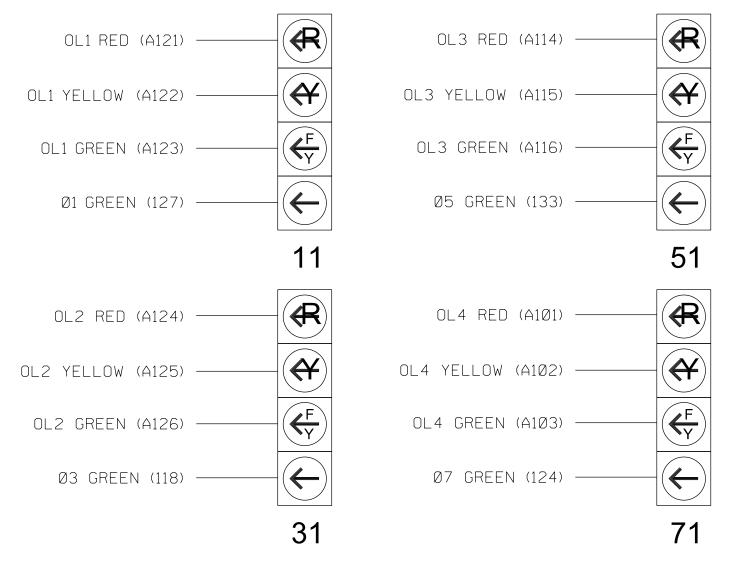
#### Sig-6 1 U-3422A

					SIC	GN/	\L H	ΙΕΑ	DΗ	00	K-U	P C	HA	RT						
LOAD SWITCH NO.	S1	S2	S3	S	4	S5	S6	S7	S8	S9	S	10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	3	4	14	5	6	15	-	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	3	4	4 PED	5	6	6 PED	-	7	8	8 PED	OL1	OL2	SPARE		OL4	SPARE
SIGNAL HEAD NO.	11	21,22	NU	22	<b>★</b> 31	41,42	P41, P42	<b>★</b> 51	61,62	P61, P62	62	<b>71</b>	81,82	P81, P82	11	31 <b>*</b>	NU	<b>★</b> 51	<b>71</b>	NU
RED		128		*		101			134		*		107							
YELLOW	*	129				102		*	135				108							
GREEN		130				103			136				109							
RED ARROW															A121	A124		A114	A101	
YELLOW ARROW				117							123				A122	A125		A115	A102	
FLASHING YELLOW ARROW															A123	A126		A116	A103	
GREEN ARROW	127			118	118			133			124	124								
₩							104			119				110						
Ķ							106			121				112						

NU = Not Used ★ See pictorial of head wiring in detail this sheet.

#### FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1327 DESIGNED: Jul 2024 SEALED: REVISED: N/A

#### Electrical Detail - Sheet 1 of 2

ELECTRICAL AND PROGRAMMING SR 1112 (Rockfish Road) DETAILS FOR: Prepared for:



Jack Britt High School/ Traemoor Village Drive Division 6 Cumberland County

Hope Mills PLAN DATE: July 2024 REVIEWED BY: L M MOON PREPARED BY: MR Stanley/DJW DRMP PROJ. NO: 2400555 REVISIONS INIT. DATE

SEAL 022516 Lisa Moon 10/3/2024

06-132

SIG. INVENTORY NO.

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

#### I1U 6 2/SYS I2U | 39 | 20 15.0 Χ 3.0 30 Χ X I6U | 41 | 3 4 10.0 Χ

PED 4

PED 6

INSTALL DC ISOLATORS IN INPUT FILE SLOTS

I12 AND I13.

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP INPUT PIN INPUT DETECTOR CALL DELAY EXTEND EXTEND ADDED NO. PHASE TIME TIME

15 15.0 5A <sup>3</sup> J1U TB3-1,2 3.0 Χ 6A TB3-5,6 J2U 40 2 16 6/SYS Χ 15.0 J5U TB5-5,6 3.0 8A TB5-9,10 J6U 42 4 8B TB5-11,12 | 46 | 8 23 10.0 J6L 1 PED PUSH **BUTTONS** 

P81,P82 I13L | 70 | 36 <sup>1</sup>Remove jumper from I1-W to J4-W, on rear of input file. <sup>2</sup>Remove jumper from I5-W to J8-W, on rear of input file.

6

<sup>3</sup>Remove jumper from J1-W to I4-W, on rear of input file. ⁴Remove jumper from J5-W to I8-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L

34

I12L | 69 | 35

68

#### INPUT FILE POSITION LAYOUT (front view)

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.

2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.

3. Ensure that the Red Enable is active at all times during normal operation

4. Integrate monitor with Ethernet network in cabinet.

		1	2	3	4	5	6	7	8	9	10	11	12	13	14
	U	Ø 1	Ø 2	S L	S L	Ø 3	Ø 4	S L	S L	S L	S	S	NOT	Ø6 PED	FS
FILE		1A	2A	O T	O T	3A	4A	O T	O T	O T	O T	O T	USED	DC ISOLATOR	DC ISOLATOR
" "	ı	NOT	NOT	E M P	E M P	NOT	NOT	E M P	E M P	E M P	E M P	E M P	Ø4 PED	/	
	_	USED	USED	Y	Y	USED	USED	Y	Y	Y	Y	Y	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR
		Ø 5	Ø 6	S L	S L	Ø 7	Ø 8	S	S	S L	S	S	S	S	S L
FILE	U	5A	6A	O T	O T	7A	8A	O T	P T	P T	O T	þ	O T	O T	O T
"J"		NOT	NOT	E M P	E M P	NOT	Ø 8	E M P	E M P	E M P	E M P	E M P	E M P	E M P	E M P
	-	USED	USED	Y Y	Y	USED	8B	T Y	T Y	T Y	T Y	T Y	Y	T Y	T Y
	L	EV . 14 24 FTC - LOOP NO IS												ENOE	

EX.: 1A, 2A, ETC. = LOOP NO.'S FS = FLASH SENSE ST = STOP TIME Note: See notes under the Input File Connection & Programming Chart for removal of jumpers on rear of input file.

Phase 1 Yellow Field Terminal (126)

#### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

ACCEPTABLE VALUES Value (ohms) | Wattage 1.5K - 1.9K | 25W (min) 2.0K - 3.0K | 10W (min) |

Phase 3 Red Field Terminal (116) Phase 5 Yellow Field Terminal (132) Phase 7 Red Field AC-Terminal (122) AC-

FILE J -SLOT 2 LOWER -

**Plans Prepared By:** 

CALL DURING

Χ

Χ

Χ

Χ

Χ

Χ

X

Χ

Χ

#### OVERLAP PROGRAMMING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

#### Overlap Plan 1

Overlap	1	2	3	4
Туре	FYA 4 - Section			
Included Phases	2	4	6	8
Modifier Phases	1	3	5	7
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

#### OUTPUT CHANNEL CONFIGURATION

Front Panel

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channel Configuration

#### **Channel Configuration**

- Triain						
Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Phase Vehicle	1		Х	Х	1
2	Phase Vehicle	2		Х		2
3	Phase Vehicle	3		Х	X	3
4	Phase Vehicle	4		X		4
5	Phase Vehicle	5		X		5
6	Phase Vehicle	6		X	Х	6
7	Phase Vehicle	7		Х		7
8	Phase Vehicle	8		X	Х	8
9	Overlap	1		Х	X	9
10	Overlap	2		Х	X	10
11	Overlap	3		Х		11
12	Overlap	4		X		12
13	Phase Ped	2				13
14	Phase Ped	4				14
15	Phase Ped	6				15
16	Phase Ped	8				16
17	Overlap	5		X	Х	17
18	Overlap	6		X		18



#### MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel Main Menu >Controller >Unit

Web Interface Home >Controller >Unit

Modify parameters as shown below and save changes.

Start Up Parameters StartUp Clearance Hold **Unit Flash Parameters** All Red Flash Exit Time

#### COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

#### FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

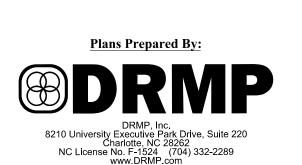
- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

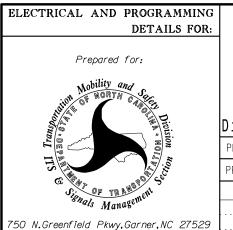
THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1327 DESIGNED: Jul 2024 SEALED:

REVISED: N/A

INIT. DATE





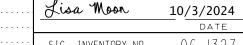
Electrical Detail - Sheet 2 of 2 SR 1112 (Rockfish Road) Jack Britt High School/

REVISIONS

Traemoor Village Drive
Division 6 Cumberland County Hope Mills PLAN DATE: July 2024 REVIEWED BY: LM Moon PREPARED BY: MR Stanley/DJW DRMP PROJ. NO: 2400555

SEAL 022516

SIG. INVENTORY NO.

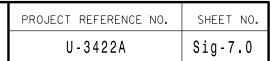


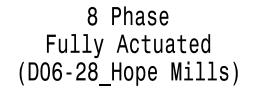
Sig-6.2

PROJECT REFERENCE NO.

U-3422A

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED





#### NOTES

- 1. Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January
- 2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- 3. Phase 1 and/or phase 5 may be lagged.
- 4. Phase 3 and/or phase 7 may be lagged.
- 5. Set all detector units to presence mode.
- 6. Omit "WALK" and flashing "DON'T WALK" with no pedestrian calls.
- 7. Program pedestrian heads to countdown the flashing "Don't Walk"
- time only
- 8. Pavement markings are existing unless otherwise shown/noted. See Inset. 9. Maximum times shown in timing chart are for free-run operation only.

LEGEND

Traffic Signal Head

Modified Signal Head

Pedestrian Signal Head With Push Button & Sign Signal Pole with Guy Signal Pole with Sidewalk Guy Inductive Loop Detector Controller & Cabinet Junction Box

2-in Underground Conduit

Right of Way

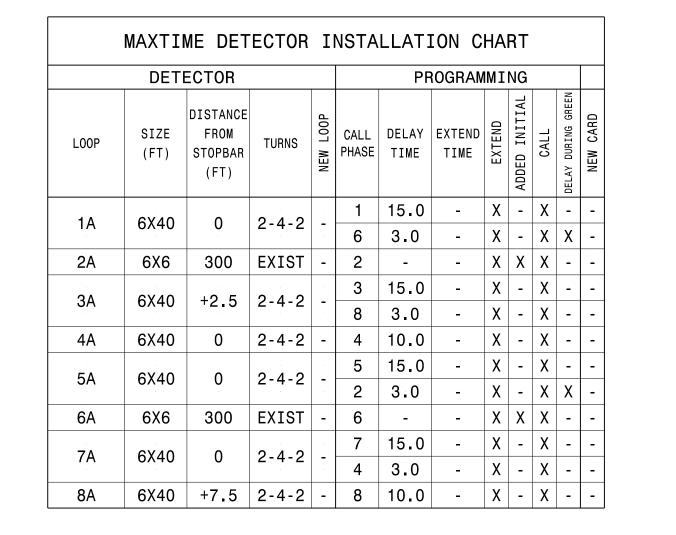
Directional Arrow

Wheelchair Ramp

Sidewalk

Type II Signal Pedestal

- Coordinated signal system timing values supersede these values.
- 10. Install 2070LX controller with MAXTIME software in existing signal cabinet.



Ø4+7 UNDETECTED MOVEMENT (OVERLAP) TABLE OF OPERATION

FACE

41,42

51

61

6.2

81,82

P21,P22

P81,P82

PHASE

|DW|DW| W | W |DW|DW|DW|DW|DF

DW DW DW DW DW W DW W D1

21 41,42 61 81,82

PHASING DIAGRAM DETECTION LEGEND

PHASING DIAGRAM

Ø2+6

Ø2+5

Ø1+6

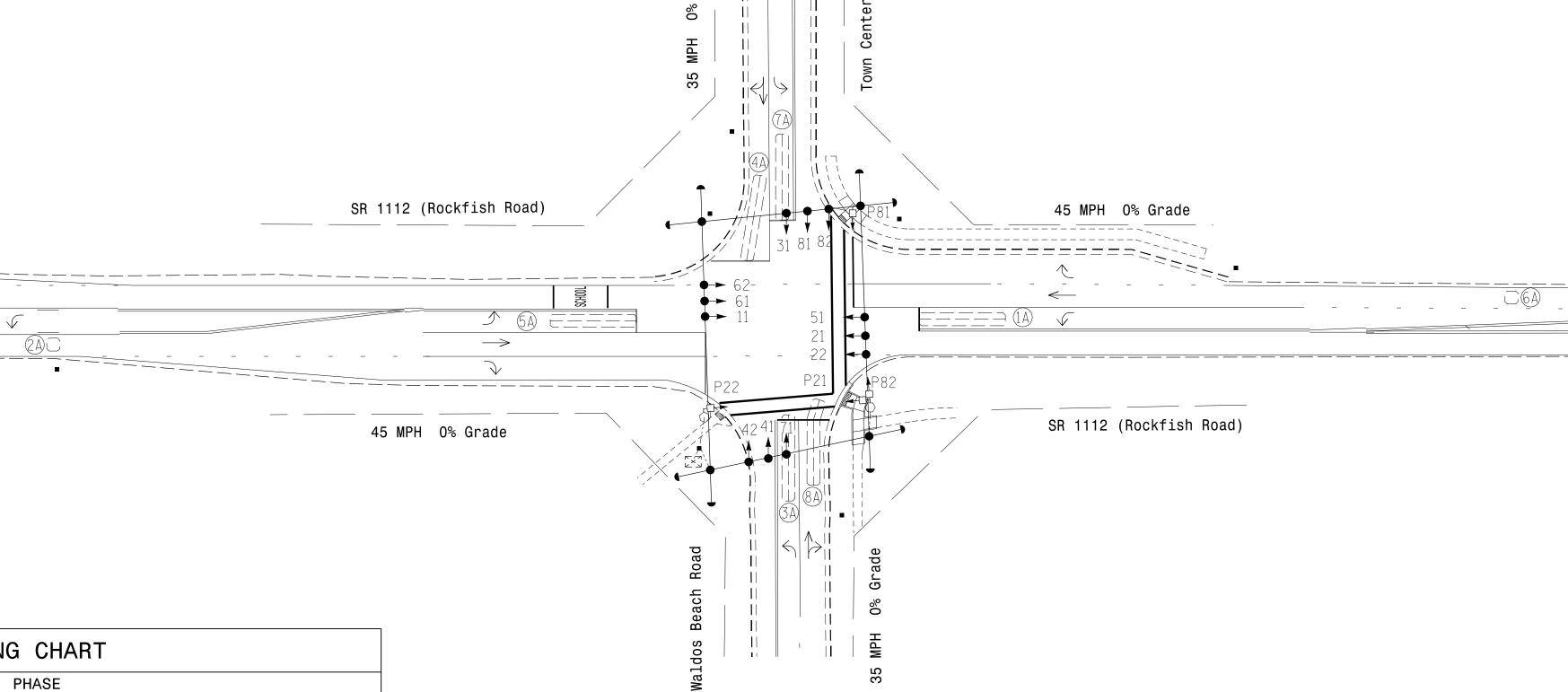
Ø3+7

Ø3+8

DETECTED MOVEMENT

UNSIGNALIZED MOVEMENT

 $<\!\!\!<\!\!\!--\!\!\!>$  PEDESTRIAN MOVEMENT



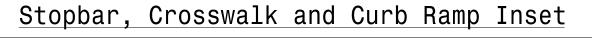
SIGNAL FACE I.D.

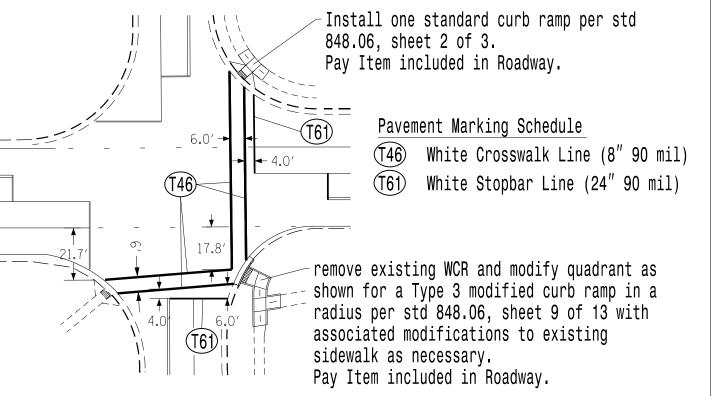
All Heads L.E.D.

P21,P22 P81,P82

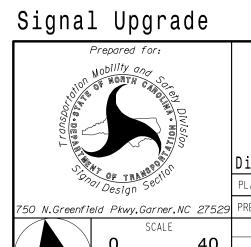
		MAX	TIME T	IMING	CHART			
FEATURE				PH	IASE			
FEATURE	1	2	3	4	5	6	7	8
Walk *	_	14	_	-	_	_	_	14
Ped Clear	_	13	_	-	_	_	_	21
Min Green *	7	12	7	7	7	12	7	7
Passage *	2.0	6.0	2.0	2.0	2.0	6.0	2.0	2.0
Max 1 *	15	60	15	20	15	60	15	20
Yellow Change	3.0	4.5	3.0	3.8	3.0	4.5	3.0	3.8
Red Clear	2.8	1.4	2.1	1.3	2.9	1.4	2.1	1.3
Added Initial *	_	2.5	_	_	_	2.5	_	_
Maximum Initial *	_	34	_	_	_	34	_	_
Time Before Reduction *	_	15	_	_	_	15	_	_
Time To Reduce *	_	45	_	_	_	45	_	_
Minimum Gap	_	3.0	_	-	_	3.0	_	_
Advance Walk	_	7	_	_	_	_	_	7
Non Lock Detector	х	_	Х	Х	Х	_	Х	Х
Vehicle Recall	_	MIN RECALL	_	_	_	MIN RECALL	_	_
Dual Entry	_	_	_	Х	_	_	_	Х

\* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.





**Plans Prepared By:** DRMP, Inc, 8210 University Executive Park Drive, Suite 220 Charlotte, NC 28262 NC License No. F-1524 (704) 332-2289 www.DRMP.com



1"=40'

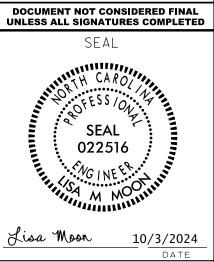
gnal Upgrade					
Prepared for:	SF	R 1112	2 (Ro	ckfish	Road)
MONTH CAR			a	t	ŕ
Div Ods:				ach Ro	
Tron Majon Majon		Towr	ı Cen	ter Dr	ive
	Division	6 Cum	berland	County	Fayett
Op TRANSCION	PLAN DATE:	August	2024	REVIEWED BY:	LM Moo

)rive Fayetteville LM Moon 50 N.Greenfield Pkwy, Garner, NC 27529 PREPARED BY: MR Stanley/DJW DRMP PROJ. NO.: 2400555 REVISIONS INIT. DATE

**PROPOSED** 

N/A

N/A



SIG. INVENTORY NO.

06-1347

**EXISTING** 

**●** →

N/A

 $\longrightarrow$ 

-----

## 18 CHANNEL IP CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 2-13, 3-7, 3-8, 3-10, 3-12, 3-16, 4-7, 4-8, 4-10, 4-12, 4-16, 5-9, 5-11, 5-13, 6-9, 6-11, 6-13, 7-10, 7-12, 8-10, 8-12, 8-16, 9-11, 9-13, 10-12, 10-16, 11-13 and 12-16. - RF 2010 — - RP DISABLE — WD 1.0 SEC - GY ENABLE INTERNAL DIP SWITCHES - SF#1 POLARITY - FYA COMPACT— — FYA 1-9 FYA 3-10 FYA 5-11 FYA 7-12

#### REMOVE JUMPERS AS SHOWN

#### NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.

COMPONENT SIDE

- 2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- 3. Ensure that the Red Enable is active at all times during normal operation
- 4. Integrate monitor with Ethernet network in cabinet.

#### NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- 2. Program phases 4 and 8 for Dual Entry.
- 3. Program controller to start up in phase 2 Green No Walk and phase 6 Green No Walk.
- 4. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
- 5. The cabinet and controller are part of the D06-28\_Hope Mills Closed Loop Signal System.

#### **EQUIPMENT INFORMATION**

Controller	
Software	
Cabinet Mount	Base
Output File Positions	18 With Aux. Output File
Load Switches Used	S1, S2, S3, S4, S5, S7, S8, S10, S11, S12, AUX S1, AUX S2, AUX S4, AUX S5
Phases Used	1, 2, 2PED, 3, 4, 5, 6, 7, 8, 8PED
Overlap "1"	*
Overlap "2"	*
	Cabinet Software Cabinet Mount Output File Positions Load Switches Used Phases Used Overlap "1"

Overlap "4"...

\*See overlap programming detail on sheet 2.

Overlap "3".....

#### Sig-7 1 U-3422A

					SI	GN/	\L H	łΕΑ	DΗ	00	K-U	P C	HA	RT						
LOAD SWITCH NO.	S1	S2	S3	S	4	S5	S6	S7	S8	S9	S	10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	3	4	14	5	6	15	-	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	(	3	4	4 PED	5	6	6 PED	-	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	11	21,22	P21, P22	22	<b>★</b> 31	41,42	NU	<b>★</b> 51	61,62	NU	62	<b>71</b>	81,82	P81, P82	11	31	NU	<b>★</b> 51	<b>71</b>	NU
RED		128			*	101			134			*	107							
YELLOW	*	129				102		*	135				108							
GREEN		130				103			136				109							
RED ARROW															A121	A124		A114	A101	
YELLOW ARROW				117							123				A122	A125		A115	A102	
FLASHING YELLOW ARROW															A123	A126		A116	A103	
GREEN ARROW	127			118	118			133			124	124								
₩		-	113											110						
Ķ			115		·									112						

NU = Not Used

★ See pictorial of head wiring in detail this sheet.

ST = STOP TIME

15 ] 16

= DENOTES POSITION OF SWITCH

ON OFF

SW2

#### INPUT FILE POSITION LAYOUT

(front view)

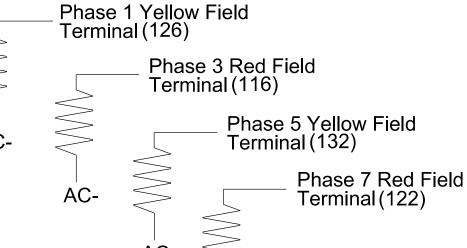
	r	1	2	3	4	5	6	7	8	9	10	11	12	13	14
	U	Ø 1	Ø 2	S L	S L	Ø 3	Ø 4	S L	S	S	S	S L	Ø2 PED	NOI	FS
FILE		1A	2A	O T	O T	3A	4A	O T	O T	O T	O T	O T	DC ISOLATOR	USED	DC ISOLATOR
" "	L	NOT USED	NOT USED	E M P T	E M P T	NOT USED	NOT USED	E M P T	E M P T	E M P T	E M P T	E M P	NOT USED	Ø 8 PED	ST DC
		USLD	OSLD	Ÿ	Ý	USLD	USLD	Ý	Ý	Ý	Ý	Y	USLD	ISOLATOR	
	U	Ø 5	Ø 6	S L	SL	Ø 7	Ø 8	S L	SL	SL	SL	S L	SL	SL	SL
FILE		5A	6A	O T	Ō T	7A	8A	O T	O T	O T	O T	Ō T	Ŏ T	O T	O T
"J"	L	NOT USED	NOT USED	E M P T Y	E M P T Y	NOT USED	NOT USED	E M P T Y							
	L	EX. : 1A, 2A, ETC. = LOOP NO.'S									FS = FLASH SENSE				

LOAD RESISTOR INSTALLATION DETAIL

Note: See notes under the Input File Connection & Programming Chart for removal of jumpers on rear of input file.

(install resistors as shown)

ACCEPTABLE VALUES Value (ohms) | Wattage 1.5K - 1.9K | 25W (min) 2.0K - 3.0K | 10W (min) |



#### INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
1A 1	TB2-1,2	I1U	56	18	1	1	15.0		Х		Х	
IA IA	102-1,2	110	30	-	29	6	3.0		Х		Х	Х
2A	TB2-5,6	I2U	39	1	2	2	-		Х	Χ	Х	
3A <sup>2</sup>	TD4.5.6	I5U	58	20	7	3	15.0		X		Х	
3A	TB4-5,6	150	30	-	30	8	3.0		Х		Х	Х
4A	TB4-9,10	I6U	41	3	8	4	10.0		Х		Х	
5A <sup>3</sup>	TD2 4.2	1411	55	17	15	5	15.0		Х		Х	
ЭA	TB3-1,2	J1U	၂ ၁၁	-	31	2	3.0		Х		Х	Х
6A	TB3-5,6	J2U	40	2	16	6	-		Х	Х	Х	
7A <sup>4</sup>	TDE E C	IELI	E 7	19	21	7	15.0		Х		Х	
/A	TB5-5,6	J5U	57	-	32	4	3.0		Х		Х	Х
8A	TB5-9,10	J6U	42	4	22	8	10.0		Х		Х	
PED PUSH								<b>'</b>				
BUTTONS							NOTE:					
P21,P22	TB8-4,6	I12U	67	33	2	PED 2						
P81,P82	TB8-8,9	I13L	70	36	8	PED 8	IN INPO		13			

<sup>1</sup>Remove jumper from I1-W to J4-W, on rear of input file.

<sup>2</sup>Remove jumper from I5-W to J8-W, on rear of input file.

<sup>3</sup>Remove jumper from J1-W to I4-W, on rear of input file.

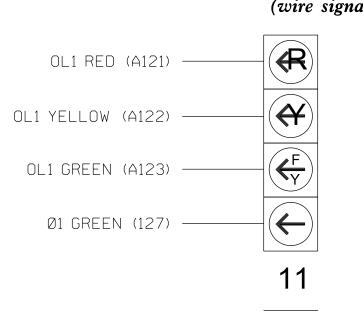
⁴Remove jumper from J5-W to I8-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L

FILE J SLOT 2 LOWER -

#### FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



OL3 RED (A114) OL3 YELLOW (A115) OL3 GREEN (A116) Ø5 GREEN (133)



51 OL4 RED (A1Ø1) OL4 YELLOW (A102) OL4 GREEN (A1Ø3) Ø7 GREEN (124)

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1347 DESIGNED: Aug 2024 SEALED: REVISED: N/A

LM Moon

#### Electrical Detail - Sheet 1 of 2

ELECTRICAL AND PROGRAMMING DETAILS FOR:

PLAN DATE: August 2024 REVIEWED BY: PREPARED BY: MR Stanley/DJW DRMP PROJ. NO: 2400555

REVISIONS

Waldos Beach Road, Town Center Drive Division 6 Cumberland County Fayetteville

SR 1112 (Rockfish Road)

SEAL 022516

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

INIT. DATE 10/3/2024 SIG. INVENTORY NO. 06-1347

**Plans Prepared By:** 

Prepared for: 50 N.Greenfield Pkwy,Garner,NC 27529

#### PROJECT REFERENCE NO. U-3422A

Sig-7.2

#### OVERLAP PROGRAMMING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

#### Overlap Plan 1

Overlap	1	2	3	4
Туре	FYA 4 - Section			
Included Phases	2	4	6	8
Modifier Phases	1	3	5	7
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

#### **OUTPUT CHANNEL CONFIGURATION**

Front Panel

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channel Configuration

#### Channel Configuration

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Phase Vehicle	1		Χ	Χ	1
2	Phase Vehicle	2		Х		2
3	Phase Vehicle	3		Х	Χ	3
4	Phase Vehicle	4		Х		4
5	Phase Vehicle	5		Х		5
6	Phase Vehicle	6		Х	Χ	6
7	Phase Vehicle	7		Х		7
8	Phase Vehicle	8		Х	Χ	8
9	Overlap	1		Х	X	9
10	Overlap	2		Х	Χ	10
11	Overlap	3		Х		11
12	Overlap	4		Х		12
13	Phase Ped	2				13
14	Phase Ped	4				14
15	Phase Ped	6				15
16	Phase Ped	8				16
17	Overlap	5		Х	Х	17
18	Overlap	6		Х		18



#### MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Unit

Web Interface

Home >Controller >Unit

Modify parameters as shown below and save changes.

Start Up Parameters

StartUp Clearance Hold

All Red Flash Exit Time

Unit Flash Parameters

#### FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

#### COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1347 DESIGNED: Aug 2024 SEALED:

REVISED: N/A

Electrical Detail - Sheet 2 of 2 ELECTRICAL AND PROGRAMMING SR 1112 (Rockfish Road) Prepared for:

Waldos Beach Road/ Town Center Drive Division 6 Cumberland County Fayetteville

PLAN DATE: August 2024 REVIEWED BY: LM Moon PREPARED BY: MR Stanley/DJW DRMP PROJ. NO: 2400555 REVISIONS INIT. DATE

10/3/2024 SIG. INVENTORY NO.

022516

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

**Plans Prepared By:** 

PHASING DIAGRAM

PHASING DIAGRAM DETECTION LEGEND

DETECTED MOVEMENT

UNSIGNALIZED MOVEMENT PEDESTRIAN MOVEMENT

UNDETECTED MOVEMENT (OVERLAP)

Ø3+7

Ø3+8

04+7

04+8

Ø2+6

02+5

#### Sig-8.0 U-3422A

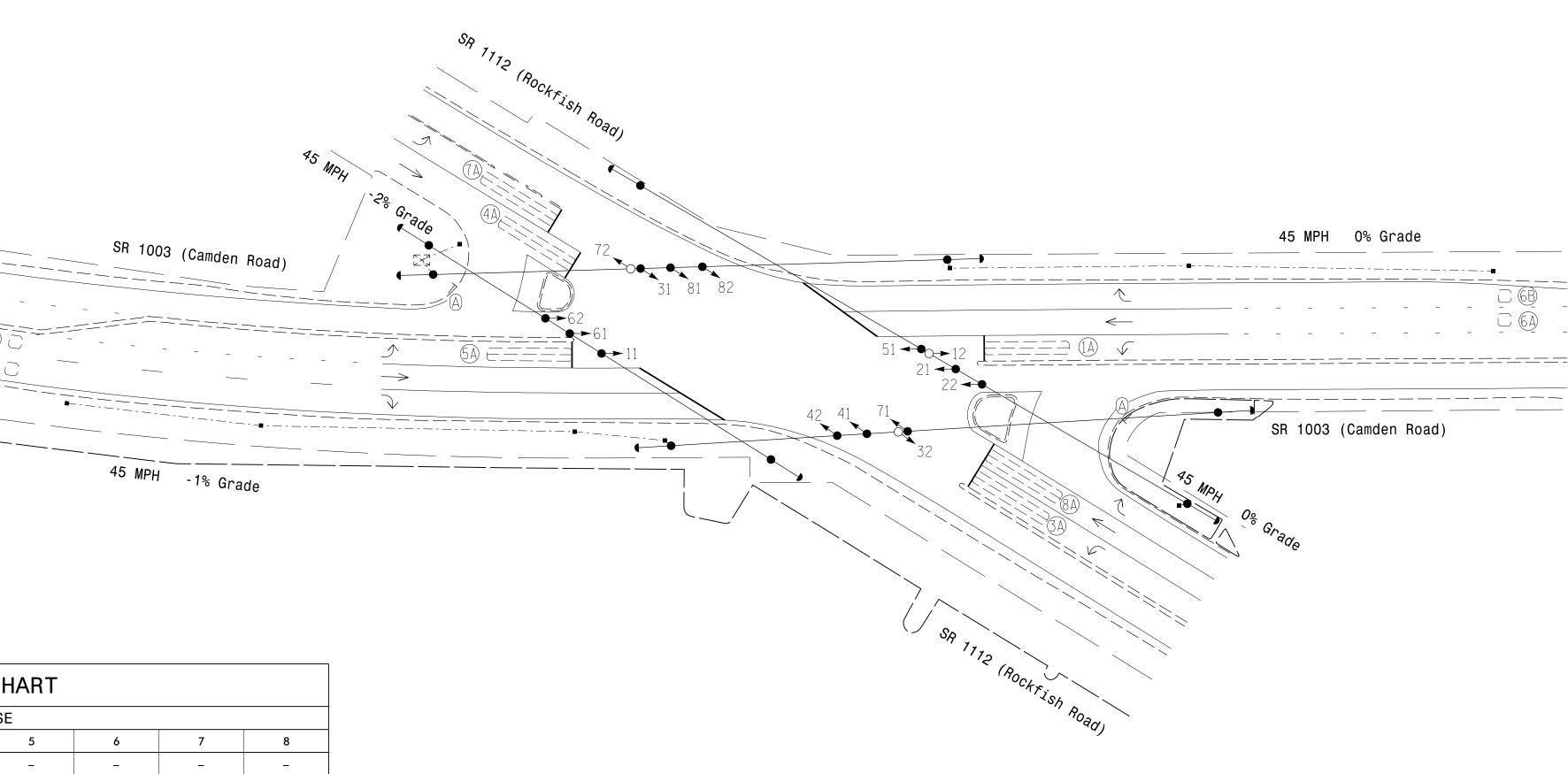
# MAXTIME DETECTOR INSTALLATION CHART

	DETE	ECTOR			PROGRAMMING								ı
L00P	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN	NEW CARD	
1 A	6X40	0	2-4-2	_	1	15.0	-	Χ	1	Χ	-	-	I
1 A	0740	U	2 4 2		6	3:0	-	Χ	-	Χ	Χ	-	I
2 A	6X6	300	EXIST	_	2	_	_	Χ	Χ	Χ	-	-	I
2B	6X6	300	EXIST	-	2	-	_	Χ	Χ	Χ	1	1	I
3 <sup>1</sup> A	6X40	0	2-4-2	_	3	15.0	-	Χ	-	Χ	-	-	l
JA	0 1 4 0	U	2-4-2		8	3.0	_	Χ	1	Χ	ı	1	ı
4 A	6X40	0	2-4-2	-	4		-	Χ	_	Χ	ı	- 1	l
5 A	6X40	0	2-4-2	_	5	15.0	_	Χ	-	Χ	-	-	ı
SA	0 0 4 0		2-4-2	_	2	3.0	_	Χ	1	Χ	X	-	I
6 A	6X6	300	EXIST	_	6	-	-	Χ	Χ	Χ	-	-	ı
6B	6X6	300	EXIST	-	6	-	-	Χ	Χ	Χ	-	-	ı
7 A	6X40	0	2-4-2	_	7	15.0	-	Χ	-	Χ	-	_	I
1 'A	00040		Z-4-Z		4	3.0	_	Χ	-	Χ	_	_	ı
8 A	6X40	0	2-4-2	_	8	-	-	Χ	-	Χ	-	_	I

#### 8 Phase Fully Actuated (D06-28\_Hope Mills)

#### NOTES

- 1. Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January
- 2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- 3. Phase 1 and/or phase 5 may be lagged.
- 4. Phase 3 and/or phase 7 may be lagged.
- 5. Set all detector units to presence mode.
- 6. Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.
- 7. Install 2070LX controller with MAXTIME software in existing signal cabinet.



SIGNAL FACE I.D.

All Heads L.E.D.

21 41,42 61 81,82

TABLE OF OPERATION

SIGNAL

FACE

11,12

31,32

41,42

71,72

81,82

	LEGEND	
PROPOSED		<u>EXISTING</u>
$\bigcirc$	Traffic Signal Head	•
	Modified Signal Head	N/A
	Sign	
$\downarrow$	Pedestrian Signal Head	•
	Signal Pole with Guy Signal Pole with Sidewalk Guy	· • • • • • • • • • • • • • • • • • • •
	Inductive Loop Detector	
	Controller & Cabinet	K Z
	Junction Box	
	2-in Underground Conduit	
N/A	Right of Way	
$\longrightarrow$	Directional Arrow	$\longrightarrow$
	"YIELD" Sign (R1-2)	

		MAX.	TIME	TIMING	CHART								
FFATURE	PHASE												
FEATURE	1	2	3	4	5	6	7	8					
Walk *	_	_	_	_	_	-	_	_					
Ped Clear	_	_	_	_	_	-	_	_					
Min Green *	7	12	7	7	7	12	7	7					
Passage *	2.0	6.0	2.0	2.0	2.0	6.0	2.0	2.0					
Max 1 *	15	90	15	35	15	90	15	35					
Yellow Change	3.0	4.6	3.2	4.7	3.0	4.6	3.3	4.7					
Red Clear	3.3	1.9	3.8	2.5	3.1	1.9	3.9	2.5					
Added Initial *	_	2.5	_	_	_	2.5	_	_					
Maximum Initial *	_	34	_	_	_	34	_	_					
Time Before Reduction *	_	15	_	_	_	15	_	_					
Time To Reduce *	-	30	_	_	_	30	_	_					
Minimum Gap	_	3.0	_	_	_	3.0	_	_					
Advance Walk	-	_	_	_	_	-	_	_					
Non Lock Detector	Х	_	х	х	Х	-	х	х					
Vehicle Recall	_	MIN RECALL	_	_	_	MIN RECALL	_	_					
Dual Entry	_	_	_	Х	_	_	_	Х					

\* These values may be field adjusted. Do not adjust Min Green and Passage times for phases 2 and 6 lower than what is shown. Min Green for all

other phases should not be lower than 4 seconds.

**Plans Prepared By:** 



Signal Upgrade

SR 1003 (Camden Road) SR 1112 (Rockfish Road)

Division 6 Cumberland County Hope Mills PLAN DATE: August 2024 REVIEWED BY: LM Moon N.Greenfield Pkwy, Garner, NC 27529 PREPARED BY: MR Stanley/DJW RKA PROJ. NO.: 2400555 REVISIONS INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

#### 18 CHANNEL IP CONFLICT MONITOR ON OFF PROGRAMMING DETAIL (remove jumpers and set switches as shown) SW2 REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 3-7, 3-8, 3-10, 3-12, 4-7, 4-8, 4-10, 4-12, 5-9, 5-11, 6-9, 6-11, 7-10, 7-12, 8-10, 8-12, 9-11 and 10-12. — RF 2010 — — RP DISABLE ■ WD 1.0 SEC GY ENABLE SF#1 POLARITY ■— LEDguard - RF SSM - FYA COMPACT FYA 1-9 FYA 3-10 - FYA 5-11 - FYA 7-12 10 <sup>1</sup> 11 12 COMPONENT SIDE ] 13 14 15 REMOVE JUMPERS AS SHOWN 16 18 – 1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently. = DENOTES POSITION OF SWITCH 2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.

3. Ensure that the Red Enable is active at all times during normal operation.

4A

NOT

NOT

USED

USED USED

NOT

USED

INPUT FILE POSITION LAYOUT

(front view)

4. Integrate monitor with Ethernet network in cabinet.

1A

NOT

USED

USED

EX.: 1A, 2A, ETC. = LOOP NO.'S

FILE

#### NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- 2. Program phases 4 and 8 for Dual Entry.
- 3. Program controller to start up in phase 2 Green No Walk and 6 Green No
- 4. If this signal will be managed by an ATMS software, enable controller and
- 5. The cabinet and controller are part of the D06-28\_Hope Mills Closed Loop

#### **EQUIPMENT INFORMATION**

Controller	New 2070LX
Cabinet	Existing 332 w/ Aux
Software	Q-Free MAXTIME
Cabinet Mount	Base
Output File Positions	18 With Aux. Output File
Load Switches Used	S1, S2, S4, S5, S7, S8, S10, S11,
	AUX S1, AUX S2, AUX S4, AUX S5
Phases Used	1, 2, 3, 4, 5, 6, 7, 8
Overlap "1"	*
Overlap "2"	*
Overlap "3"	*
Overlap "4"	*

- detector logging for all detectors used at this location.
- Signal System.

\*See overlap programming detail on sheet 2

## (wire signal heads as shown)

FYA SIGNAL WIRING DETAIL

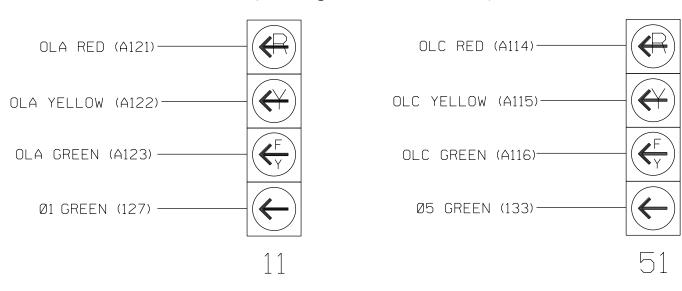
123

124 | 124

22 31 41,42 NU 51 61,62 NU 62 71 81,82 NU 11 31 NU 51 71 NU

108

109



SIGNAL HEAD HOOK-UP CHART

134

**\*** 135

133

S2 | S3

128

**\*** 129

127

★See pictorial of head wiring in detail this sheet.

101

102

103

117

118 | 118

\* Denotes install load resistor. See load resistor installation detail this sheet.

SWITCH NO.

CMU CHANNEL NO.

PHASE

SIGNAL HEAD NO

RED

YELLOW

GREEN

RED ARROW

YELLOW ARROW

FLASHING

YELLOW ARROW GREEN

ARROW

NU = Not Used

OLB RED (A124)	
OLB YELLOW (A125)	
OLB GREEN (A126)	F
Ø3 GREEN (118)	<b>(</b>
	21



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0470 DESIGNED: Aug 2024 SEALED: REVISED: N/A

#### INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
1A <sup>1</sup>	TD2 4.2	1411	56	18	1	1	15		Х		Х	
IA	TB2-1,2	I1U	00	-	29	6	3		Х		Х	Х
2A	TB2-5,6	I2U	39	1	2	2			Х	Х	Х	
2B	TB2-7,8	I2L	43	5	3	2			Х	Х	Х	
3A <sup>2</sup>	TD4 5 6	IELI	58	20	7	3	15		Х		Х	
JA	TB4-5,6	I5U	00	-	30	8	3		Х		Х	
4A	TB4-9,10	I6U	41	3	8	4			Х		Х	
5A <sup>3</sup>	TD2 1 2	1411	55	17	15	5	15		Х		Х	
DA.	TB3-1,2	J1U	55	-	31	2	3		Х		Х	Х
6A	TB3-5,6	J2U	40	2	16	6			Х	Х	Х	
6B	TB3-7,8	J2L	44	6	17	6			Х	Χ	Х	
7A <sup>4</sup>	TB5-5,6	J5U	57	19	21	7	15		Х		Х	
/A	0,6-001	130	3/	-	32	4	3		Х		Х	
8A	TB5-9,10	J6U	42	4	22	8			Х		Х	

<sup>1</sup>Remove jumper from I1-W to J4-W, on rear of input file.

<sup>2</sup>Remove jumper from I5-W to J8-W, on rear of input file.

<sup>3</sup>Remove jumper from J1-W to I4-W, on rear of input file.

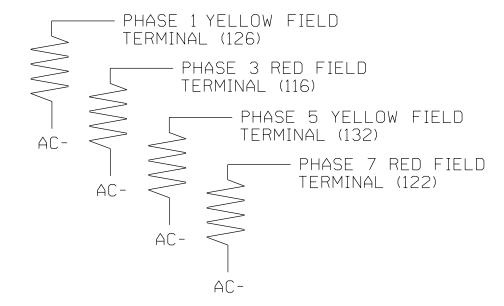
⁴Remove jumper from J5-W to I8-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L FILE J-SLOT 2 **LOWER** 

#### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

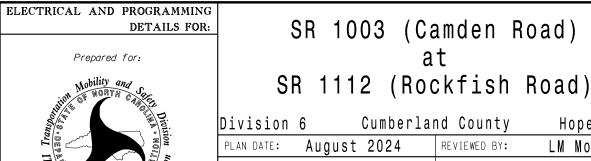
ACCEPTABLE VALUES VALUE (ohms) WATTAGE 1.5K - 1.9K | 25W (min) 2.0K - 3.0K 10W (min)

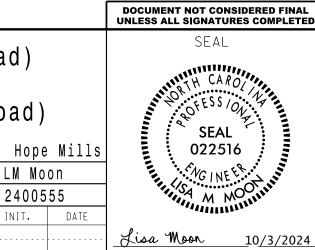


FS = FLASH SENSE ST = STOP TIME

**Plans Prepared By:** 

#### Electrical Detail - Sheet 1 of 2





SIG. INVENTORY NO. 06-0470

Sig-8

U-3422A

9 | 10 | 17 | 11 | 12 | 18

A114 A101

A115 A102

A116 A103

8 OL1 OL2 SPARE OL3 OL4 SPARE

S11 S12 AUX AUX AUX AUX AUX S5 S6

A121 A124

A122 A125

A123 A126

LM Moon PREPARED BY: MR Stanley/DJW | RKA PROJ. NO: 2400555 REVISIONS INIT. DATE

#### MAXTIME OVERLAP PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

#### Overlap Plan 1

Overlap	1	2	3	4
Type	FYA 4 - Section			
Included Phases	2	4	6	8
Modifier Phases	1	3	5	7
Modifier Overlaps	-	-	<del>-</del>	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

#### FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

#### **OUTPUT CHANNEL CONFIGURATION**

Front Panel

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channel Configuration

#### **Channel Configuration**

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channel
1	Phase Vehicle	1		Χ	Х	1
2	Phase Vehicle	2		Х		2
3	Phase Vehicle	3		Х	Х	3
4	Phase Vehicle	4		Х		4
5	Phase Vehicle	5		Х		5
6	Phase Vehicle	6		Х	Х	6
7	Phase Vehicle	7		Х		7
8	Phase Vehicle	8		Х	Х	8
9	Overlap	1		Х	Х	9
10	Overlap	2		Х	Х	10
11	Overlap	3		Х		11
12	Overlap	4		Х		12
13	Phase Ped	2				13
14	Phase Ped	4				14
15	Phase Ped	6				15
16	Phase Ped	8				16
17	Overlap	5		Х	Х	17
18	Overlap	6		Х		18

#### MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Unit

Web Interface

Home >Controller >Unit

Modify parameters as shown below and save changes.

Start Up Parameters

StartUp Clearance Hold

Unit Flash Parameters All Red Flash Exit Time

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0470 DESIGNED: Aug 2024 SEALED: REVISED: N/A

Electrical Detail - Sheet 2 of 2

Electrical and Programming Details For:

SR 1003 (Camden Road) SR 1112 (Rockfish Road)

Division 6 Cumberland County Hope Mills PLAN DATE: August 2024 REVIEWED BY: LM Moon 2400555 PREPARED BY: MR Stanley/DJW | REVIEWED BY: REVISIONS INIT. DATE

022516

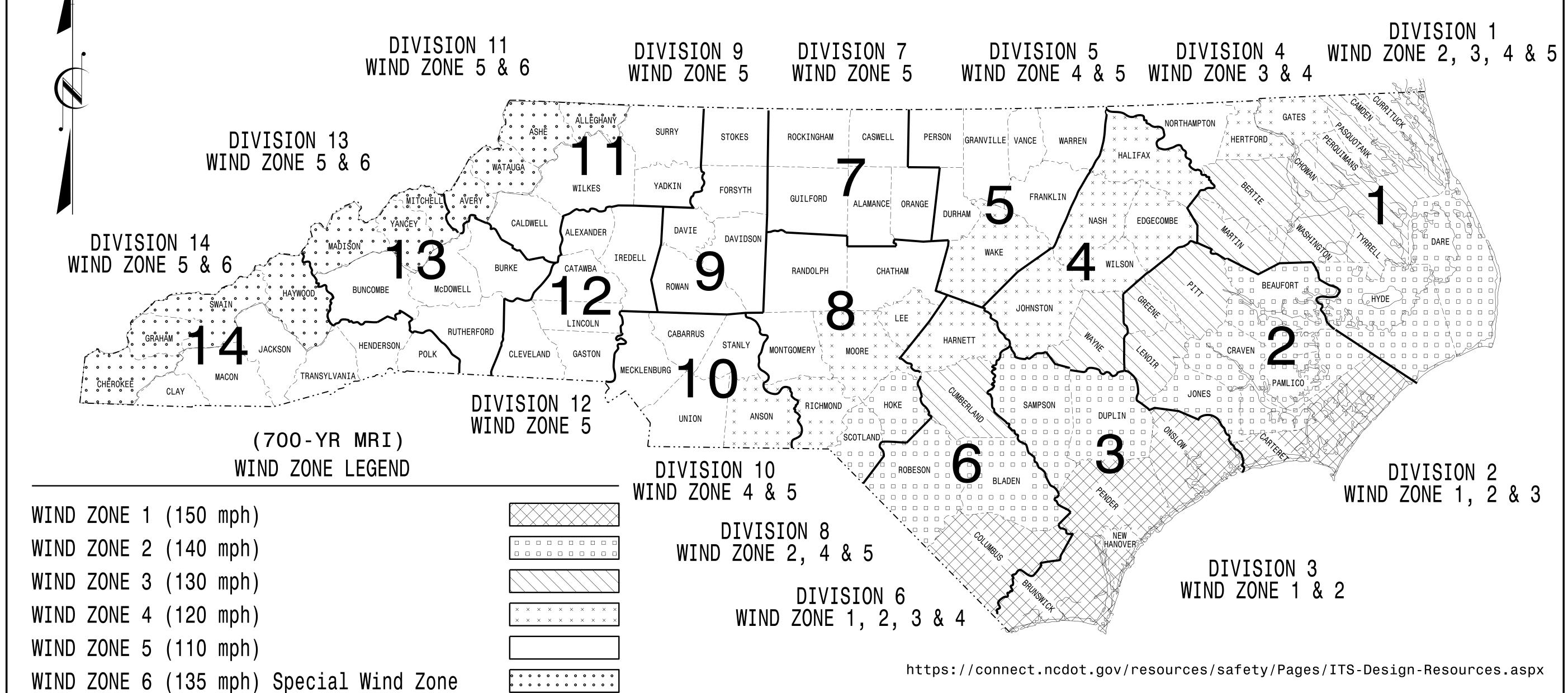
DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

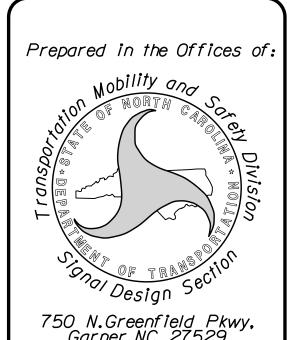
**Plans Prepared By:** 

SIG. INVENTORY NO. () 6 - () 4 7 ()

STATE OF NORTH CAROLINA DIVISION OF HIGHWAYS PROJECT I.D. NO. SHEET NO. Sig.M1A

STANDARD DRAWINGS FOR ALL METAL POLES (LRFD)





Designed in conformance
with the latest
2020 Interim to the
1st Edition 2015

## AASHTO LRFD

Standard Specifications for Highway Signs, Luminaires, and Traffic Signals

# DRAWING INDEX OF PLANS NUMBER DESCRIPTION

Sig. M 9

Statewide Wind Zone Map (700-yr MRI) Sig. M 1A Statewide Wind Zone Map (10-yr MRI) Sig. M 1B Typical Fabrication Details-All Metal Poles **Sig.** M 2 **Sig.** *M* 3 Typical Fabrication Details-Strain Poles Typical Fabrication Details-Mast Arm Poles Sig. M 4 Typical Fabrication Details-Mast Arm Connection **Sig.** *M* 5 Typical Fabrication Details-Strain Pole Attachments Sig. M 6 Construction Details-Foundations Sig. M Standard Strain Pole Foundation-All Soil Conditions Sig. M 8

Typical Fabrication Details-CCTV Camera Poles

MOBILITY AND SAFETY DIVISION – TRANSPORTATION SYSTEMS MANAGEMENT AND OPERATIONS UNIT

D.Y. ISHAK – STATE SIGNALS ENGINEER

K. DURIGON, P.E. – ITS AND SIGNALS STRUCTURAL ENGINEER

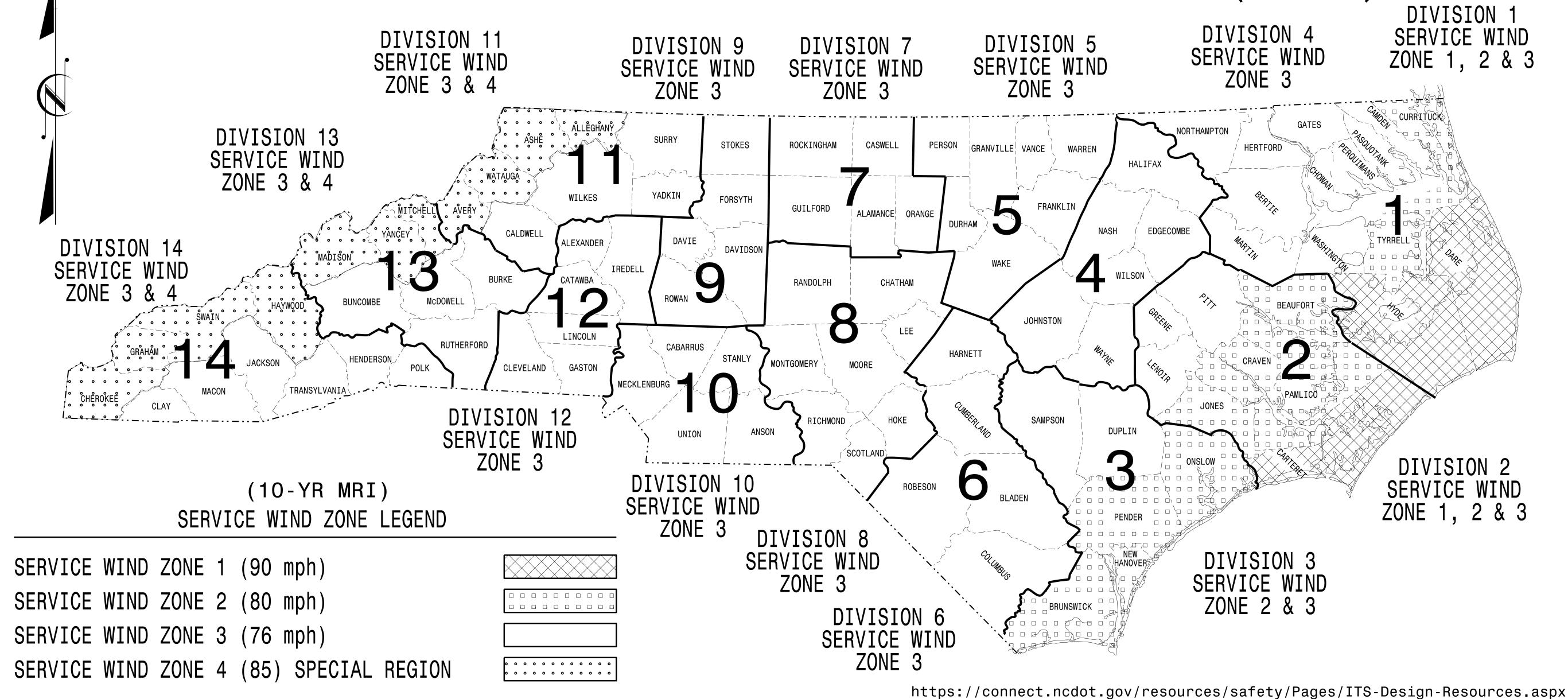
B. WALKER, P.E. – ITS AND SIGNALS STRUCTURAL ENGINEER

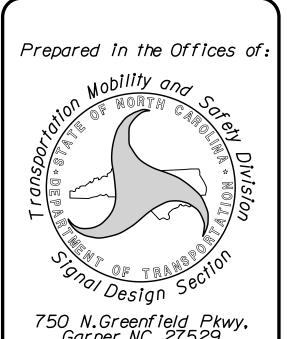


STATE OF NORTH CAROLINA DIVISION OF HIGHWAYS

PROJECT I.D. NO. SHEET NO Sig.M1B

STANDARD DRAWINGS FOR ALL METAL POLES (LRFD)





Designed in conformance with the latest 2020 Interim to the 1st Edition 2015

## **AASHTO LRFD**

Standard Specifications for Highway Signs, Luminaires, and Traffic Signals

#### INDEX OF PLANS **DRAWING** NUMBER DECODIDATON

NUMBER		DESCRIPTION
Sig. M	<i>1A</i>	Statewide Wind Zone Map (700-yr MRI)
Sig. M	1 <b>B</b>	Statewide Wind Zone Map (10-yr MRI)
Sig. M	2	Typical Fabrication Details-All Metal Poles
Sig. M	3	Typical Fabrication Details-Strain Poles
Sig. M	4	Typical Fabrication Details-Mast Arm Poles
Sig. M	5	Typical Fabrication Details-Mast Arm Connection
Sig. M	6	Typical Fabrication Details-Strain Pole Attachments
Sig. M	7	Construction Details-Foundations
Sig. M	8	Standard Strain Pole Foundation-All Soil Conditions
Sig. M	9	Typical Fabrication Details-CCTV Camera Poles

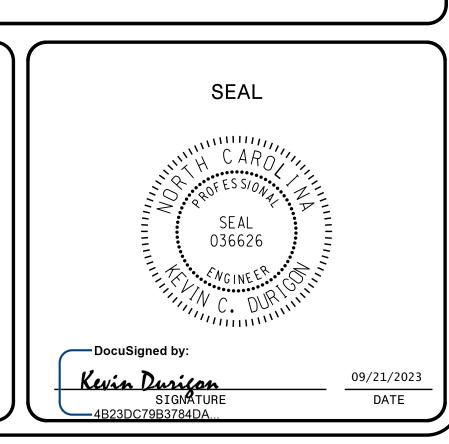
#### **NCDOT CONTACTS:**

MOBILITY AND SAFETY DIVISION -TRANSPORTATION SYSTEMS MANAGEMENT AND OPERATIONS UNIT

D.Y. ISHAK – STATE SIGNALS ENGINEER

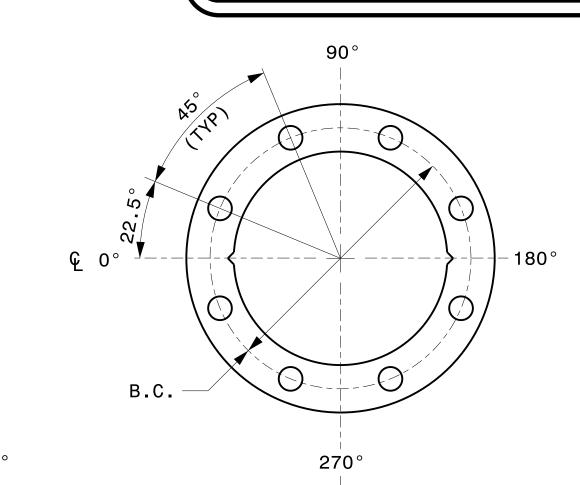
K. DURIGON, P.E. – ITS AND SIGNALS STRUCTURAL ENGINEER

B. WALKER, P.E. – ITS AND SIGNALS STRUCTURAL ENGINEER



B.C.

4 BOLT PATTERN



8 BOLT PATTERN

12 BOLT PATTERN

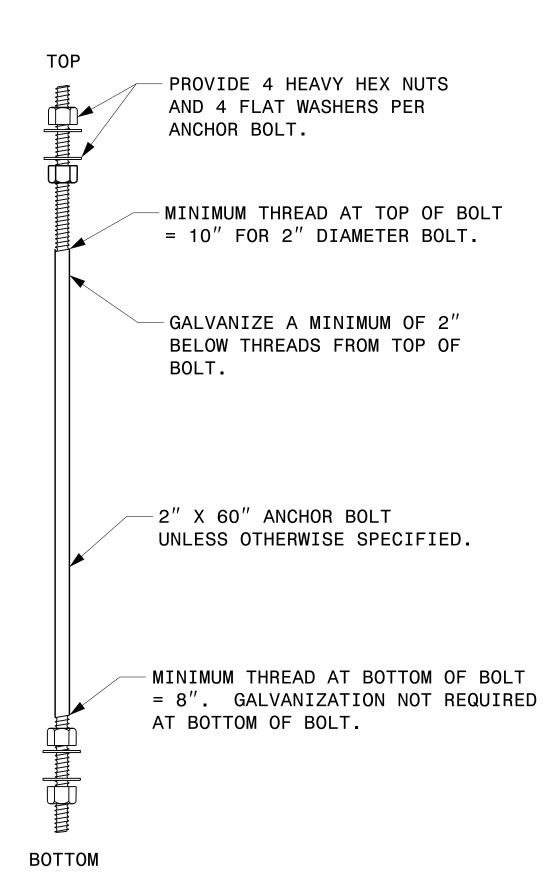
180°

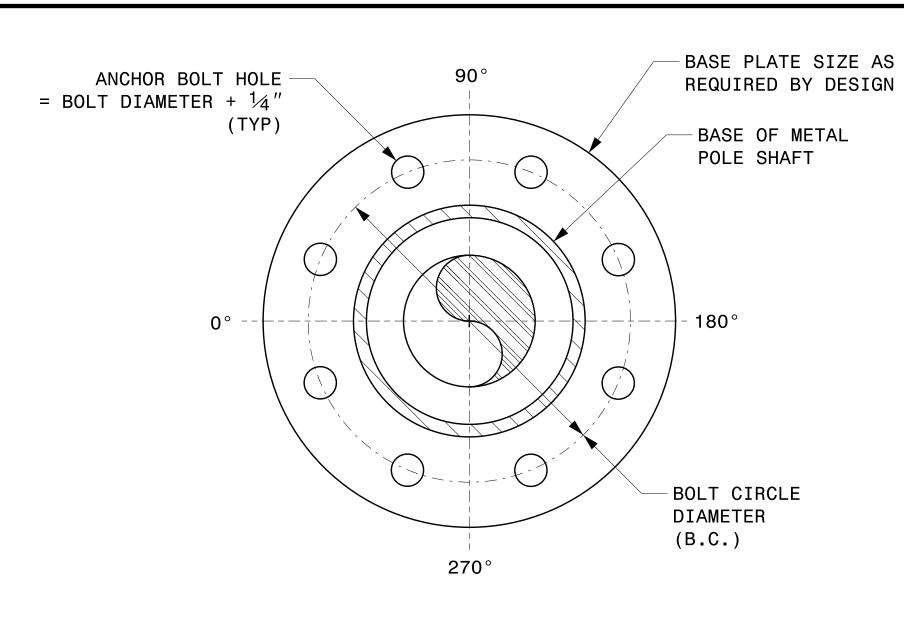
21/4" DIAMETER HOLE FOR 2" DIAMETER ANCHOR BOLT

B.C.

CONSTRUCT TEMPLATES AND PLATES FROM 1/4" (MIN) THICK STEEL. GALVANIZING IS NOT REQUIRED.

## BASE PLATE TEMPLATE AND ANCHOR BOLT LOCK PLATE DETAILS



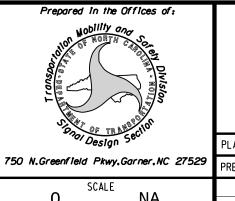


-PLATE WIDTH = 4" (MIN)

(TYPICAL FOR ALL PLATES)

NOTE: BASE PLATE MAY BE CIRCULAR, OCTAGONAL, SQUARE OR RECTANGULAR IN SHAPE.

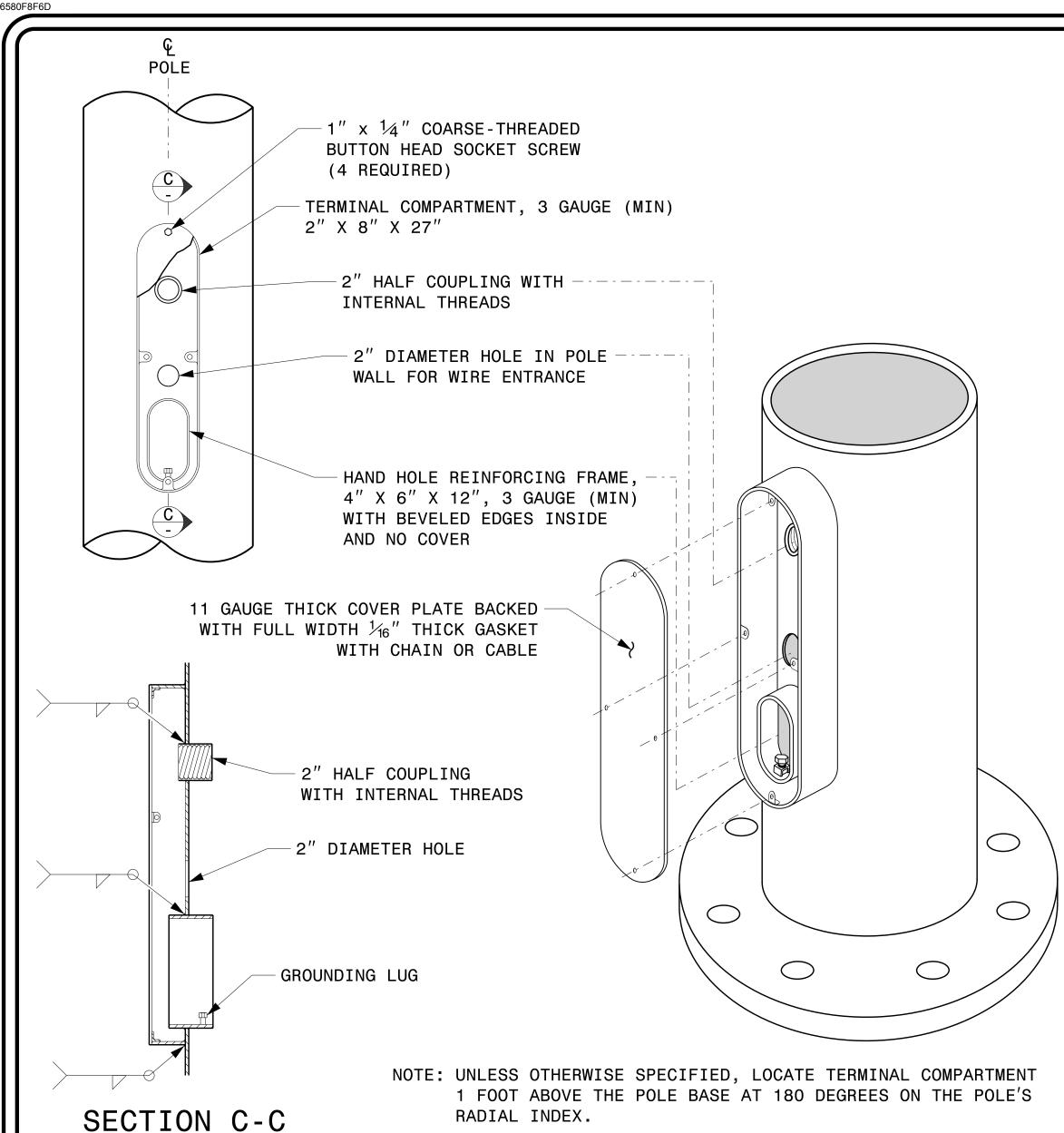
#### TYPICAL BASE PLATE DETAIL



Typical Fabrication Details All Metal Poles

PLAN DATE: SEPTEMBER 2023 DESIGNED BY: C.F.ANDREWS PREPARED BY: K.C. DURIGON REVIEWED BY: D.C. SARKAR

SEAL DocuSigned by: Kevin Durigan 09/21/2023



#### TERMINAL COMPARTMENT DETAIL

MFG. DATE: MM/YY SHAFT D/T/L/Y ----/----ARM-A D/T/L/Y ----/----\_\_\_\_/\_\_\_/\_\_\_/ ARM-B D/T/L/Y ----/----\_\_\_\_/\_\_\_/\_\_\_/ A.B. DIA./B.C./L/Y \_\_\_\_/\_\_\_ NCDOT SIG. INV. NO. \_\_\_\_\_ NCDOT POLE NO. \_\_\_\_\_ SHAFT I.D. TAG (PROVIDE ON SHAFT OF STRAIN POLES

MFG. DATE: MM/YY SECTION D/T/L/Y ----/---NCDOT SIG. INV. NO. \_\_\_\_\_ NCDOT POLE NO. \_\_\_\_\_

ARM I.D. TAG (PROVIDE ON EACH SECTION OF `A MULTI-SECTION MAST ARM)

AND MAST ARM POLE SHAFT)

- 1. D = DIAMETER, T = THICKNESS, L = LENGTH, Y = YIELD STRENGTH
- 2. A.B. = ANCHOR BOLT

NOTES:

- 3. B.C. = BOLT CIRCLE OF ANCHOR BOLTS
- 4. IF STANDARD DESIGN, INCLUDE CASE NUMBER IN ADDITION TO
- POLE NUMBER ON "NCDOT POLE NO." LINE.
- 5. SIGNAL INV. NUMBER AND POLE I.D. NUMBER. SEE DRAWING M3 AND M4 FOR MOUNTING POSITIONS OF I.D. TAGS.

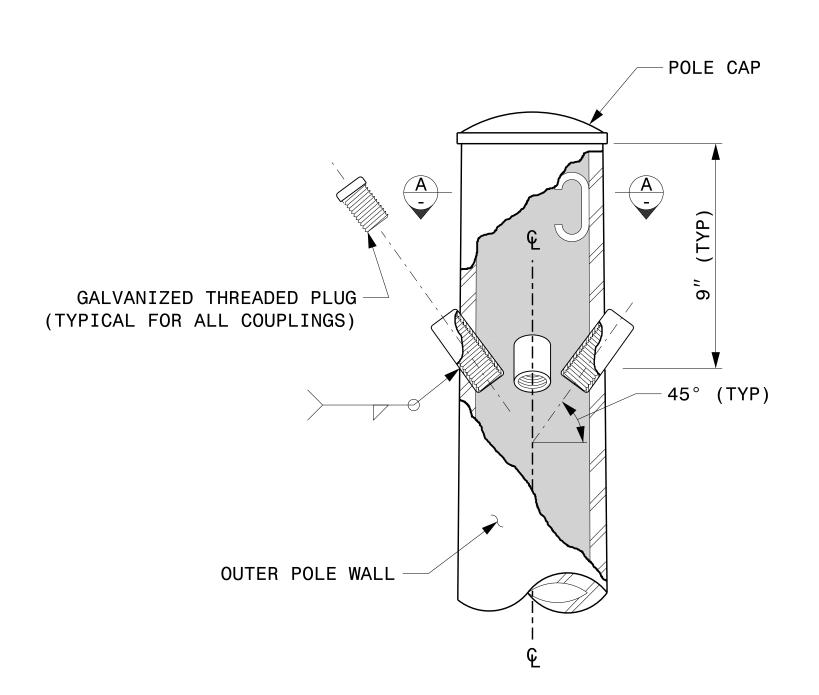
IDENTIFICATION TAG DETAILS

NONE

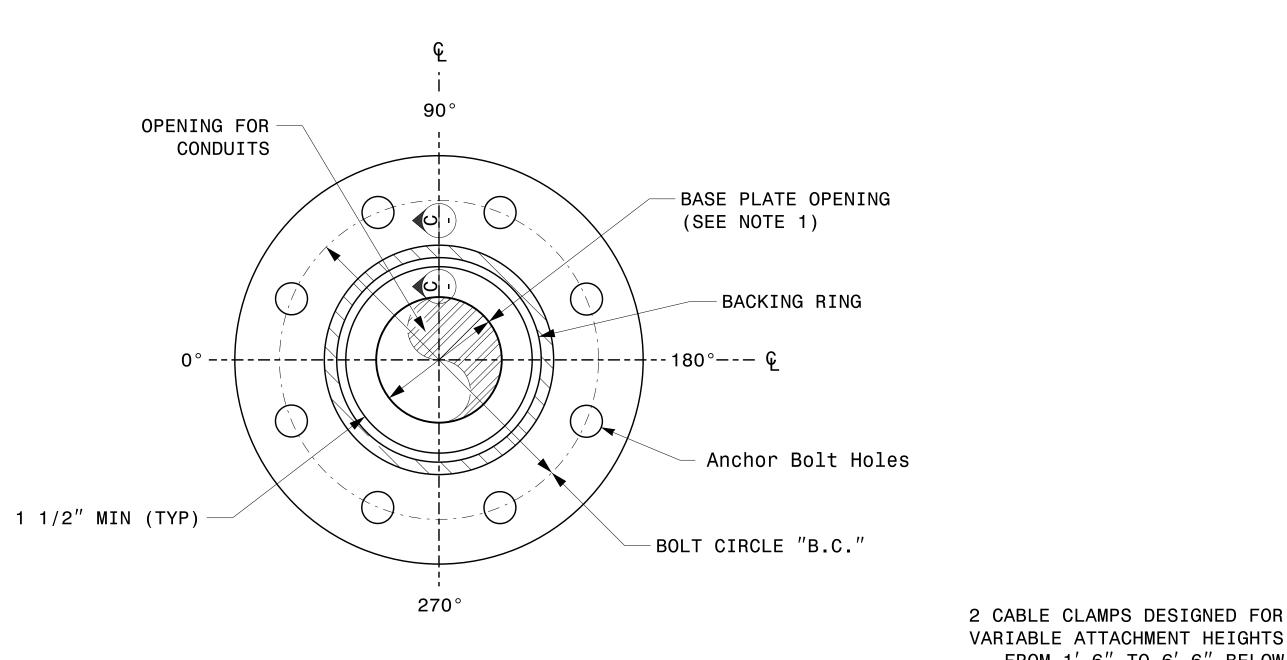
ANCHOR BOLT DETAIL

1. OPENING IN POLE BASE PLATE SHALL BE EQUAL TO POLE BASE INSIDE DIAMETER MINUS  $3\frac{1}{2}''$ BUT SHALL NOT BE LESS THAN  $8\frac{1}{2}$ ".

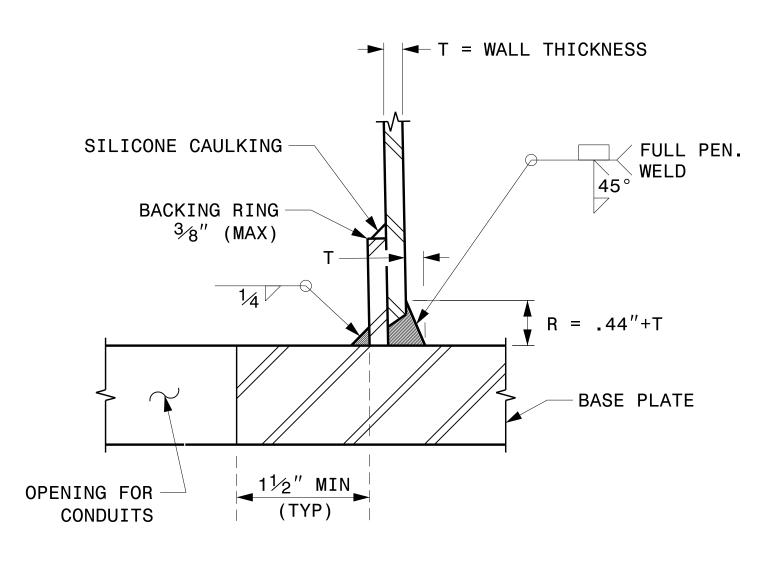
PROJECT I.D. NO. SHEET NO Sig.M3



CABLE ENTRANCES AT TOP OF POLE

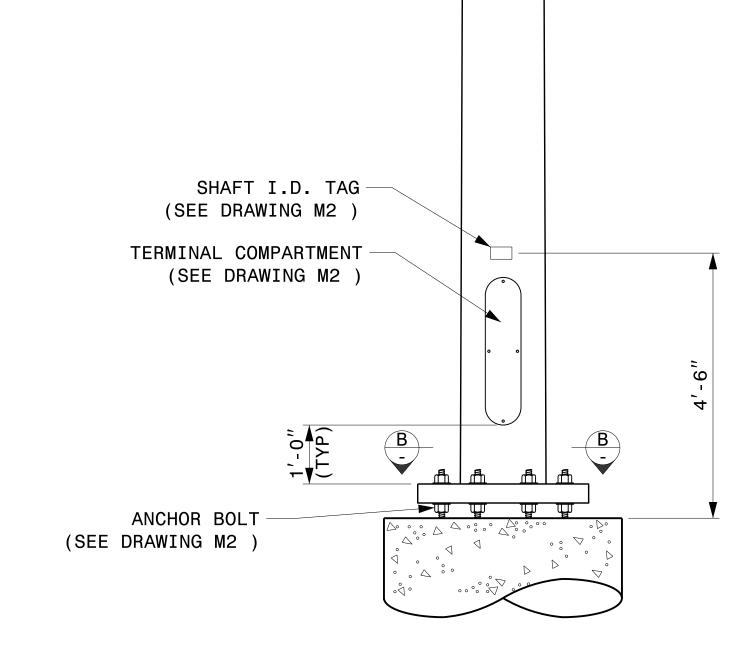


SECTION B-B POLE BASE PLATE DETAILS (8 AND 12 BOLT PATTERN)



SECTION C-C (POLE ATTACHMENT TO BASE PLATE)

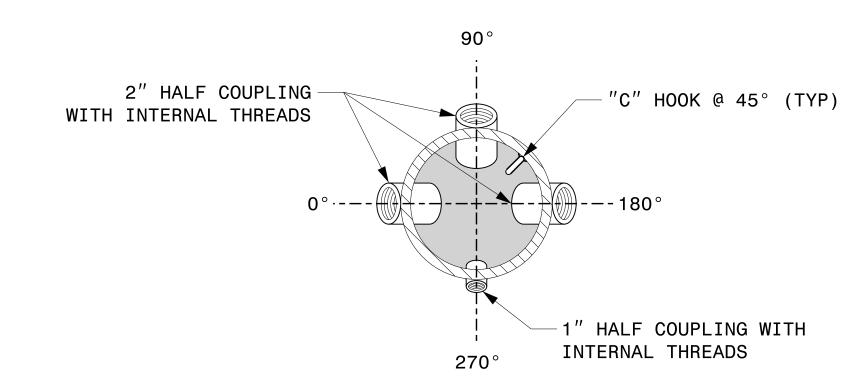
FULL-PENETRATION GROOVE WELD DETAIL



FROM 1'-6" TO 6'-6" BELOW

THE TOP OF THE POLE

MONOTUBE STRAIN POLE



RADIAL ORIENTATION OF FACTORY INSTALLED ACCESSORIES AT TOP OF POLE

SECTION A-A

SEAL Typical Fabrication Details Strain Poles PLAN DATE: SEPTEMBER 2023 DESIGNED BY: K.C. DURIGON PREPARED BY: K.C. DURIGON REVIEWED BY: D.C. SARKAR Kevin Durison 09/21/2023 DATE

09/21/2023 DATE

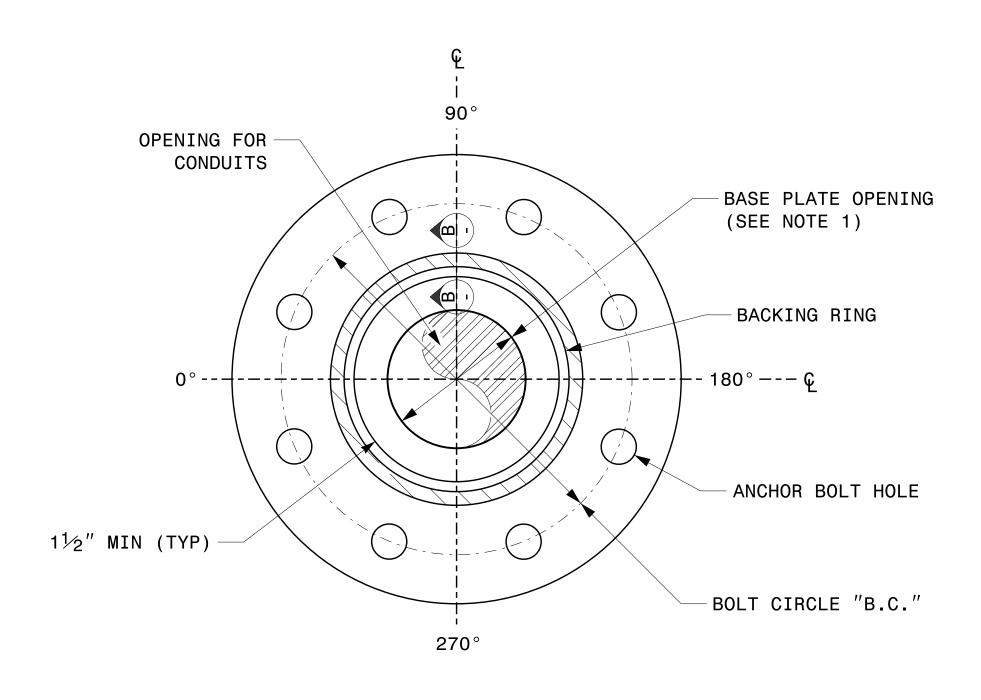
SHEET NO

Sig.M4

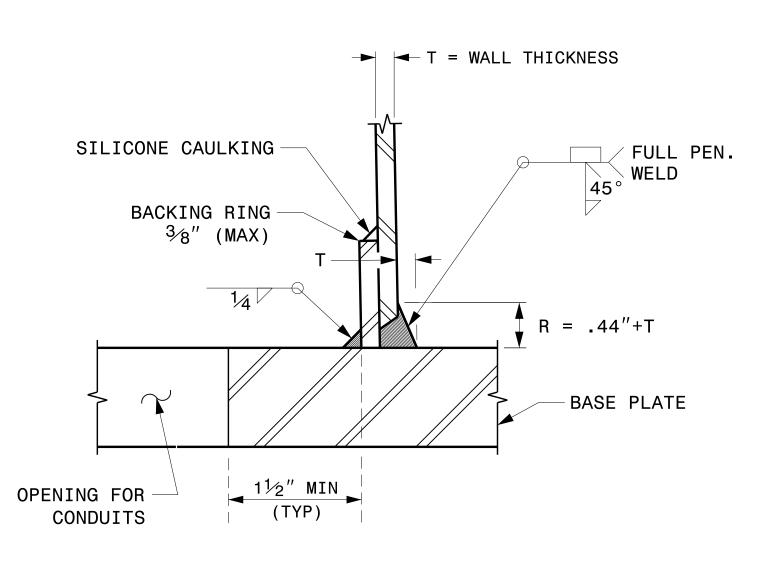
PROJECT I.D. NO.

NOTE:

1. OPENING IN POLE BASE PLATE SHALL BE EQUAL TO POLE BASE INSIDE DIAMETER MINUS  $3\frac{1}{2}$ " BUT SHALL NOT BE LESS THAN  $8\frac{1}{2}$ ".

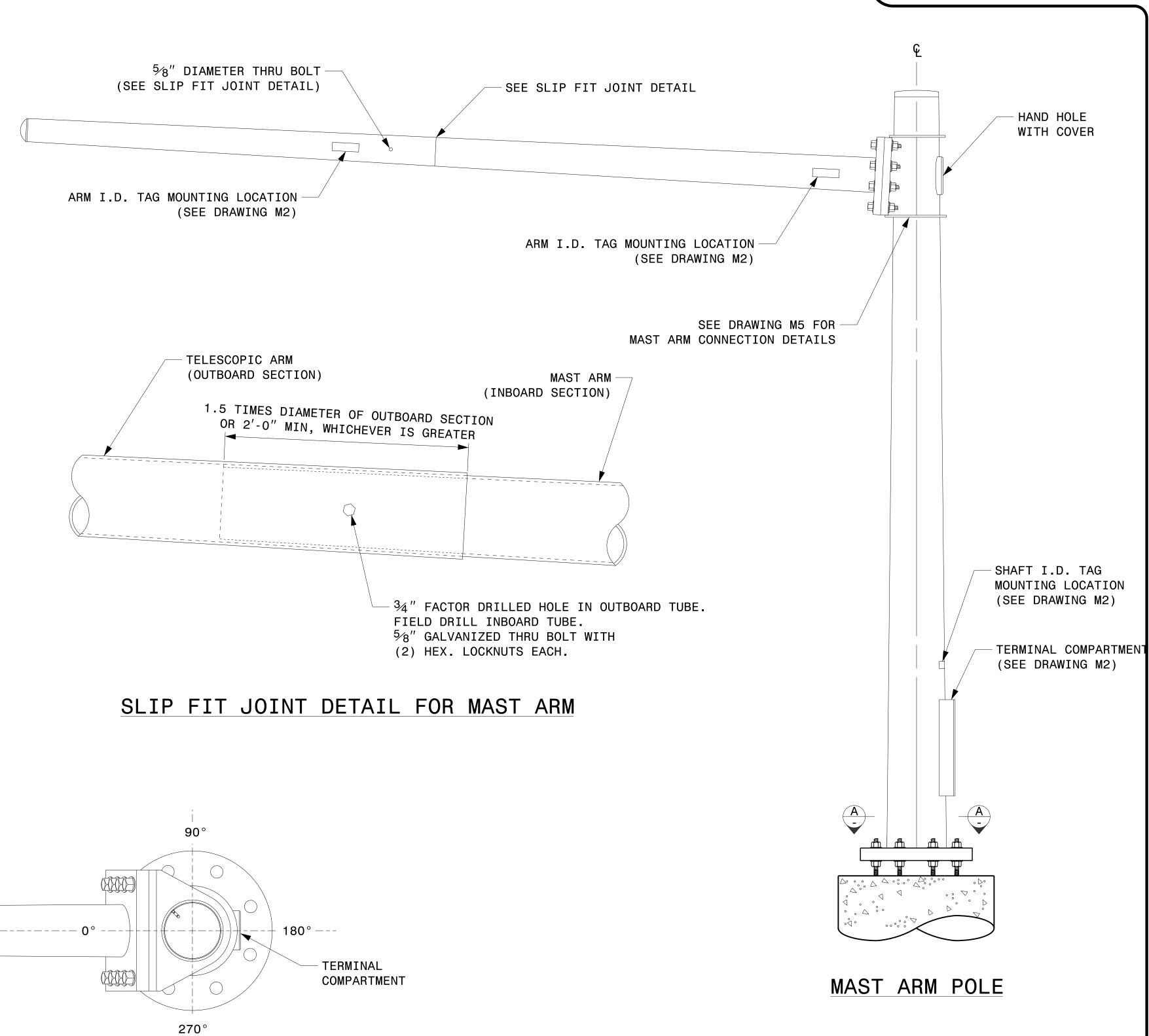


SECTION A-A
POLE BASE PLATE DETAILS



SECTION B-B
(POLE ATTACHMENT TO BASE PLATE)

FULL-PENETRATION
GROOVE WELD DETAIL



Typical Fabrication Details

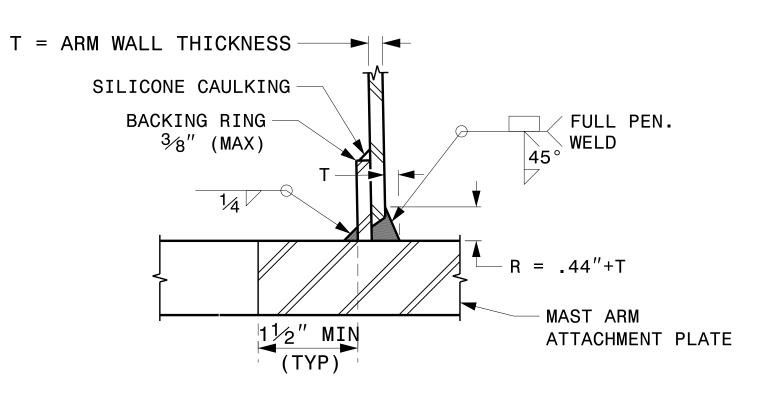
Mast Arm Poles

PLAN DATE: SEPTEMBER 2023 DESIGNED BY: K.C. DURIGON PREPARED BY: K.C. DURIGON REVIEWED BY: D.C. SARKAR

Kevin Durigan

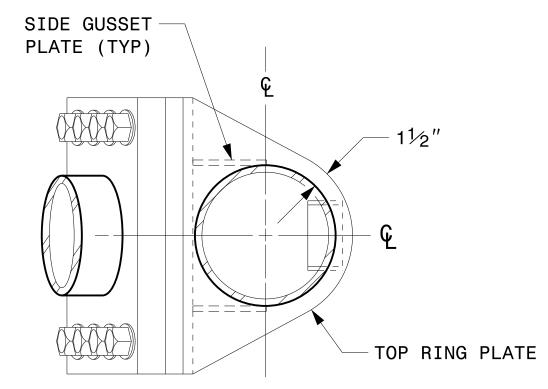
MAST ARM RADIAL ORIENTATION

# WELDED RING STIFFENED MAST ARM CONNECTION

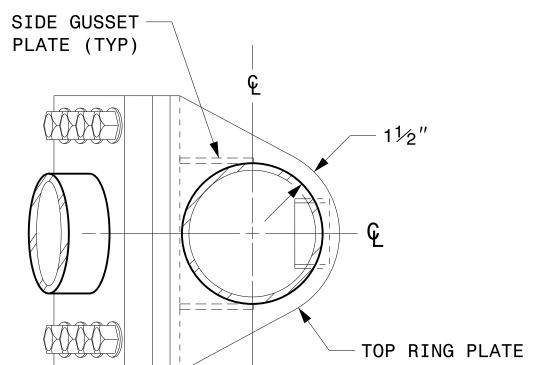


SECTION B-B FULL-PENETRATION GROOVE WELD DETAIL

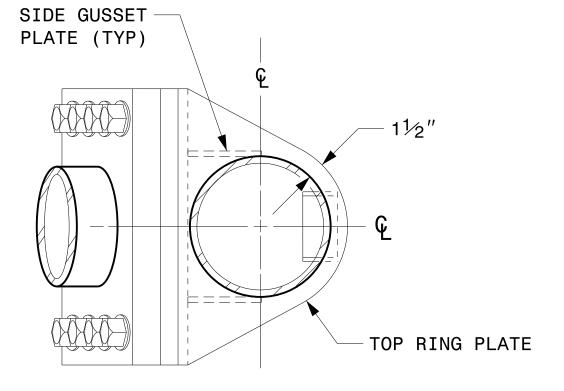
BACKING RING

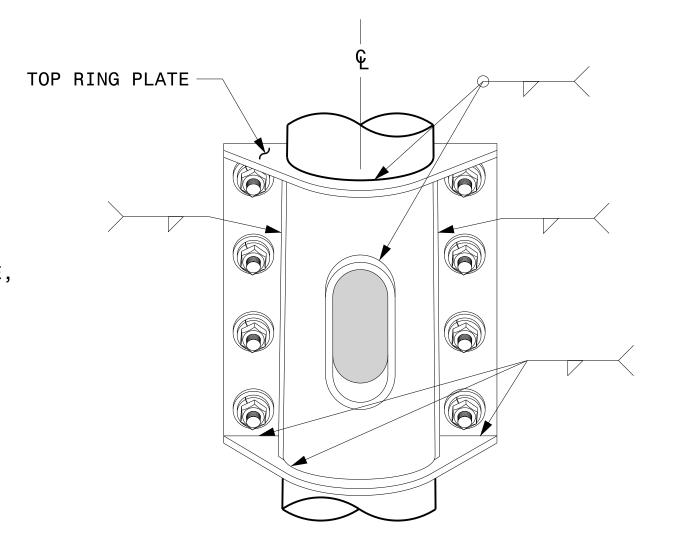


PLAN VIEW









1. PROVIDE A PERMANENT MEANS OF IDENTIFICATION ABOVE THE MAST ARM TO

PLATES, FASTENERS, AND WELDS SHOWN UNLESS THEY ARE ALREADY SPECIFIED.

3. FABRICATOR IS RESPONSIBLE FOR PROVIDING APPROPRIATE HOLES AT DRAINAGE

4. FOR MINIMUM EDGE DISTANCE AND NOMINAL BOLT HOLE SIZE, FOLLOW THE LATEST

5. PROVIDE UPPER HANDHOLE AS NECESSARY WHEN SHAFT EXTENSIONS ARE REQUIRED

FOR LUMINAIRE ARMS OR CAMERA. FOR POLES WITHOUT LUMINAIRES/CAMERA,

6. ALLOWABLE RANGE OF FLANGE TILT ANGLE WILL VARY FROM 0° TO AS REQUIRED.

INDICATE PROPER ATTACHMENT ORIENTATION OF THE MAST ARM.

POINTS TO DRAIN GALVANIZING MATERIALS.

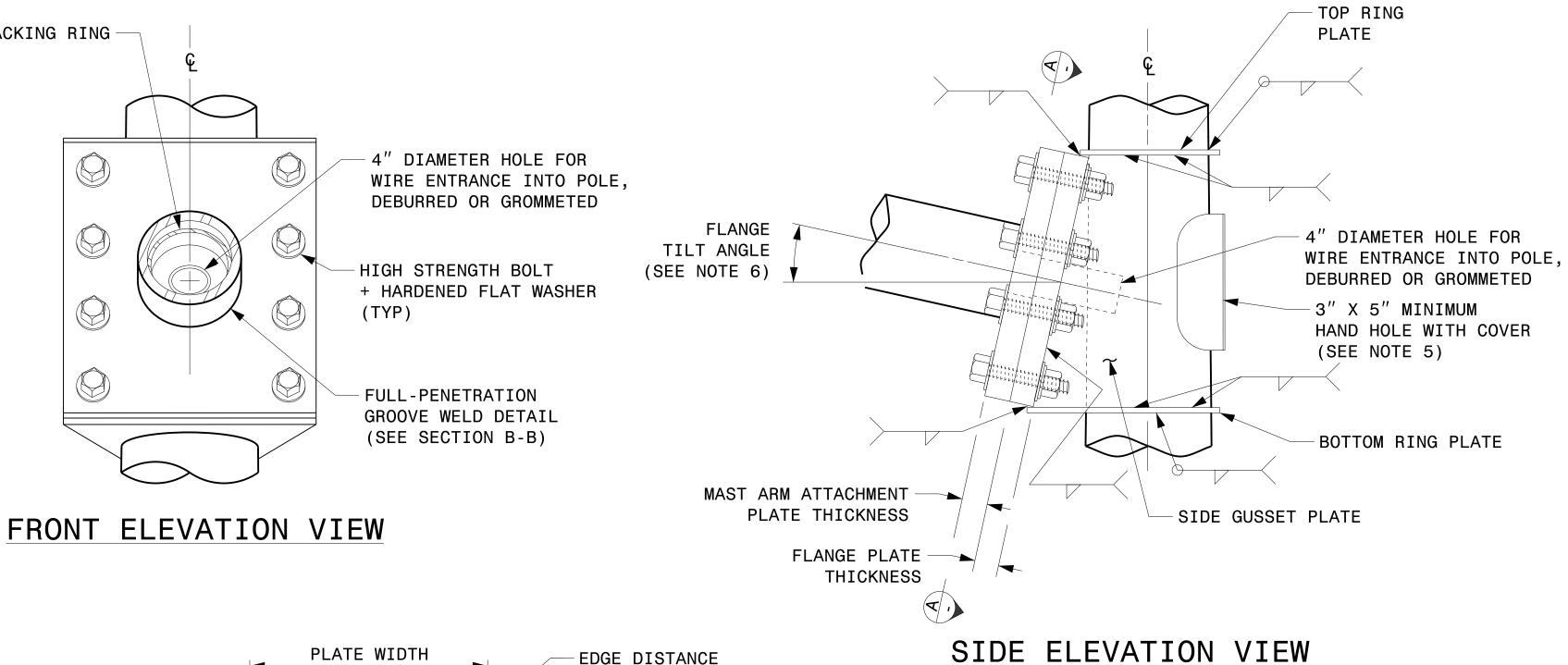
WIRING CAN BE DONE THROUGH THE TOP OF POLE.

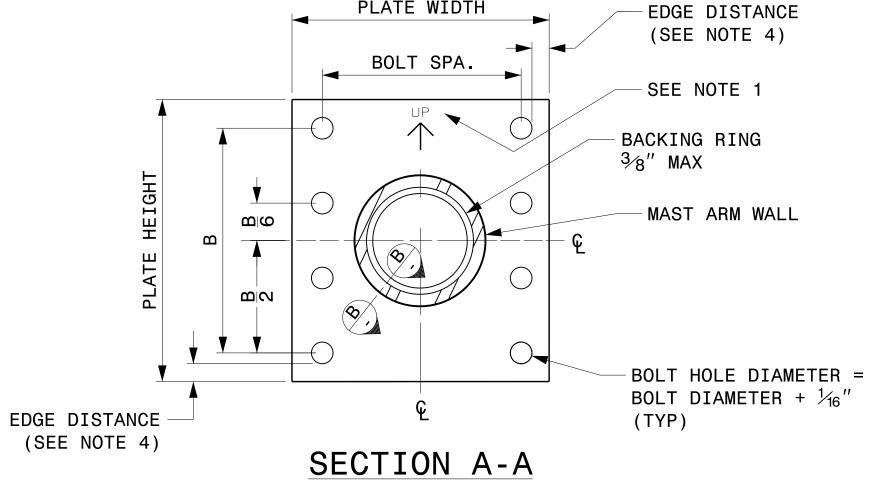
AISC STEEL CONSTRUCTION MANUAL.

2. DESIGNER WILL DETERMINE THE SIZE OF ALL STRUCTURAL COMPONENTS,

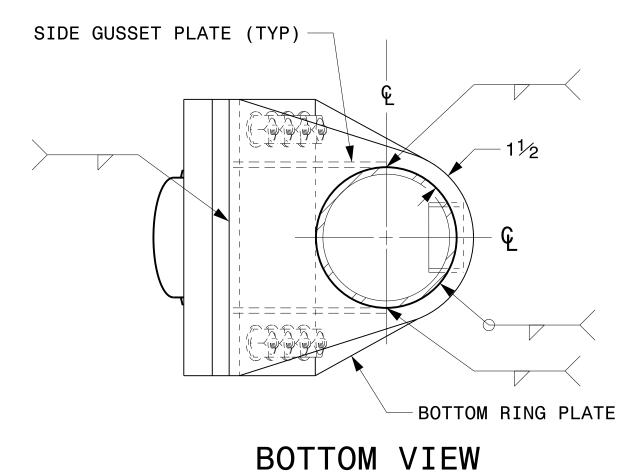
NOTES:

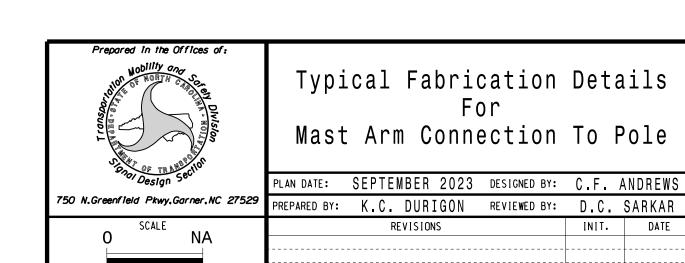
BACK ELEVATION VIEW

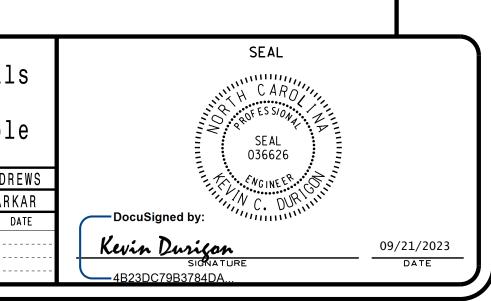




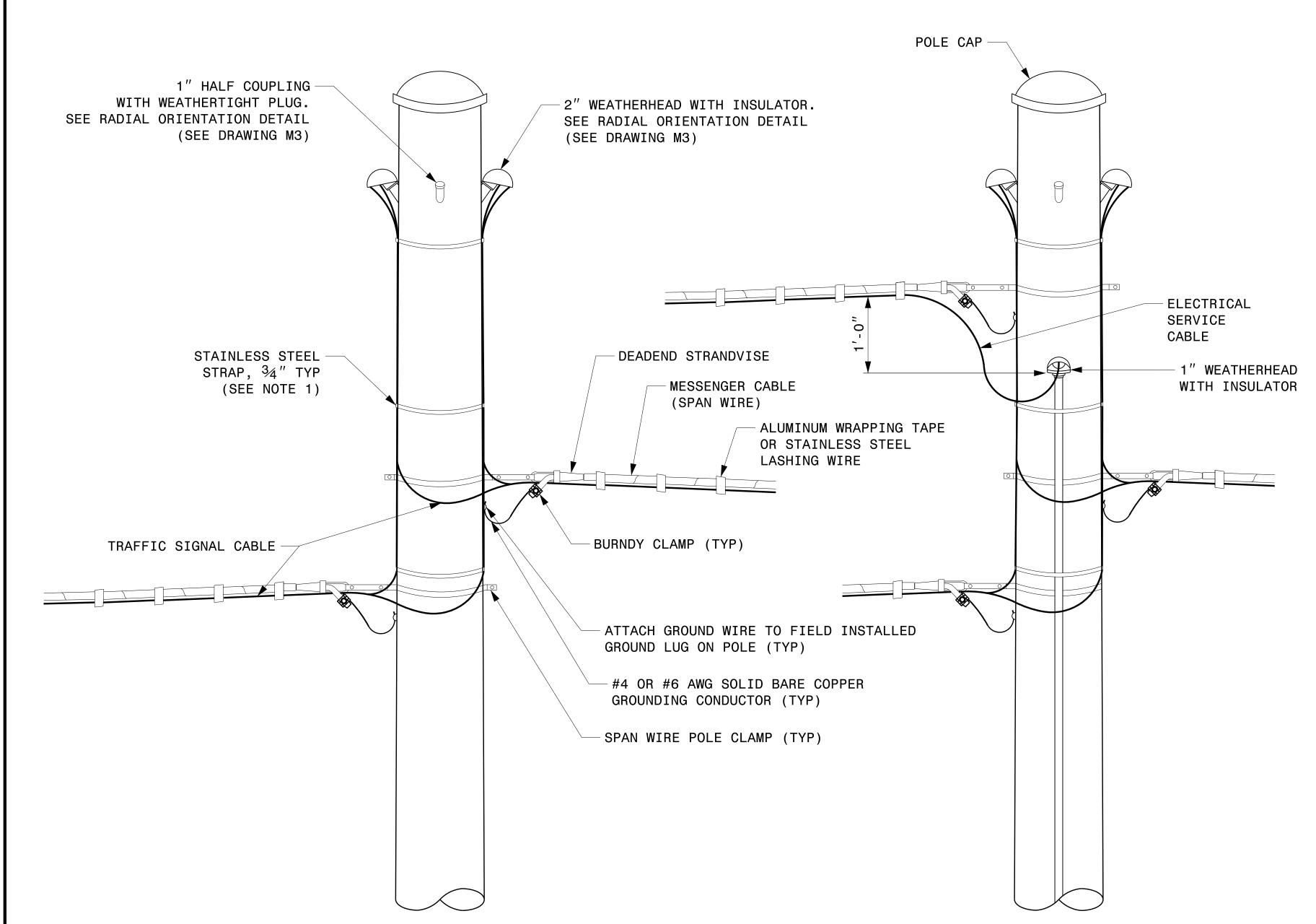
MAST ARM ATTACHMENT PLATE







PROJECT I.D. NO.

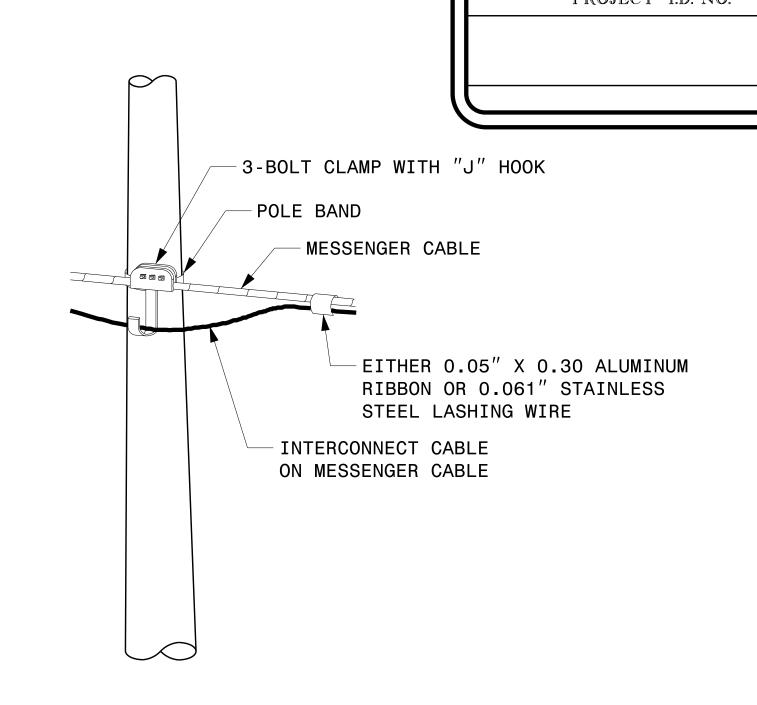


#### STRAIN POLE ATTACHMENTS

Christopher R. Silver
3002CC612BF34F2...

#### NOTES:

- 1. STRAP ALL SIGNAL CABLES TO THE SIDE OF THE POLE WITH  $^3\!4''$  STAINLESS STEEL STRAPS WHEN THE DISTANCE BETWEEN SPAN WIRE ATTACHMENT CLAMP AND WEATHERHEADS EXCEEDS  $^3'$ - $^0'$ .
- 2. PROVIDE MINIMUM TWO SPAN WIRE POLE CLAMPS PER POLE.
- 3. IT IS PROHIBITED TO ATTACH TWO SPAN WIRES AT ONE POLE CLAMP.
- 4. FOR GENERAL REQUIREMENTS, REFER TO NCDOT STANDARD SPECIFICATIONS FOR ROADWAY AND STRUCTURES, JANUARY 2024.

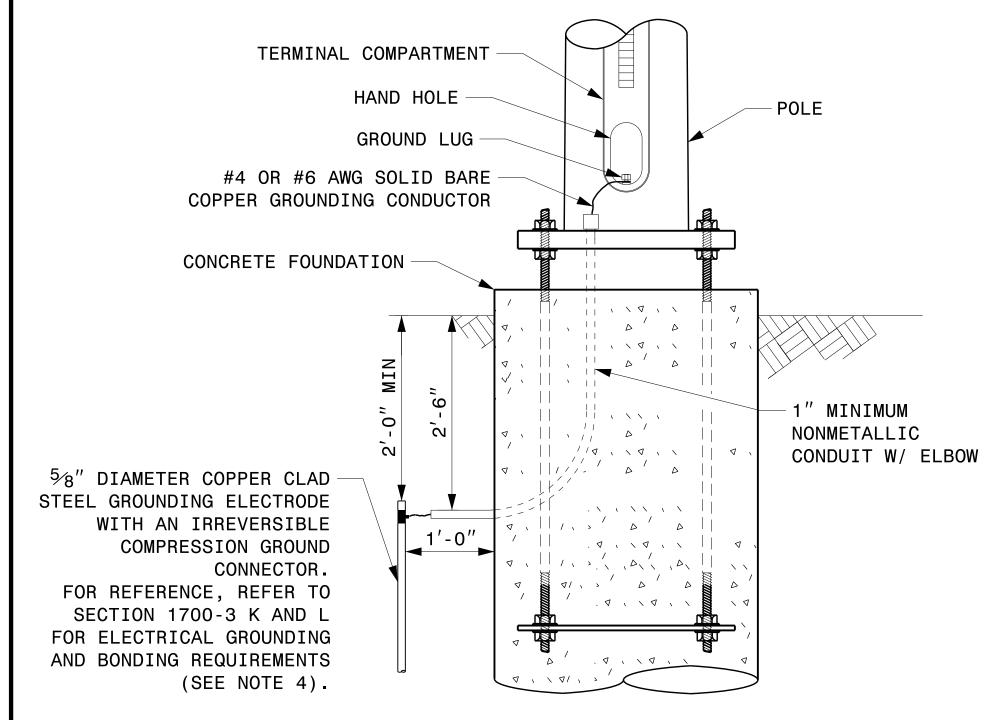


SHEET NO

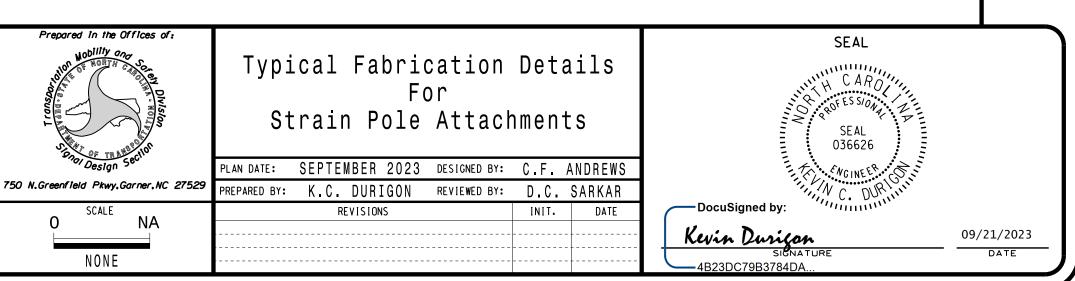
Sig.M6

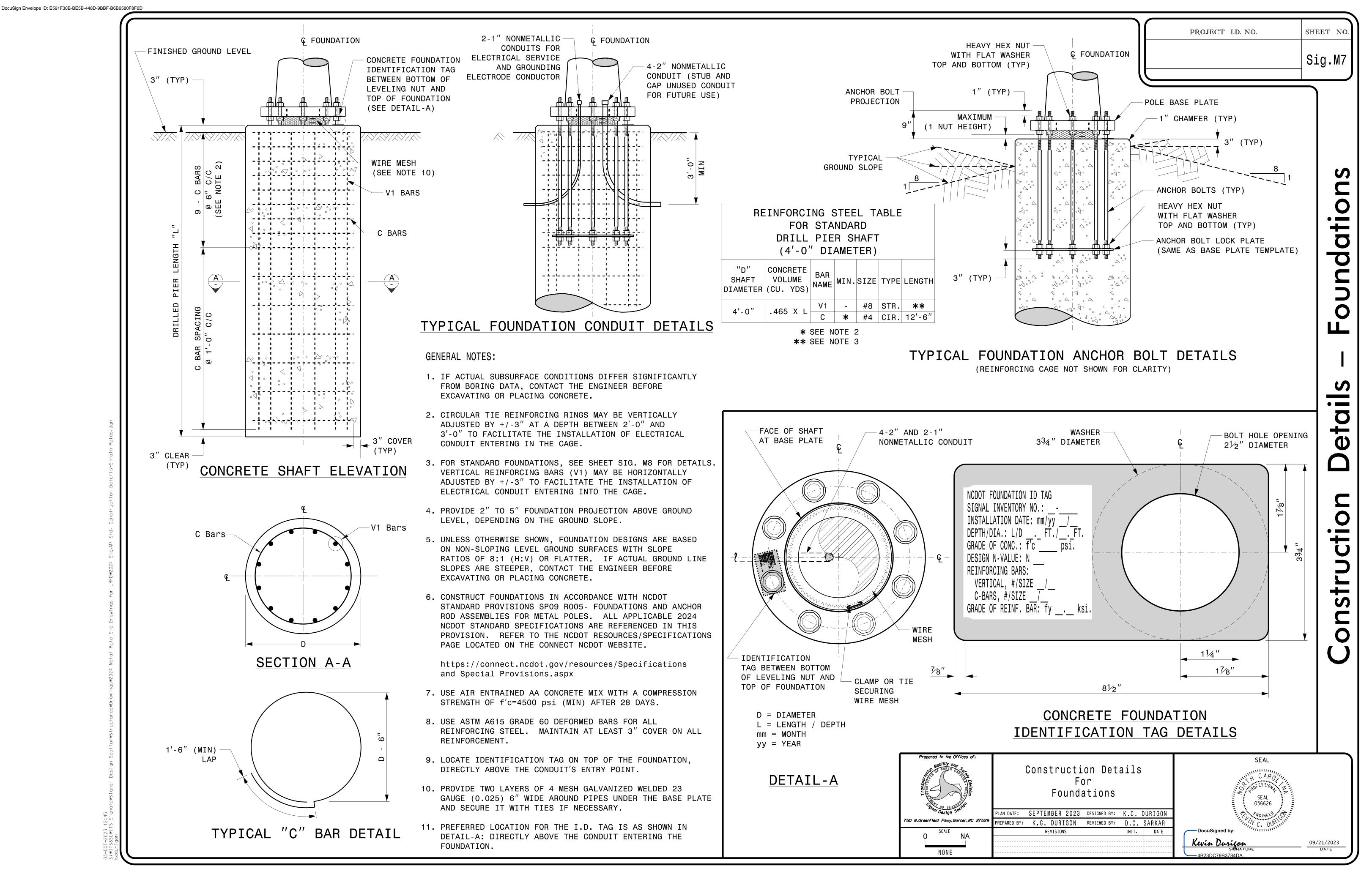
Str

# ATTACHMENT OF CABLE TO INTERMEDIATE METAL POLE



## METAL POLE GROUNDING DETAIL FOR STRAIN POLE AND MAST ARM





Standard

SHEET NO.

#### GENERAL NOTES:

- 1. VALUES SHOWN IN THE "REACTIONS AT THE POLE BASE" COLUMN REPRESENT THE MINIMUM ACCEPTABLE CAPACITY ALLOWED FOR DESIGN USING A COMBINED FORCE RATIO (CFR) OF 1.00.
- 2. USE CHAIRS AND SPACERS TO MAINTAIN PROPER CLEARANCE.
- 3. FOR FOUNDATION, ALWAYS USE AIR-ENTRAINED CONCRETE MIX.

#### FOUNDATION SELECTION:

- 1. PERFORM A STANDARD PENETRATION TEST AT EACH PROPOSED FOUNDATION SITE TO DETERMINE "N" VALUE.
- 2. SELECT THE APPROPRIATE WIND ZONE FROM M1 DRAWING.
- 3. SELECT THE SOIL TYPE (CLAY OR SAND) THAT BEST DESCRIBES THE SOIL CHARACTERISTICS.
- 4. GET THE APPROPRIATE STANDARD POLE CASE NUMBER FROM THE PLANS OR FROM THE ENGINEER.
- 5. SELECT THE APPROPRIATE COLUMN UNDER "STANDARD FOUNDATIONS" BASED ON SOIL TYPE AND "N" VALUE. SELECT THE APPROPRIATE ROW BASED ON THE POLE LOAD CASE.
- 6. THE FOUNDATION DEPTH IS THE VALUE SHOWN IN THE "STANDARD FOUNDATIONS" CATEGORY WHERE THE COLUMN AND THE ROW INTERSECT.
- 7. USE CONSTRUCTION PROCEDURES AND DESIGN METHODS PRESCRIBED BY FHWA-NHI-10-016 MANUAL FOR DRILLED SHAFTS.

OIL	CONDITION

STANDARD STRAIN POLES					STANDARD FOUNDATIONS 48" Diameter Drilled Pier Length (L) – Feet							Reinforcement				
Base Reactions at the Pole Base				Clay			Sand			Longitudinal		Stirrups				
Case No.	Pole Height (Ft.)	Plate	Axial (kip)	Shear (kip)	Moment (ft–kip)	Medium N–Value 4–8		Very Stiff N–Value 16–30		Loose N–Value 4–10	Medium N–Value 11–30	Dense N–Value >30	Bar Size (#)	Quantity (ea.)	Bar Size (#)	Spacing (in.)
S26L1	26	22	2	9	210	19.5	12.5	9	6.5	15.5	14.5	13	8	12	4	12
S26L2	26	23	2	10	240	19.5	12	9	6.5	15.5	14.5	13	8	12	4	12
S26L3	26	25	2	11	260	20.5	12	10	8	16	15	13	8	12	4	12
S30L1	30	22	2	9	230	19	11	9	7	15.5	14	12.5	8	12	4	12
S30L2	30	23	2	10	270	20	12	10	8	16	14.5	13	8	12	4	12
S30L3	30	25	2	11	290	21	12	10	8	17	15	13.5	8	12	4	12
S30H1	30	25	3	13	355	23	13	11	9	18	16.5	14.5	8	12	4	12
S30H2	30	29	3	15	405	25	14	11	9	19	17.5	15.5	8	14	4	12
S30H3	30	29	3	16	430	26	15	12	9	20	18	16	8	14	4	6
S35L1	35	22	3	8	260	19.5	12	10	8	15.5	14.5	13	8	12	4	12
S35L2	35	23	3	10	300	21	12	10	8	16.5	15	13.5	8	12	4	12
S35L3	35	25	3	10	320	21.5	13	10	8	17	15.5	14	8	12	4	12
S35H1	35	25	3	12	390	23.5	14	11	9	18	17	15	8	14	4	12
S35H2	35	29	4	14	460	26	15	12	9	20	18	16	8	14	4	6
S35H3	35	29	4	16	495	28.5	15	13.5	10	21.5	19	17	8	14	4	6

48" DIAMETER FOUNDATION CONCRETE VOLUME (CUBIC YARDS) = (0.465) x DRILLED PIER LENGTH



Standard Strain Pole Foundation for All Soil Conditions

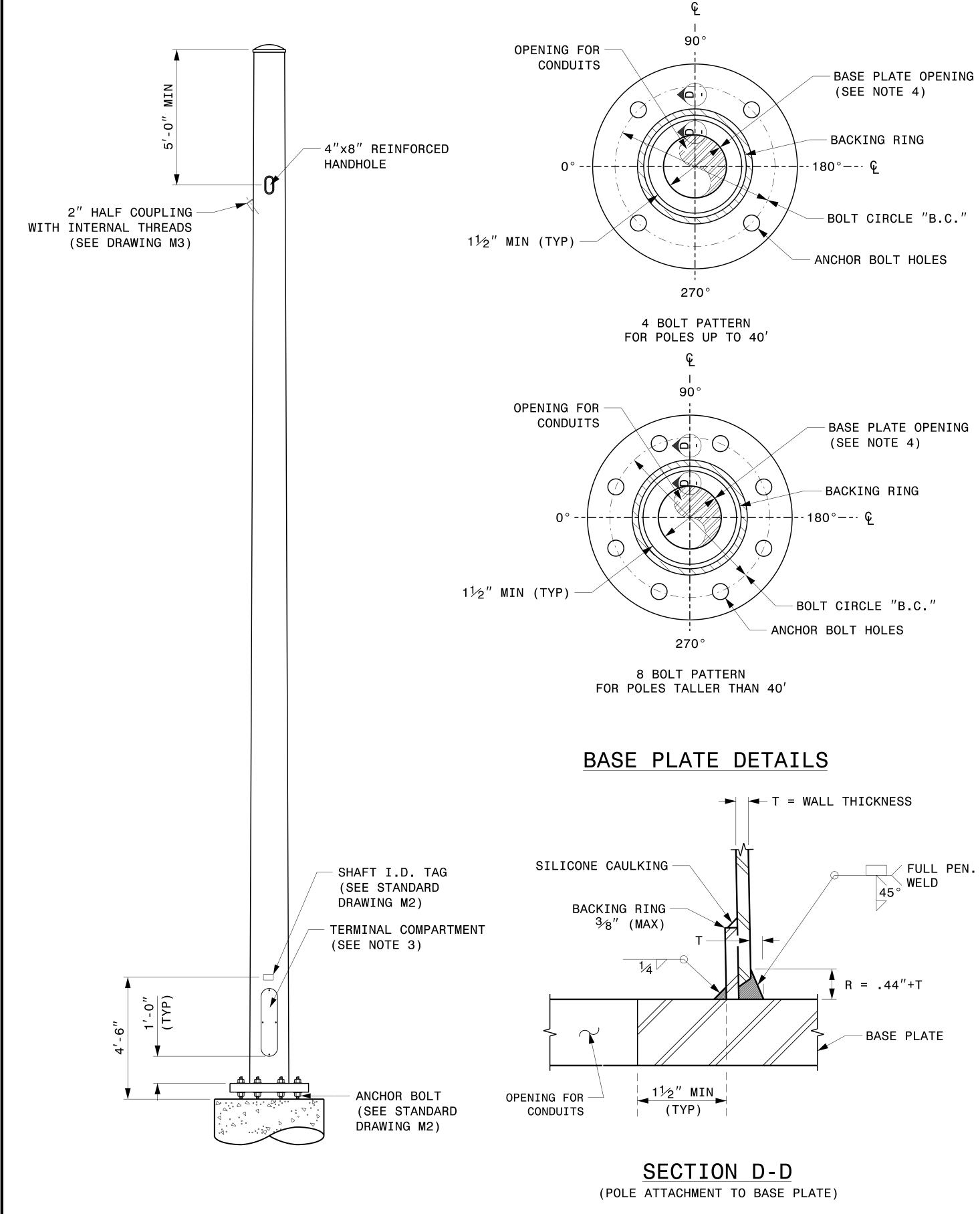
PLAN DATE: SEPTEMBER 2023 DESIGNED BY: K.C. DURIGON 750 N.Greenfleid Pkwy.Garner.NC 27529 PREPARED BY: K.C. DURIGON REVIEWED BY: D.C. SARKAR

Kevin Durigon 09/21/2023

# abricatio

## NOTES:

- 1. THIS DRAWING PROVIDES BASIC DETAILS FOR CCTV POLES. PROJECT REQUIREMENTS MAY REQUIRE SPECIAL FACTORY PREPS THAT ARE NOT SHOWN ON THESE DETAILS.
- 2. DETAILS FOR INTERNAL CAMERA LOWERING SYSTEMS ARE NOT SHOWN.
- 3. POLE MOUNTED CABINETS MAY REQUIRE MODIFICATIONS TO THE LOWER HANDHOLE OPENING TO MOUNT CABINETS. 4" X 8" REINFORCED HANDHOLES ARE ACCEPTABLE OPTIONS, AND MAY BE PREFERRED.
- 4. OPENING IN POLE BASE SHALL BE EQUAL TO POLE BASE INSIDE DIAMETER MINUS  $3\frac{1}{2}$ " BUT SHALL NOT BE LESS THAN  $8\frac{1}{2}$ ".
- 5. USE COMPACT SECTION CRITERIA D/T RATIO PER AASHTO LTS-LRFD 1ST EDITION SECTION 5.7.2.



**FULL-PENETRATION** GROOVE WELD DETAIL 750 N.Greenfield Pkwy.Garner.NC 27529

NONE

Typical Fabrication Details For CCTV Poles

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SIGNATURE 09/21/2023

CCTV CAMERA POLE

(NOT TO SCALE)