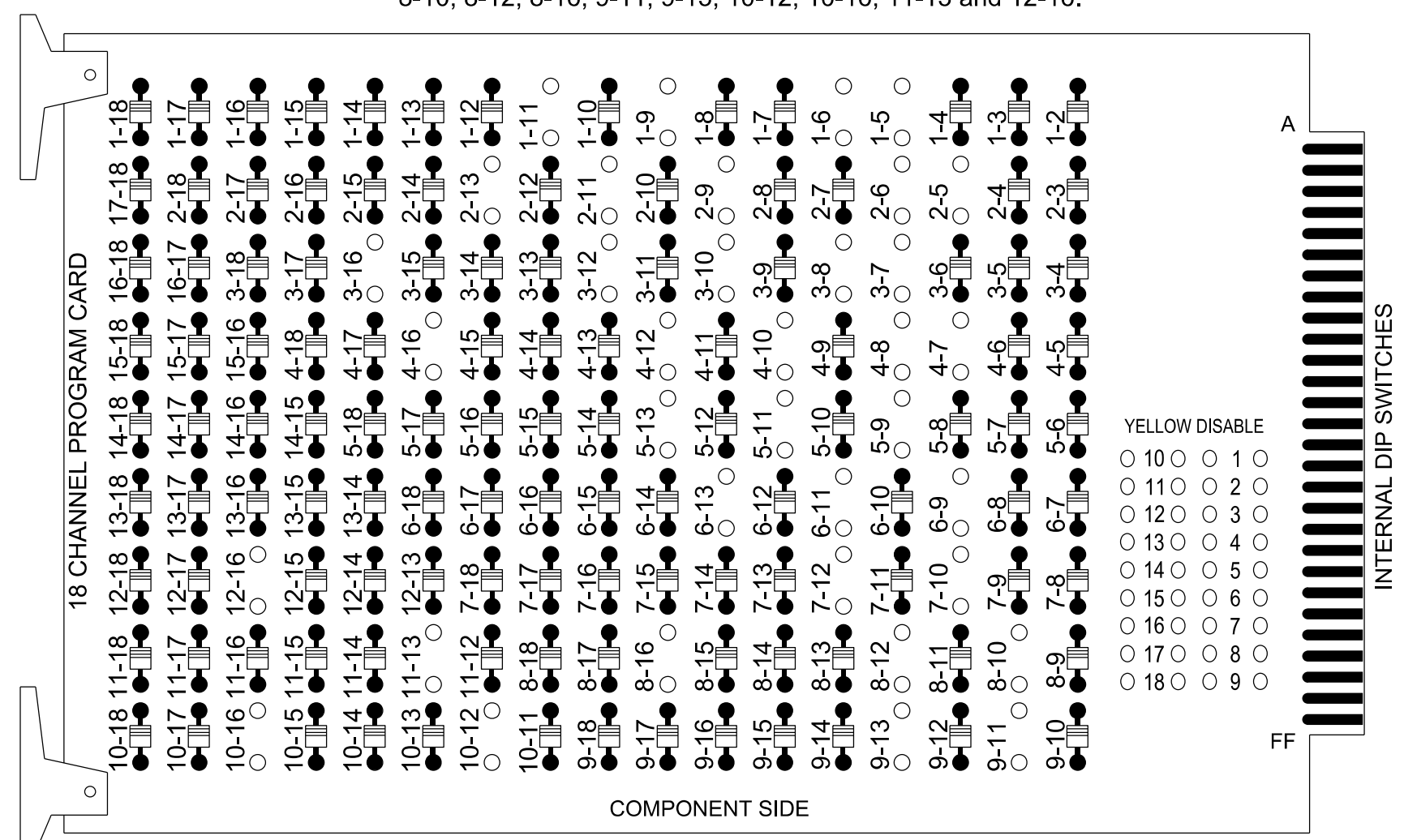


18 CHANNEL IP CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

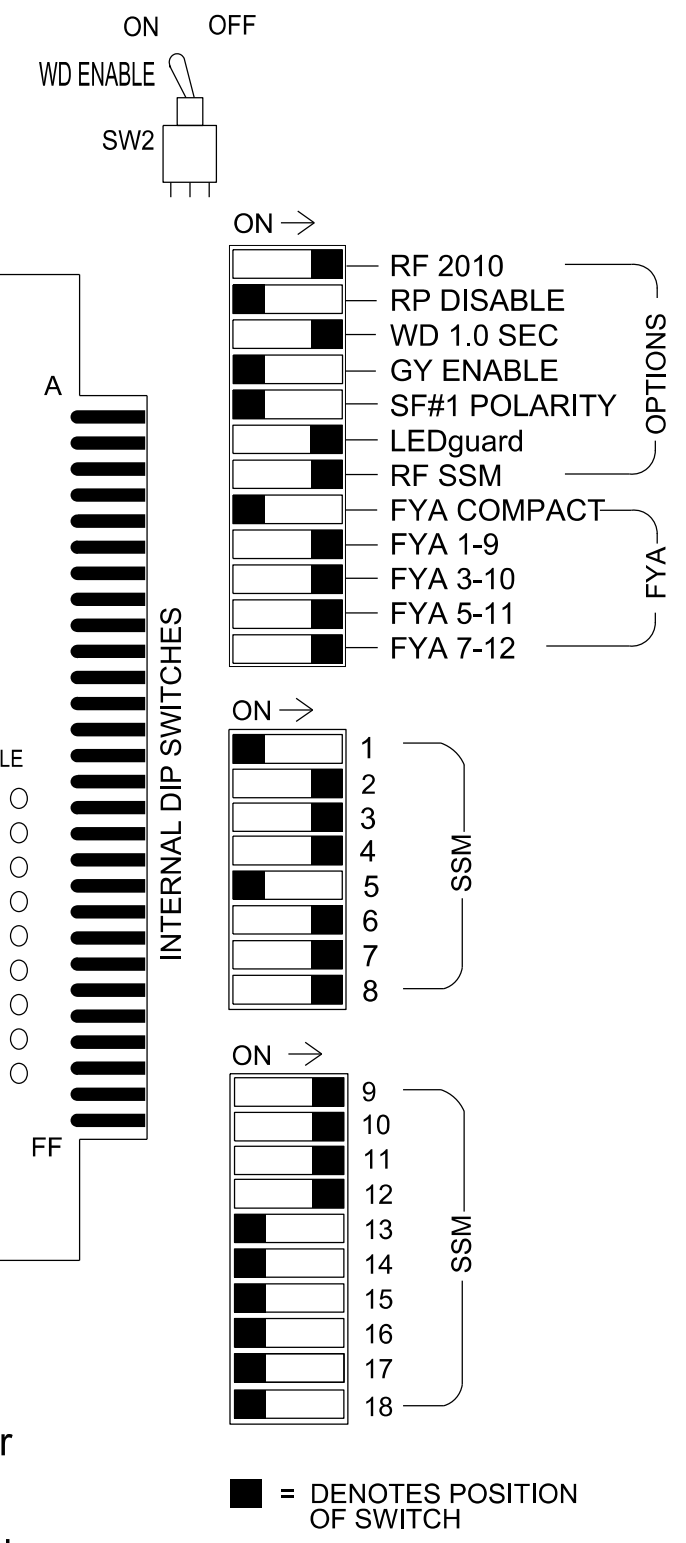
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 2-13, 3-7, 3-8, 3-10, 3-12, 3-16, 4-7, 4-8, 4-10, 4-12, 4-16, 5-9, 5-11, 5-13, 6-9, 6-11, 6-13, 7-10, 7-12, 8-10, 8-12, 8-16, 9-11, 9-13, 10-12, 10-16, 11-13 and 12-16.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that the Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.



NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Program phases 4 and 8 for Dual Entry.
- Program controller to start up in phase 2 Green No Walk and phase 6 Green No Walk.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
- The cabinet and controller are part of the D06-28_Hope Mills Closed Loop Signal System.

EQUIPMENT INFORMATION

Controller.....2070LX
 Cabinet.....332 w/ Aux
 Software.....Q-Free MAXTIME
 Cabinet Mount.....Base
 Output File Positions.....18 With Aux. Output File
 Load Switches Used.....S1, S2, S3, S4, S5, S7, S8, S10, S11, S12, AUX S1, AUX S2, AUX S4, AUX S5
 Phases Used.....1, 2, 2PED, 3, 4, 5, 6, 7, 8, 8PED
 Overlap "1".....*
 Overlap "2".....*
 Overlap "3".....*
 Overlap "4".....*

*See overlap programming detail on sheet 2.

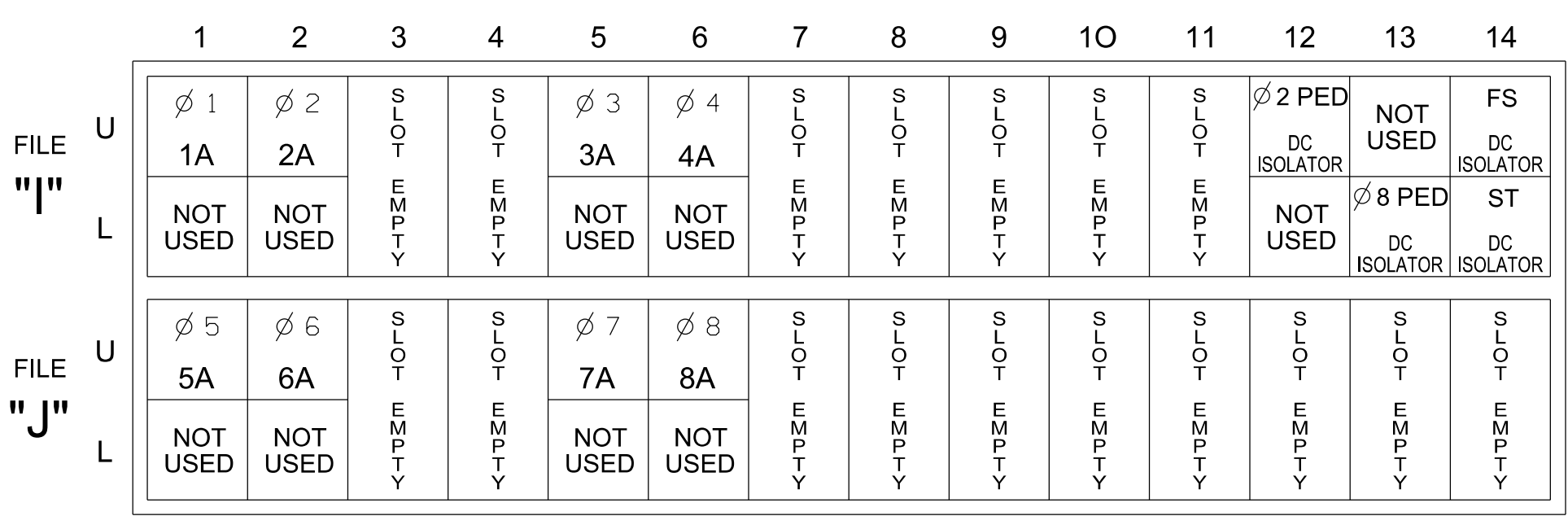
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE	
SIGNAL HEAD NO.	11	21,22	P21, P22	22	31	41,42	NU	51	61,62	NU	62	71	81,82	P81, P82	11	31	NU	51	71
RED		128		*	101			134		*	107								
YELLOW	*	129			102		*	135			108								
GREEN		130			103			136			109								
RED ARROW													A121	A124		A114	A101		
YELLOW ARROW				117						123			A122	A125		A115	A102		
FLASHING YELLOW ARROW													A123	A126		A116	A103		
GREEN ARROW	127			118	118			133		124	124								
Hand icon			113																110
Walking person icon			115																112

NU = Not Used
 * See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

(front view)

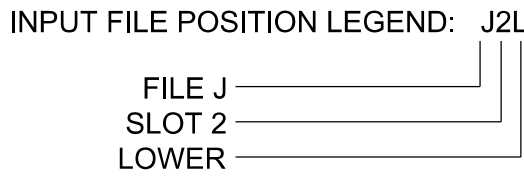


EX. : 1A, 2A, ETC. = LOOP NO.'S
 FS = FLASH SENSE
 ST = STOP TIME
 Note: See notes under the Input File Connection & Programming Chart for removal of jumpers on rear of input file.

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
1A ¹	TB2-1,2	I1U	56	18	1	1	15.0		X		X	
				-	29	6	3.0		X	X		
2A	TB2-5,6	I2U	39	1	2	2	-		X	X	X	
				20	7	3	15.0		X	X		
3A ²	TB4-5,6	I5U	58	-	30	8	3.0		X	X	X	
				4	8	4	10.0		X	X		
4A	TB4-9,10	I6U	41	3	8	4	10.0		X	X	X	
				17	15	5	15.0		X	X		
5A ³	TB3-1,2	J1U	55	-	31	2	3.0		X	X	X	
				2	16	6	-		X	X		
6A	TB3-5,6	J2U	40	19	21	7	15.0		X	X	X	
				-	32	4	3.0		X	X		
7A ⁴	TB5-5,6	J5U	57	4	22	8	10.0		X	X	X	
8A	TB5-9,10	J6U	42									
PED PUSH BUTTONS												
P21,P22	TB8-4,6	I12U	67	33	2	PED 2						
P81,P82	TB8-8,9	I13L	70	36	8	PED 8						

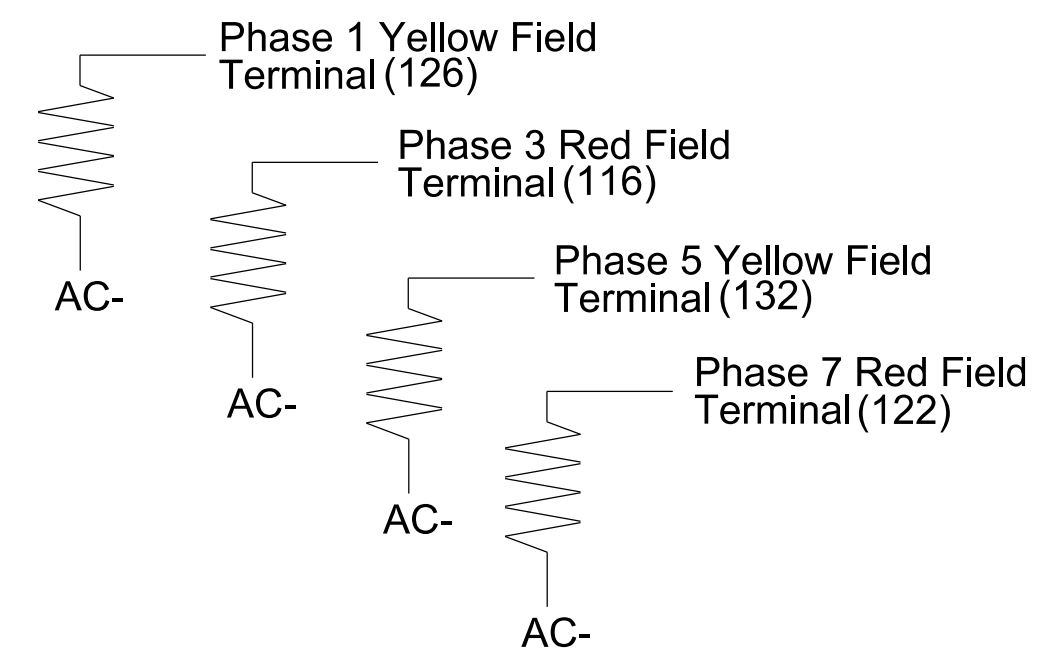
- Remove jumper from I1-W to J4-W, on rear of input file.
- Remove jumper from I5-W to J8-W, on rear of input file.
- Remove jumper from J1-W to I4-W, on rear of input file.
- Remove jumper from J5-W to I8-W, on rear of input file.



LOAD RESISTOR INSTALLATION DETAIL

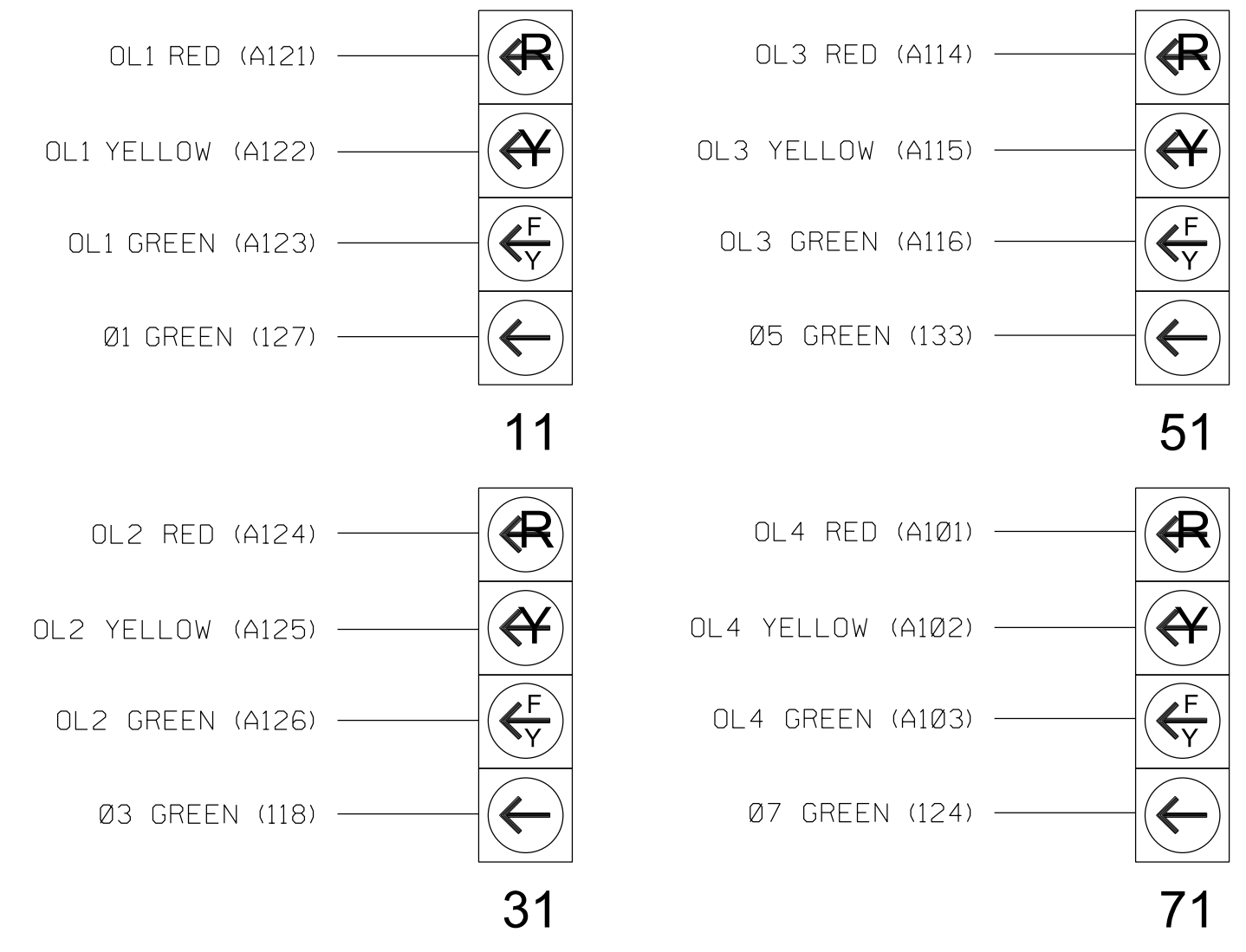
(install resistors as shown)

ACCEPTABLE VALUES	
Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1347
 DESIGNED: Aug 2024
 SEALED:
 REVISED: N/A

Electrical Detail - Sheet 1 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Prepared For: **DRMP**

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Division 6 at Waldos Beach Road/Town Center Drive
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PLAN DATE: August 2024 REVIEWED BY: LM Moon
 PREPARED BY: MR Stanley/DJW DRMP PROJ. NO: 2400555

SEAL 022516
 LISA M. MOON
 10/3/2024

SIG. INVENTORY NO. 06-1347