## MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel

Main Menu > Controller > Overlap > Overlap Parameters / Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

#### Overlap Plan 1

Overlap	1	2	3	4	5	6
Type	FYA 4 - Section	FYA 4-Section	FYA 4 - Section			
Included Phases	2	4	6	8	6,7	2,3
Modifier Phases	1	3	5	7	<del>-</del>	<u>-</u>
Modifier Overlap	-	-	-	<u>-</u>	-	-
Trail Green	0	0	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0	0.0	0.0

# MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel

Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface

Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

### Overlap Plan 2

Overlap	1	2	3	4	5	6	
Туре	FYA 4 - Section	FYA 4-Section	FYA 4 - Section	NOTICE			
Included Phases	1	4	-	1	6,7	2,3	INCLUDE
Modifier Phases	1	3	5	7	-	-	PHASE
Modifier Overlap	ä		=	i	-	-	
Trail Green	0	0	0	0	0	0	
Trail Yellow	0.0	0.0	0.0	0.0	0.0	0.0	
Trail Red	0.0	0.0	0.0	0.0	0.0	0.0	

# MAXTIME STARTUP AND SOFTWARE FLASH PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Unit

Web Interface

Home >Controller >Unit

Modify parameters as shown below and save changes.

Start Up Parameters

StartUp Clearance Hold

**Unit Flash Parameters** All Red Flash Exit Time

### **OUTPUT CHANNEL CONFIGURATION**

Front Panel

Main Menu >Controller >More>Channels>Channels Config

Web Interface

Home >Controller >Advanced IO>Channels>Channel Configuration

#### Channel Configuration

Channel	Control Type	Control Source	Flash Yellow	Flash Red	Flash Alt	MMU Channe
1	Phase Vehicle	1		Х	X	1
2	Phase Vehicle	2		Х		2
3	Phase Vehicle	3		Х	Х	3
4	Phase Vehicle	4		Х		4
5	Phase Vehicle	5		Х		5
6	Phase Vehicle	6		Х	Х	6
7	Phase Vehicle	7		Х		7
8	Phase Vehicle	8		Х	Х	8
9	Overlap	1		Х	Х	9
10	Overlap	2		Х	Х	10
11	Overlap	3		Х		11
12	Overlap	4		Х		12
13	Phase Ped	2				13
14	Phase Ped	4				14
15	Phase Ped	6				15
16	Phase Ped	8				16
17	Overlap	5		Х	Х	17
18	Overlap	6		Х		18



### MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 1A, 3A, 5A & 7A

Front Panel

Main Menu >Controller >Detector >Veh Det Plans

Web Interface

Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

Detector	Call Phase	Delay
1	1	0.0
29	0	ŀ

	Detector	Call Phase	Delay
3A	7	3	3.0
	30	0	3.0

	Detector	Call Phase	Delay
5A	15	5	0.0
	31	0	<u> </u>

Call Phase 7A 3.0 21 32 3.0 0





# MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel

Main Menu >Controller >Coordination >Patterns

Web Interface

Home >Controller >Coordination >Patterns

Pattern Parameters

Veh Det Plan | Overlap Plan

\*The Pattern number(s) are to be determined by the Division Traffic Engineer.

### FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1348 DESIGNED: Aug 2024

SEALED: REVISED: N/A

Electrical Detail - Sheet 2 of 3

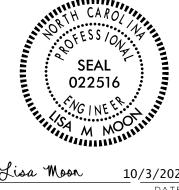
Final Design ELECTRICAL AND PROGRAMMING Prepared for:

# SR 1003 (Camden Road)

SR 1113 (Waldos Beach Road)/ Waldos Beach Road

Division 6 Cumberland County Hope Mills

PLAN DATE: August 2024 REVIEWED BY: LM Moon PREPARED BY: MR Stanley/DJW DRMP PROJ. NO: 2400555 REVISIONS INIT. DATE



SIG. INVENTORY NO. 06-1348

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

10/3/2024