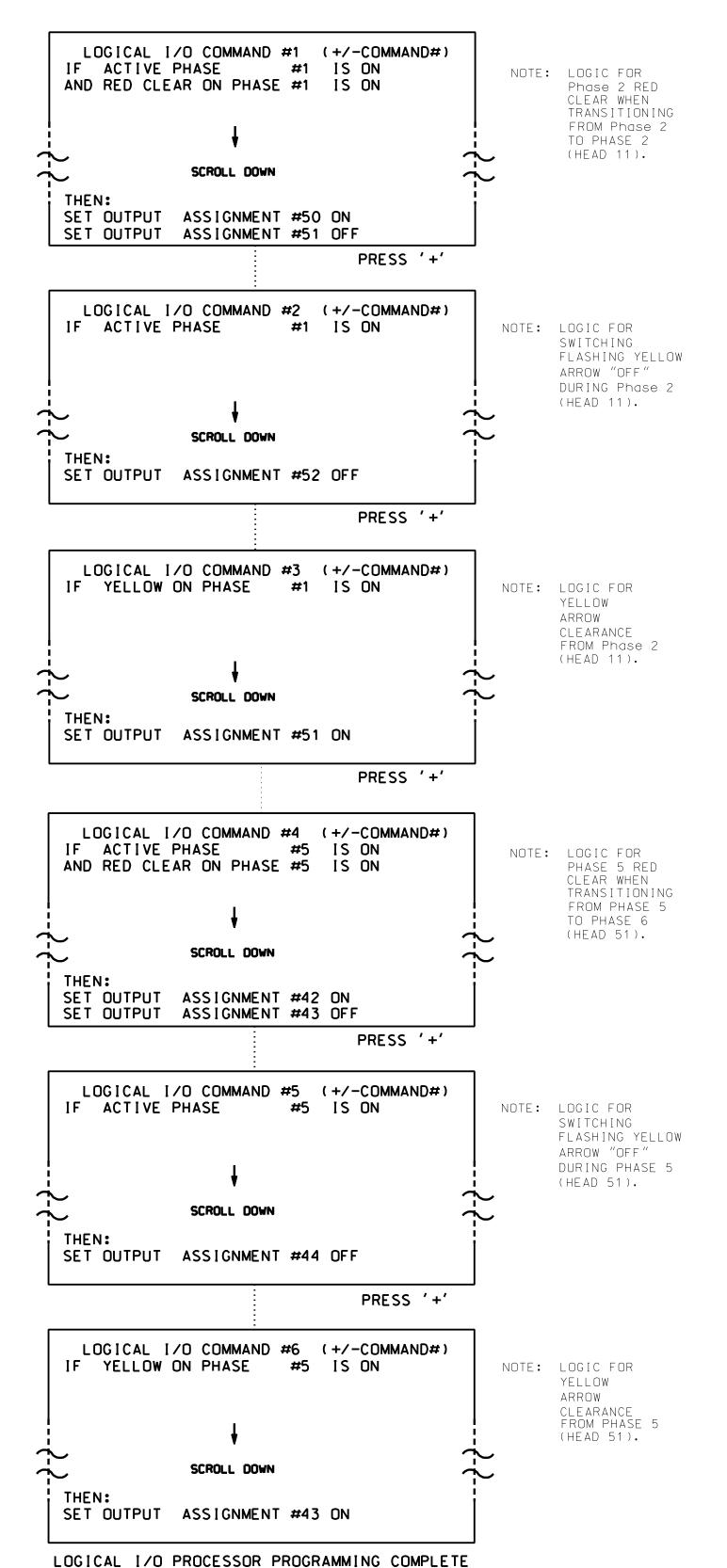
## LOGICAL I/O PROCESSOR PROGRAMMING DETAIL

# TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

### (program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, AND 6.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



#### OUTPUT REFERENCE SCHEDULE USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green

## OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

(program controller as shown below)

PROJECT REFERENCE NO. Sig 26. U-6202

FROM MAIN MENU PRESS '8' (OVERLAPS).

THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS **112345678910111213141516** VEH OVL PARENTS: XX VEH OVL NOT VEH: | VEH OVL NOT PED: | VEH OVL GRN EXT: STARTUP COLOR: \_ RED \_ YELLOW \_ GREEN FLASH COLORS: \_ RED \_ YELLOW X GREEN **←** NOTICE SELECT VEHICLE OVERLAP OPTIONS: (Y/N) GREEN FLASH YELLOW IN CONTROLLER FLASH?...Y FLASH GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (0=PARENT.3-25.5 SEC)..0.0 RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0 PRESS '+' TWICE PAGE 1: VEHICLE OVERLAP 'C' SETTINGS 12345678910111213141516 VEH OVL PARENTS: | XX VEH OVL NOT VEH: VEH OVL NOT PED:: VEH OVL GRN EXT: STARTUP COLOR: \_ RED \_ YELLOW \_ GREEN FLASH COLORS: \_ RED \_ YELLOW X GREEN | - NOTICE

PRESS '+'

SELECT VEHICLE OVERLAP OPTIONS: (Y/N)

YELLOW CLEAR (0=PARENT.3-25.5 SEC)..0.0

RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0

FLASH YELLOW IN CONTROLLER FLASH?...Y

GREEN EXTENSION (0-255 SEC).....0

OUTPUT AS PHASE # (0=NONE. 1-16)....0

PAGE 1: VEHICLE OVERLAP 'D' SETTINGS PHASE: 12345678910111213141516 VEH OVL PARENTS: VEH OVL NOT VEH: VEH OVL NOT PED: VEH OVL GRN EXT: STARTUP COLOR: \_ RED \_ YELLOW \_ GREEN FLASH COLORS: \_ RED \_ YELLOW X GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) GREEN FLASH YELLOW IN CONTROLLER FLASH?...N FLASH GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (0=PARENT.3-25.5 SEC)..0.0 RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0

OVERLAP PROGRAMMING COMPLETE

OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

PAGE 2

(program controller as shown below)

GREEN

FLASH

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2. PAGE 2: VEHICLE OVERLAP 'A' SETTINGS NOTICE PAGE 2 PHASE: 12345678910111213141516 VEH OVL PARENTS: X VEH OVL NOT VEH: : VEH OVL NOT PED: : VEH OVL GRN EXT: STARTUP COLOR: \_ RED \_ YELLOW \_ GREEN FLASH COLORS: \_ RED \_ YELLOW \_ GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...Y GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (0=PARENT.3-25.5 SEC)..0.0 RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0 PRESS '+' TWICE PAGE 2: VEHICLE OVERLAP 'C' SETTINGS 112345678910111213141516 PAGE 2 PHASE: VEH OVL PARENTS: X

VEH OVL NOT VEH: |
VEH OVL NOT PED: |
VEH OVL GRN EXT: |

THIS ELECTRICAL DETAIL IS FOR

THE SIGNAL DESIGN: 03-0847T2

STARTUP COLOR: \_ RED \_ YELLOW \_ GREEN

FLASH COLORS: \_ RED \_ YELLOW \_ GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N)

RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0

PRESS '+'

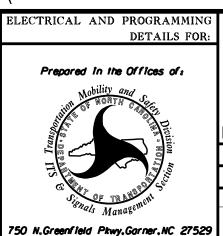
OUTPUT AS PHASE # (0=NONE, 1-16)....0

FLASH YELLOW IN CONTROLLER FLASH?...Y GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (0=PARENT.3-25.5 SEC)..0.0

PAGE 2: VEHICLE OVERLAP 'D' SETTINGS NOTICE -PHASE: 12345678910111213141516 VEH OVL PARENTS: VEH OVL NOT VEH: VEH OVL NOT PED: VEH OVL GRN EXT: : STARTUP COLOR: \_ RED \_ YELLOW \_ GREEN FLASH COLORS: \_ RED \_ YELLOW X GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...N FLASH GREEN EXTENSION (0-255 SEC).....0 YELLOW CLEAR (O=PARENT.3-25.5 SEC)..0.0 RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0 OVERLAP PROGRAMMING COMPLETE

Signal Upgrade-Electrical Detail - Sheet 2 of 5 (Construction Phase 2A)

**DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED** 



PLAN DATE: PREPARED BY:

SR 2048 (Gordon Rd) SR 2698 (Netherlands Dr/ Eaton Elementary School

Division 3 New Hanover County Wilmingtor August 2023 REVIEWED BY: N.K. Vlanich REVIEWED BY: N.R. Simmons E E. Tiller REVISIONS INIT. DATE

TH CARO SOR WESSION N 031464 COUNTER Notasha R Simmons 5/17/202 SIGNATURE DATE SIG. INVENTORY NO. 03-0847T

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