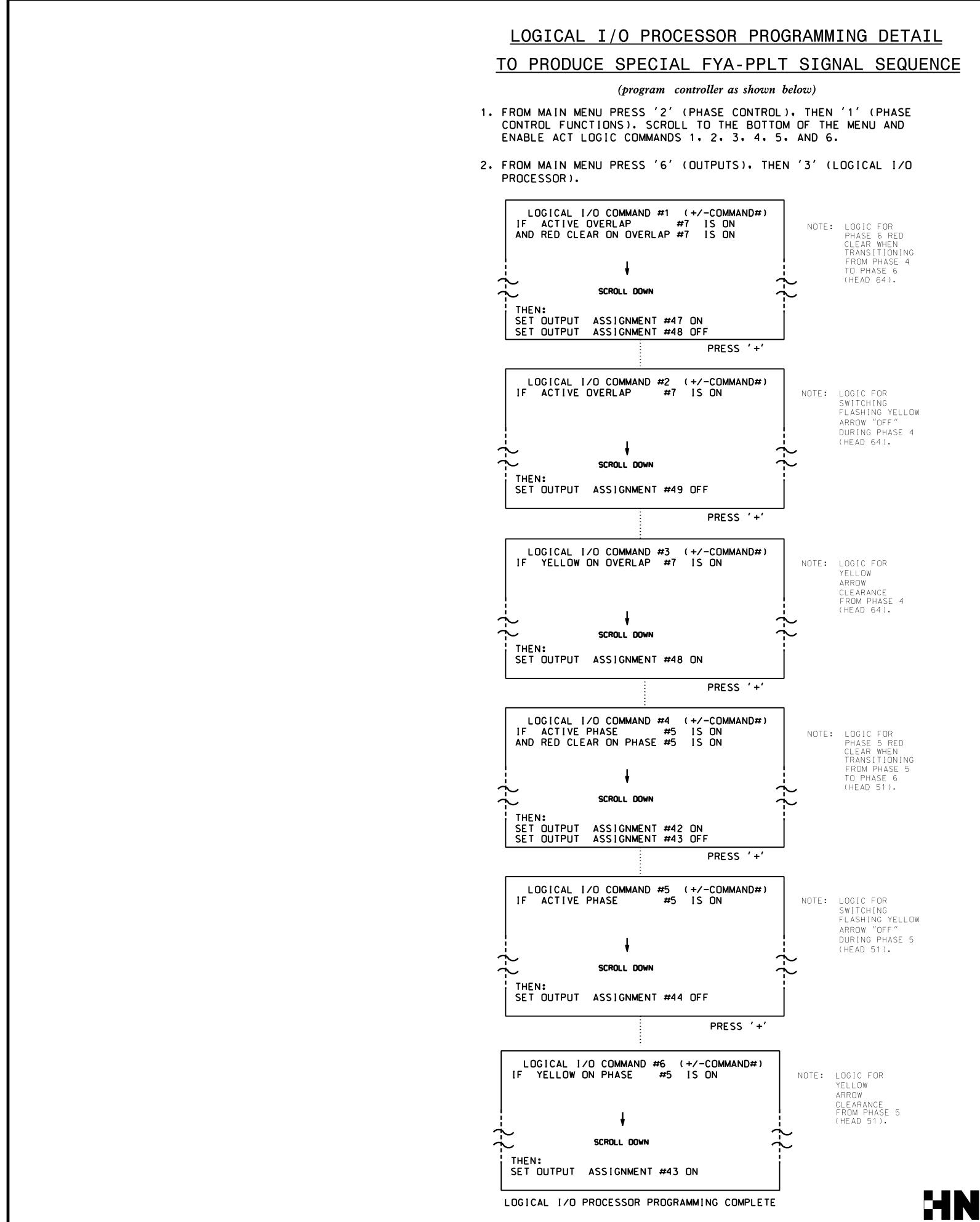
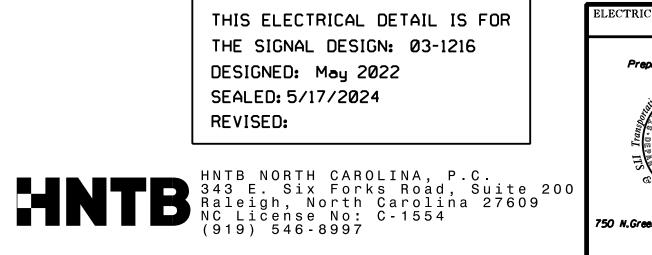
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OUTPUT R USE TO IN OUTPUT 4 OUTPUT 43 OUTPUT 44 OUTPUT 47 OUTPUT 48 OUTPUT 49



REVISED:

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	U-6202	SHEET NO. Sig. 24.2
		y. 27.2
<u>UT REFERENCE SCHEDULE</u> TO INTERPRET LOGIC PROCESSOR		
PUT 42 = Overlap C Red PUT 43 = Overlap C Yellow		
PUT 44 = Overlap C GreenPUT 47 = Overlap B RedPUT 48 = Overlap B Yellow		
UT 49 = Overlap B Green		
Signal Upgrade - Final Design		
Ilaathiaal Datail Chaat O af c DOCUMEN	IT NOT CONSIDERED L SIGNATURES COMI	
ELECTRICAL AND PROGRAMMING DETAILS FOR: SR 2048 (GOrdon Rd)	SEAL	1111111
Prepared in the Offices of: at	A TH CAN	
SR 2772 (Farrington Farms	SEAL	
PLAN DATE: August 2023 REVIEWED BY: N.K. VIa	nington O3146	4
PREPARED BY: E.E. Tiller REVIEWED BY: N.R. Sin REVISIONS INIT.	DATE DocuSigned by	ER
750 N.Greenfield Pkwy.Gorner.NC 27529	<u>Natasha R Simmo</u> SIGNATURE	
	SIC. INVENTORY NO.	03-1216