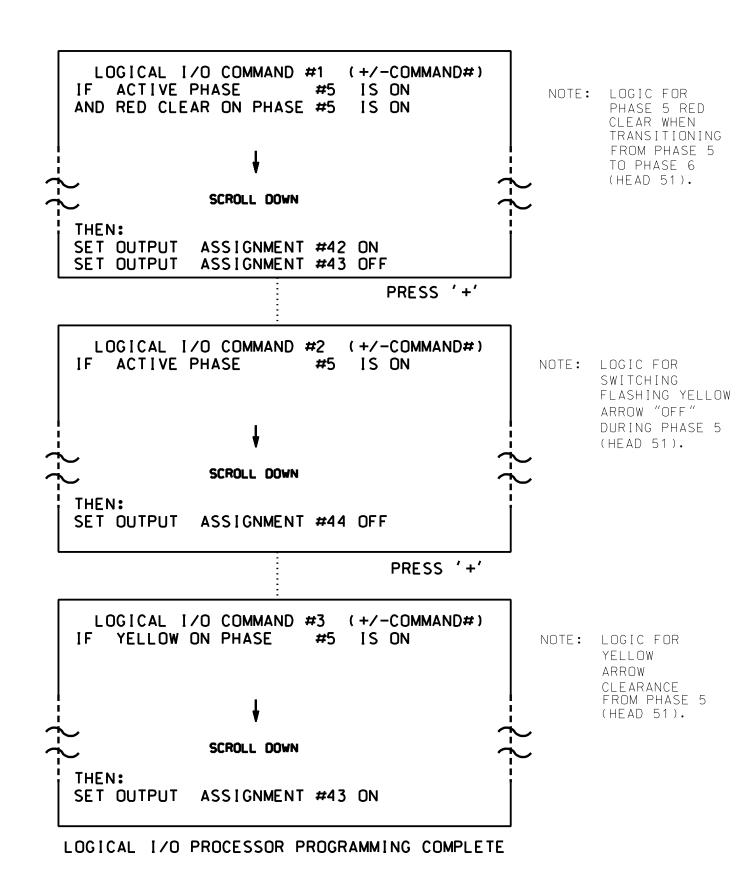
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

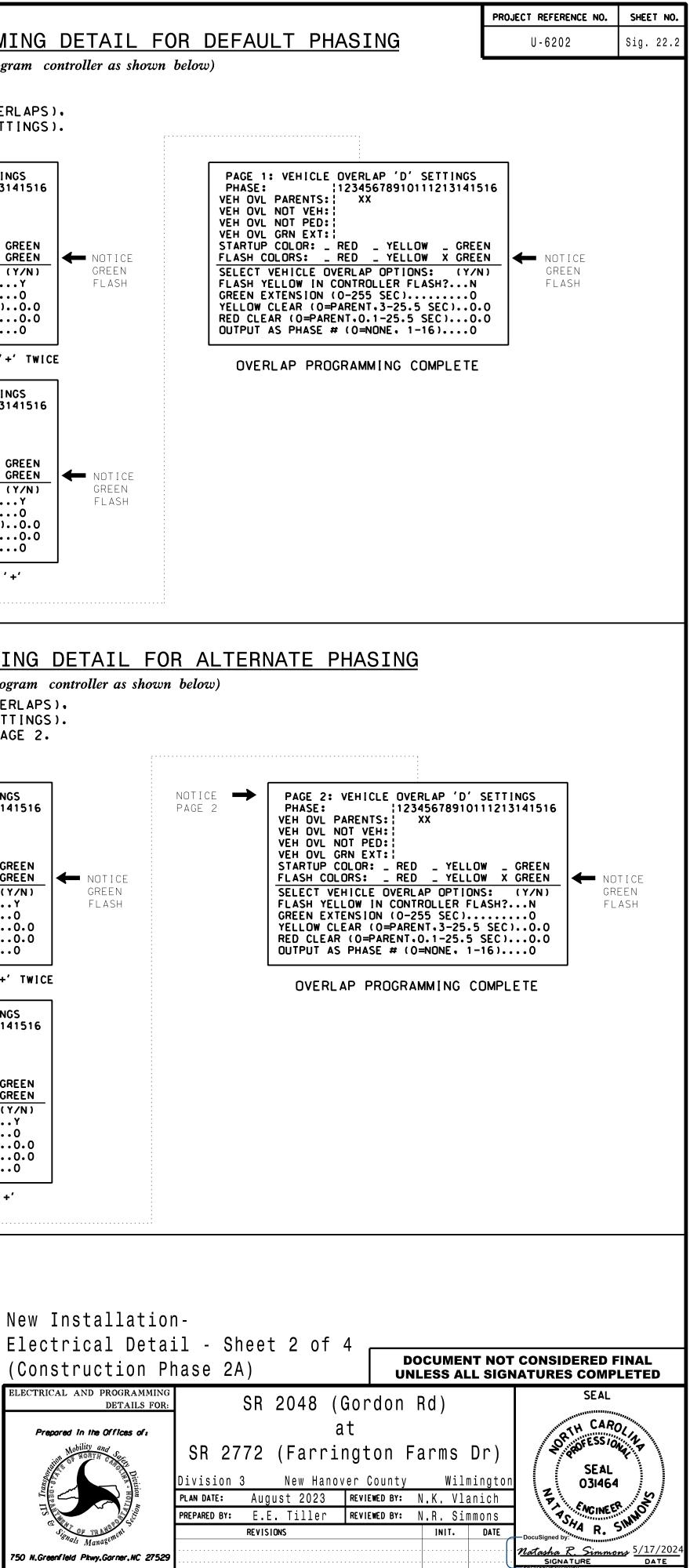
(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS). THEN '3' (LOGICAL I/O PROCESSOR).



	OVERLAP PROGRAMMING DETA
	(program controller a
	FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS).
	PAGE 2: VEHICLE OVERLAP 'A' SETTINGS PHASE: 12345678910111213141516 VEH OVL PARENTS: X VEH OVL NOT VEH: VEH OVL NOT PED: VEH OVL GRN EXT: STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW X GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?Y GREEN EXTENSION (0-255 SEC)0 YELLOW CLEAR (0=PARENT.3-25.5 SEC)0.0 RED CLEAR (0=PARENT.0.1-25.5 SEC)0.0 OUTPUT AS PHASE # (0=NONE. 1-16)0
	PRESS '+' TWICE
	PAGE 1: VEHICLE OVERLAP 'C' SETTINGS PHASE: 12345678910111213141516 VEH OVL PARENTS: XX VEH OVL NOT VEH: VEH OVL NOT PED: VEH OVL GRN EXT: STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW X GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?Y GREEN EXTENSION (0-255 SEC)0 YELLOW CLEAR (0=PARENT.3-25.5 SEC)0.0 RED CLEAR (0=PARENT.0.1-25.5 SEC)0.0 OUTPUT AS PHASE # (0=NONE, 1-16)0
	PRESS '+'
	OVERLAP PROGRAMMING DETAI
	(program controller of FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2.
OUTPUT REFERENCE SCHEDULE USE TO INTERPRET LOGIC PROCESSOR OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow	NOTICE PAGE 2: VEHICLE OVERLAP 'A' SETTINGS PHASE: 12345678910111213141516 VEH OVL PARENTS: X VEH OVL NOT VEH:: VEH OVL NOT PED:: VEH OVL GRN EXT:: STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW X GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?Y
OUTPUT 44 = Overlap C Green	GREEN EXTENSION (0-255 SEC)0 YELLOW CLEAR (0=PARENT.3-25.5 SEC)0.0 RED CLEAR (0=PARENT.0.1-25.5 SEC)0.0 OUTPUT AS PHASE # (0=NONE. 1-16)0
	PRESS '+' TWICE
	NOTICE PAGE 2: VEHICLE OVERLAP 'C' SETTINGS PHASE: 112345678910111213141516 VEH OVL PARENTS: X VEH OVL NOT VEH: VEH OVL NOT PED: VEH OVL GRN EXT: STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW _ GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?Y GREEN EXTENSION (0-255 SEC)0 YELLOW CLEAR (0=PARENT.3-25.5 SEC)0 OUTPUT AS PHASE # (0=NONE. 1-16)0
	PRESS '+'

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-1216T1 DESIGNED: May 2022 SEALED: 5/17/2024 REVISED: HNTB NORTH CAROLINA, P.C. 343 E. Six Forks Road, Suite 200 Raleigh, North Carolina 27609 NC License No: C-1554 (919) 546-8997



SIG. INVENTORY NO. 03-1216T